

ĐẠI HỌC QUỐC GIA THÀNH PHỐ HỒ CHÍ MINH
TRƯỜNG ĐẠI HỌC BÁCH KHOA

Sub-system: **BTL_HW_V1.0.0.PnjPcb**

Project: **BTL_TKHTN**

Design by: **Linh Vu**

Sheet title: **Block_Diagram**

Sheet **1** of **6**

Checked by: *

Doc No:

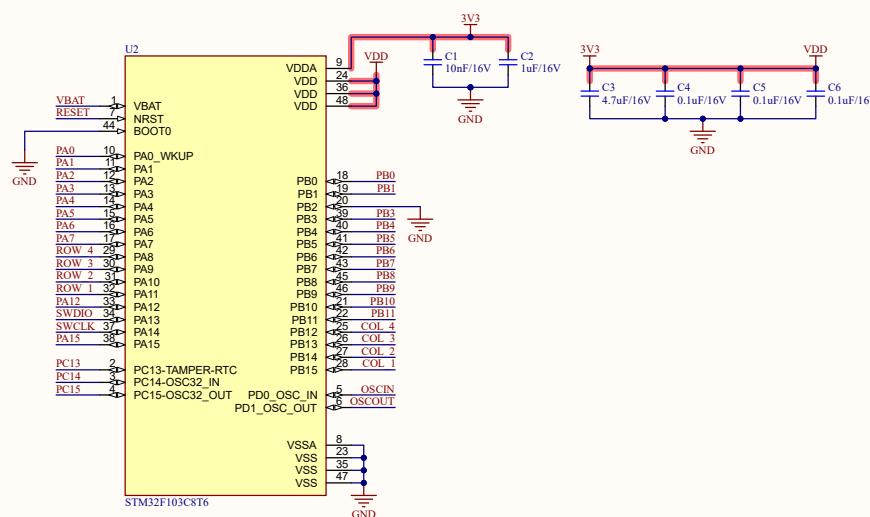
Size: **A3**

Approved by: *

Revision: *

Date: *

A

**Relay** *GPIO_NOT_IMPLEMENT*

Relay CTRL → PB1

B

RFID *GPIO_NOT_IMPLEMENT*

RFD[4B] → PA3
 RFD[3B] → PB0
 RFD[3B] → PA4
 RFD[3B] → PA5
 RFD[3B] → PA6
 RFD[3B] → PA7
 SPI1, I2C2

C

BT

BT1 → GND
 BT1_NRST → 3V3
 R3 10k → RESET
 C9 0.1uF/16V → GND

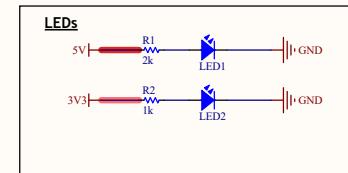
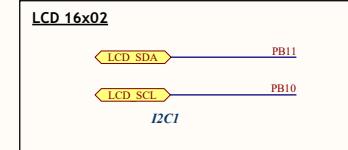
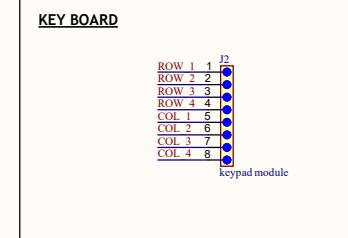
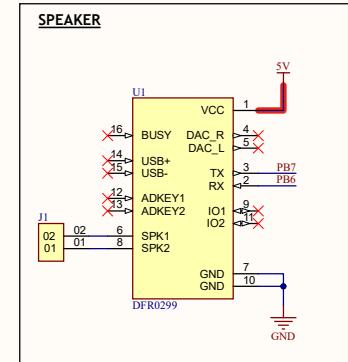
OSC_8M

GND → C7 12pF/16V → OSCIN
 C8 12pF/16V → X1
 OSCOUT → GND

SWD

3V3 → J3
 SWDIO → 01
 SWCLK → 02
 03 → 01
 04 → 02
 GND → 03
 04 → 04

D



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Sub-system: **BTL_HW_V1.0.0.PrbPcb**Project: **BTL TKHTN**Design by: **Hung Vo**Sheet title: **MCU**Sheet **2** of **6**

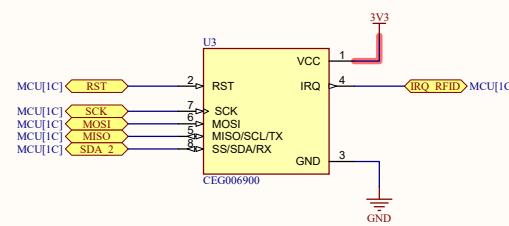
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Sub-system: BTL_HW_V1.0.0.PnjPcb

Project: BTL TKHTN

Design by: Hung Vo

Sheet title: RFID

Sheet 3 of 6

Checked by: *

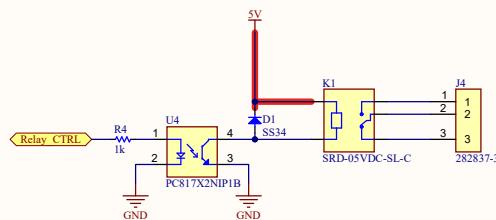
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Size: A3

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Revision: *

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<p>ĐẠI HỌC QUỐC GIA THÀNH PHỐ HỒ CHÍ MINH TRƯỜNG ĐẠI HỌC BÁCH KHOA</p>	
Sub-system: BTL_HW_V1.0.0.PnjPcb	Project: BTL TKHTN
Design by: Hung Vo	Sheet title: RELAY
Checked by: *	Doc No: 4
Approved by: *	Revision: *
	Date: *

A

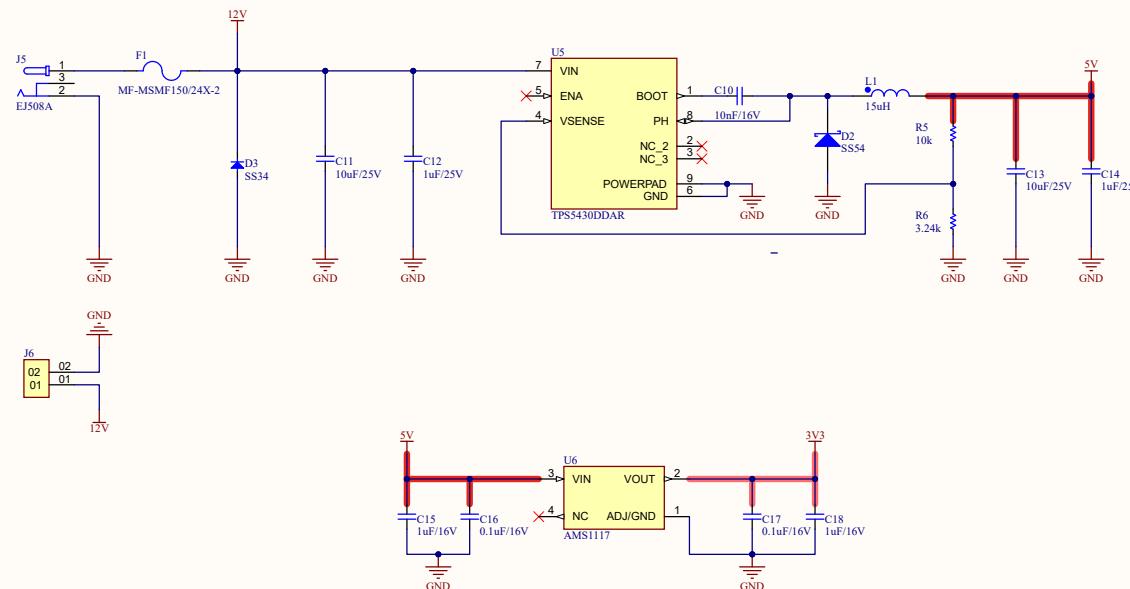
A

B

B

C

C



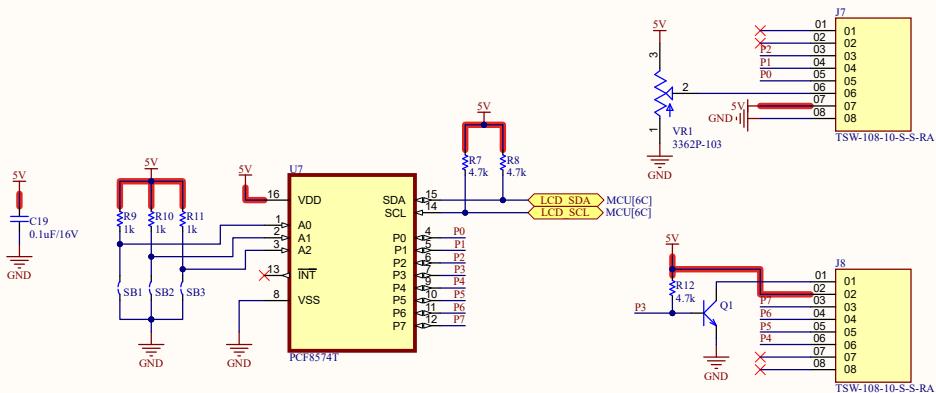
Sub-system: BTL_HW_V1.0.0.PjPcb	Project: BTL TKHTN
Design by: Hung Vo	Sheet title: POWER
Checked by: *	Doc No: 5
Approved by: *	Revision: *
	Date: *

A

A

B

B



C

C



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TRƯỜNG ĐẠI HỌC BÁCH KHOA

Sub-system: BTL_HW_V1.0.0.PnjPcb

Project: *

Design by: *

Sheet title: *

Sheet 6 of 6

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Doc No: 6

Size: A3

Approved by: *

Revision: *

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