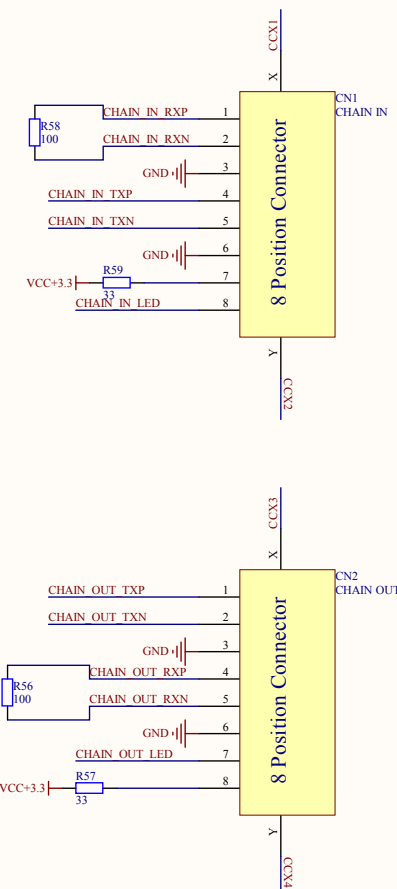
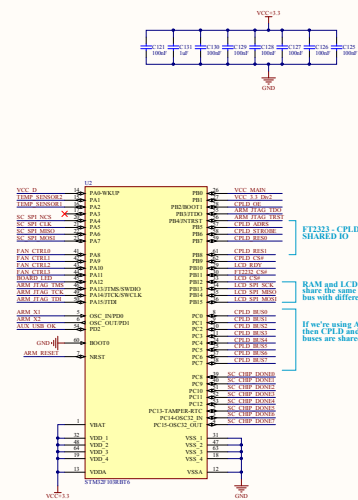
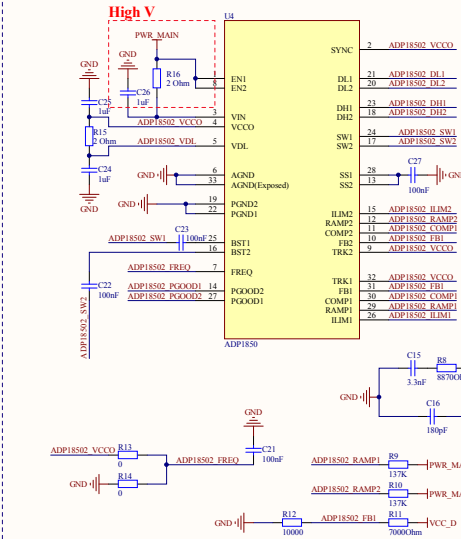
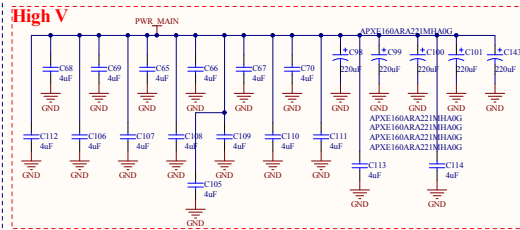


CHAIN Connections

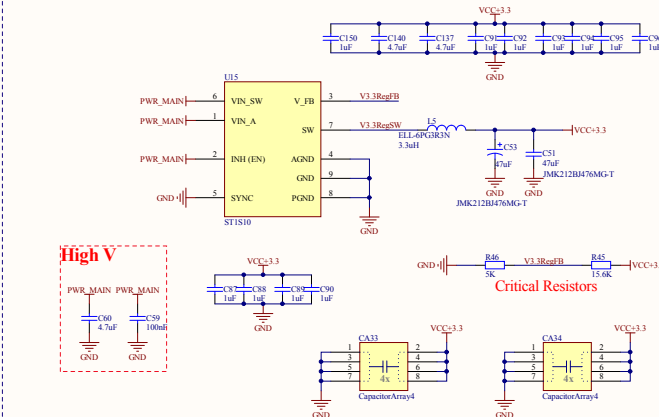
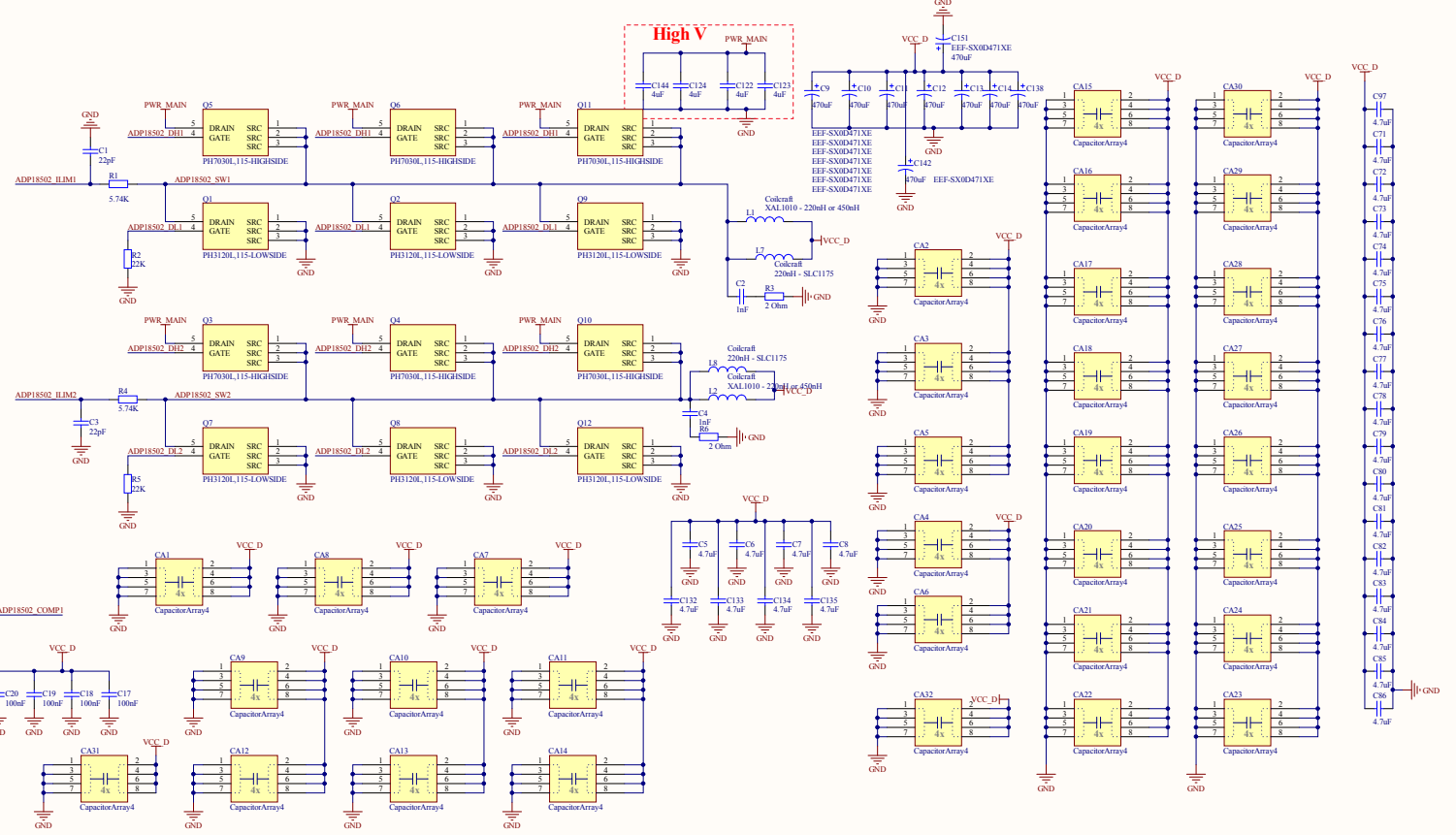


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Date: 16/06/2013	Sheet of	
File: C:\Users\...InterBoardCOMM.SchDoc	Drawn By:	Nasser Ghoseiri

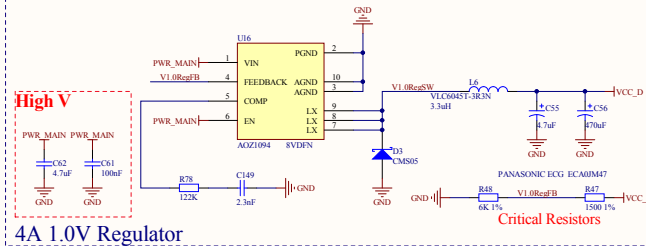




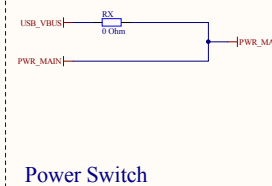
75A-80A 1.0V Regulator



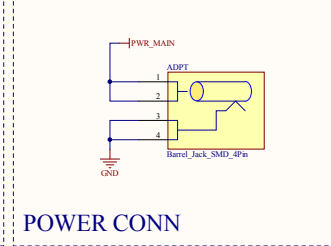
4A 3.3V Regulator



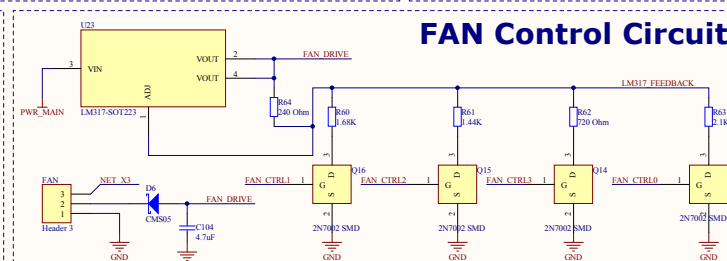
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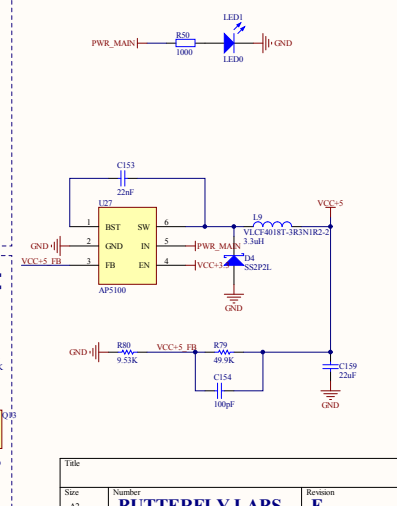
Power Switch



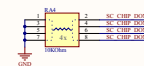
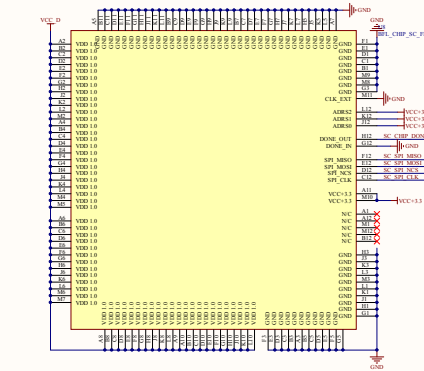
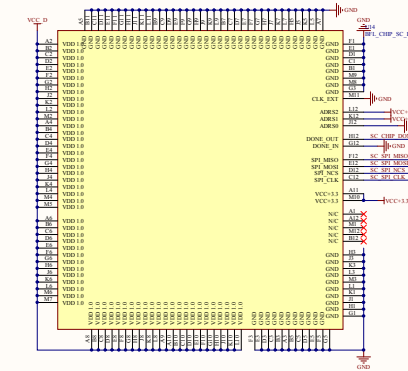
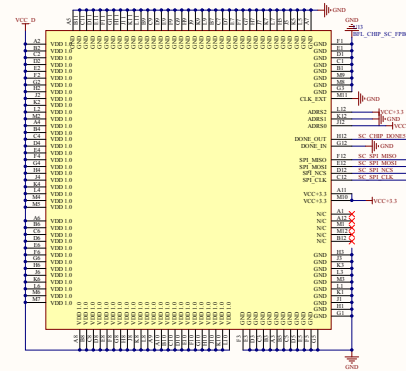
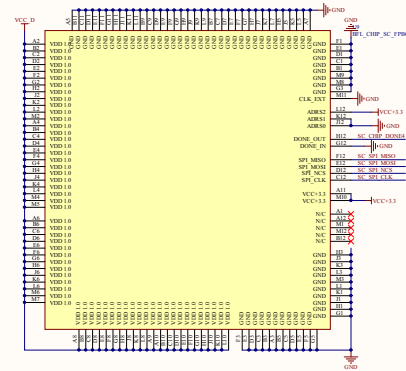
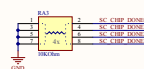
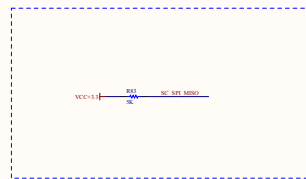
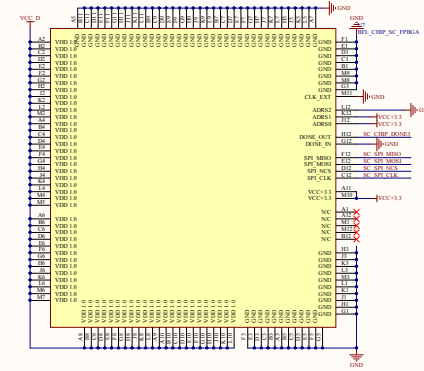
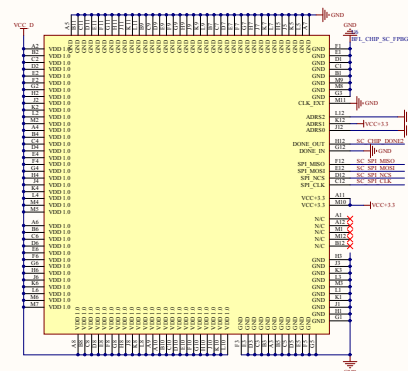
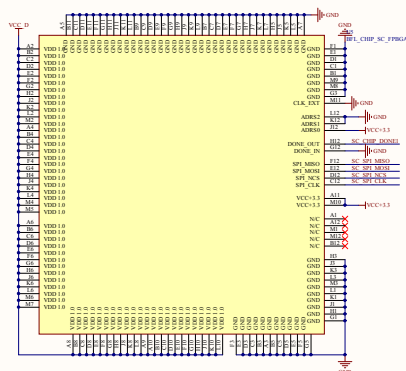
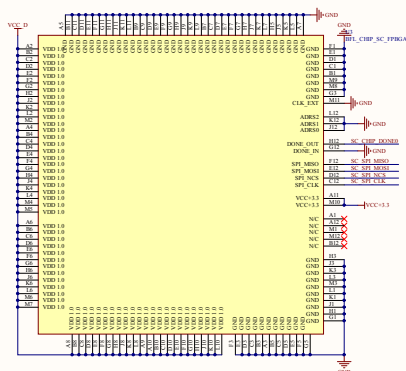
POWER CONN

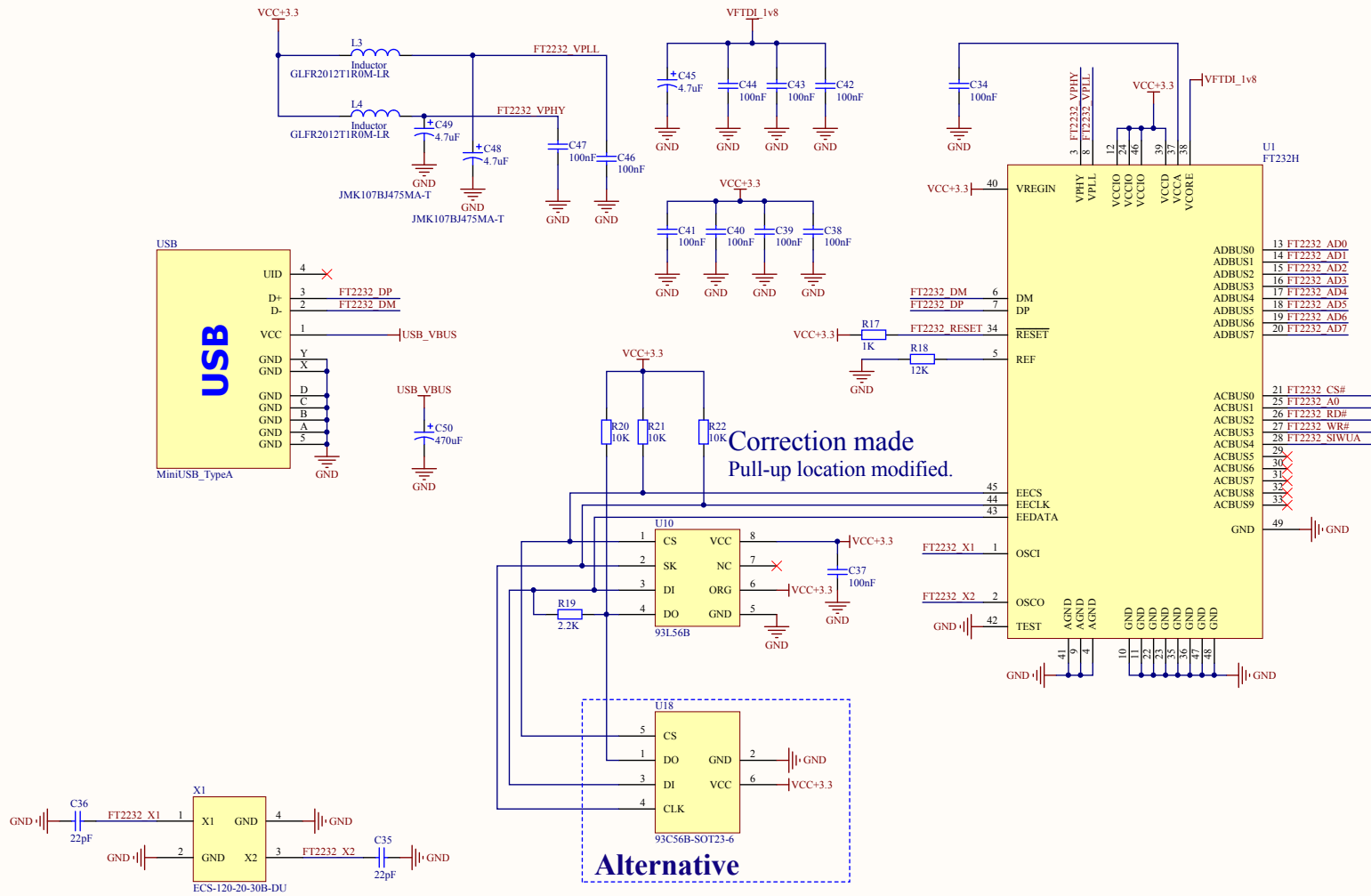


FAN Control Circuit



Title			
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Date	16/06/2013	Sheet of	1
File	C:\Users\Nasser\Documents\PowerRegulation\Doc\Doc	Drawn By	Nasser Choucri

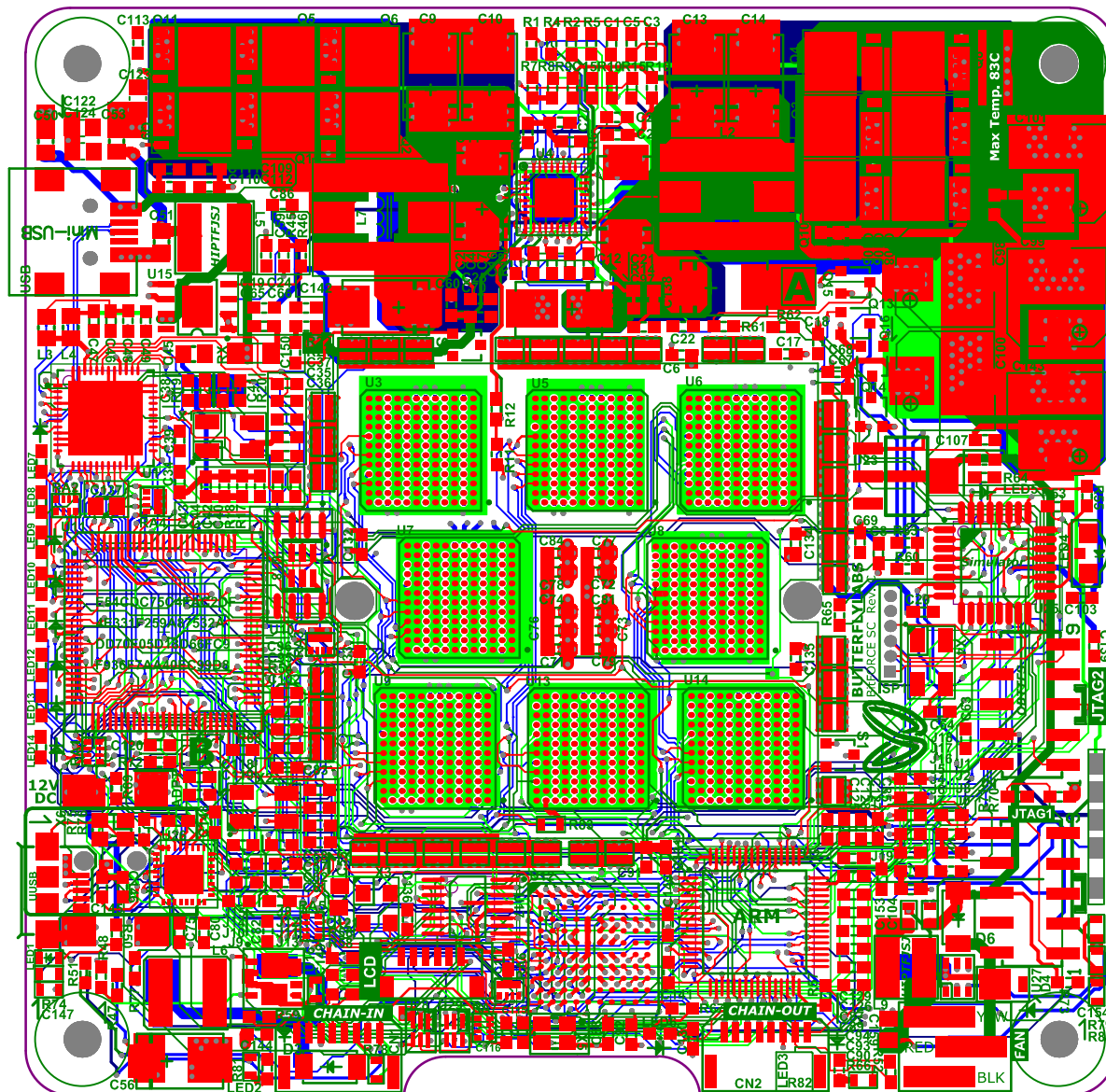




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Size	Number	Revision	
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Date:	16/06/2013	Sheet of	
File:	C:\Users\...USB Module.SchDoc	Drawn By:	Nasser Ghoseiri

Butterfly Labs BitFORCE SC 1.0

Revision E



By Nasser GHOSEIRI - Jan 2013

Revision B3

Added RA7, RA6, U28, U29, U27, L9, C159, C153, D4, C154, R79, R80, C151
Added R81, R82
Removed U24
C104 changed package from 1206 to 0603
R47 and R48 values in the BOM changed
ISP1 Header part number corrected to: GRPB061VWVN-RC
L6 changed to VLC5045T-3R3N

Revision B4

R80 changed from 16K to 9.53K
R48 Pin 1 was NOT connected to ground

Revision C

Corrected R70
Corrected TempSensors nets
Changed engines from normal QFN to BGA144
Changed values of R60 to R63
D2 was removed
L6 Was changed
R83 was added

Revision C.4

BGA Pad sizes changed from 0.45mm to 0.5mm
BitFORCE SC Chips pinout corrected