

SPEAR 3: Orbit Stability and Corrector Noise Specs

1. Orbit stability specs:

$$\Delta y < 0.1 \sigma_y \quad (= 3 \text{ } \mu\text{m rms at IDs}) \quad [1]$$

$$\Delta y' < 0.1 \sigma_{y'} \quad (= 1.5 \text{ } \mu\text{rad rms for 100-per undulator})$$

- **< 5%** (or less) preferable (= 1.5 μm rms at IDs)
- over period T:

$$\text{data integration time } (\mu\text{secs}) < T < \text{hours } (\sim 24 \text{ h}) \quad [2]$$

2. Orbit noise power spectral density:

$$\langle \Delta y^2 \rangle = \int \text{PSD } df \quad [3]$$

$$\Delta y (\text{peak}) = 3\text{-}5 \times \Delta y_{\text{rms}}$$

3. Sources of orbit instability:

- thermally induced mechanical motion (dominant)

few μm vert. for 1°C girder temp change ($\Rightarrow \sim 30 \mu\text{m}$ orbit)

beam-related chamber motion (small)

- mechanical vibration (sub-micron)
- stray fields
- power supplies

drift (gain + offset) ($\pm \sim 3^\circ\text{C}$ diurnal ambient temp)

noise spectrum:

regulation level and bandwidth
ripple
DAC quantization noise

Want contribution from each of these sources to be \ll than total noise budget, if possible.

4. Orbit stabilization:

- Minimize sources of instability:

- tunnel temp stability ($\sim 1^\circ\text{C}$)

- water-cooled chamber

- decouple/damp vibration sources

- shield stray fields

- use low-noise, low drift power supplies

- limit contribution to $\sim 10\%$ of noise budget
($< 1\%$ beam dimensions)

estimate $\sim 10 - 50\ \mu\text{m}$ vertical orbit stability without feedback
(dominated by diurnal temp)

- Orbit Feedback:

- use 54 correctors per plane to stabilize beam at 90+ BPMs

- BPMs stable to $\sim 3\ \mu\text{m}$ vertically over 24 h

- 100 Hz BW (3 dB); 2-4 kHz cycle freq

- Orbit resolution (averaged over fdbk cycle) $\sim 1\ \mu\text{m}$

goal: stabilize beam at BPMs to $\sim 3\ \mu\text{m}$ vert. over 24 h,
dominated by BPM motion

NOTE: Feedback adds noise in bandwidth \geq cycle freq

5. Corrector-orbit response:

Orbit disturbance caused by DC kick θ_i from corrector i:

$$\Delta y(s) = \theta_i \frac{\sqrt{\beta_i \beta_s}}{2 \sin \pi \nu} \cos(|\varphi_i - \varphi_s| - \pi \nu) \quad [4]$$

Assume $\beta_i = 8$ m, $\beta_s = 5$ m, $\nu = 5.23$:

$$\begin{aligned} \Delta y_i(s, \text{max}) &= 4.8 \theta_i \quad (\text{peak around ring}) \\ \Delta y_i(s, \text{rms}) &= 3.4 \theta_i \quad (\text{rms around ring}) \end{aligned} \quad [5]$$

6. Noise from many correctors:

For **uncorrelated** ensemble of N correctors ($N = 54$):

$$\Delta y_{\text{tot}}(s, \text{rms}) = 3.4 \sqrt{N} \theta_i (\text{rms}) = 25 \theta_i (\text{rms}) \quad [6]$$

Limit noise contribution from correctors to **~10%** of total noise budget ----> limit $\Delta y_{\text{tot}}(s, \text{rms})$ from correctors to **<1%** of vert beam size ($0.3 \mu\text{m rms}$) with **feedback on**:

$$\Rightarrow \theta_i (\text{rms noise}) < 0.3 \mu\text{m} / 25 = 0.012 \mu\text{rad rms} \quad [7]$$

$$\theta_i (\text{noise}) / \theta_{\text{Vcorr}} (\text{FS}) = .012/1500 = 8 \times 10^{-6} = 2^{-16.9} \quad [8]$$

\Rightarrow want **17 bit** rms beam noise from corrector kick **with feedback on**
Includes: - high freq filtering from vac chamber + magnets
- low freq noise attenuation by feedback

Feedback off: low-freq noise from 54 correctors ~10%-50% of vertical beam size \Rightarrow **14-11 bit** low freq stability

7. Orbit measurement resolution:

IF ADC ENOB:	12.2 bits
IF samples/turn:	50 ($\sqrt{50} = 2.8$ bits)
IF ADC ENOB/turn	15 bits/turn
ADC FS:	± 14 mm
Digital res/turn:	$0.85 \mu\text{m/turn}$
RF-IF analog res/turn:	$\sim 1 \mu\text{m/turn @ } 500 \text{ mA}$
Total res/turn:	$1.3 \mu\text{m/turn @ } 500 \text{ mA}$

Averages/feedback cycle: 160 @ 2 kHz cycle
80 @ 4 kHz cycle

$$\Rightarrow \text{orbit resolution} = 1.3 \mu\text{m} / \sqrt{\text{avgs}}$$

$$= \begin{array}{l} \mathbf{0.1 \mu\text{m/cycle @ } 2 \text{ kHz}} \\ \mathbf{0.15 \mu\text{m/cycle @ } 4 \text{ kHz}} \end{array} \quad [9]$$

(realistic - $0.5\text{--}1 \mu\text{m/cycle?}$)

NOTE: can get higher resolution orbit with more averages

Would like to **match corrector kick resolution to orbit measurement resolution**. From Eq 5, single corrector kick to produce $0.1 \mu\text{m}$:

$$\theta_{\min} = 0.1 \mu\text{m} / 4.8 \text{ m} = 0.021 \mu\text{rad}$$

$$= 1500 \mu\text{rad} / 2^{16.1} \Rightarrow \mathbf{16 \text{ bit}} \text{ or better kick resolution} \quad [10]$$

8. Corrector digital quantization error:

$$\theta_{\text{quant}}(\text{rms}) = \theta_{\text{min}} / \sqrt{12} \quad [11]$$

Effect on orbit from 1 corrector (Eq. 5):

$$\Delta y(s, \text{rms}) = 3.4 \theta_{\text{quant}}(\text{rms}) = 1.0 \theta_{\text{min}} \quad [13]$$

Effect on orbit from $N = 54$ correctors:

$$\Delta y(s) \text{ rms} = \sqrt{54} \theta_{\text{min}} = 7.3 \theta_{\text{min}} \quad [14]$$

Limit $\Delta y(s) \text{ rms} < 0.3 \mu\text{m rms}$ from quantization noise:

$$\theta_{\text{min}} < 0.04 \mu\text{rad rms} \Rightarrow \mathbf{15.2 \text{ bit}} \quad [15]$$

If want to limit peak disturbance to $< 0.3 \mu\text{m}$, or if want quantization noise to be fraction of total noise: **17-18 bit**

NOTE: Quantization noise spread over bandwidth given by DAC update rate

\Rightarrow higher DAC rate = lower PSD

\Rightarrow high freq quantization noise filtered by corr + vac chamber

\Rightarrow may want fast DAC update + dither, even when no feedback
---this may give higher effective DAC res as well

9. Integrated noise from DAC + power supply (DRAFT!):

$$0.012 \mu m = \left(\int PSD df \right)^{1/2} = \left(\int_0^{0.1} PSD df + \int_{0.1}^1 PSD df + \int_1^{10} PSD df + \int_{10}^{1k} PSD df + \int_{1k}^{10k} PSD df \right)^{1/2}$$

Let θ_{noise} = equivalent deflection noise at output of power supply

θ_{beam} = actual noise reaching beam after filtering by magnet
+ vac chamber and attenuation by feedback

Bandwidth	Attenuation	θ_{noise} ($\mu\text{rad rms}$)	θ_{beam} ($\mu\text{rad rms}$)
DC-0.01 Hz	fdbk ~ 1000	1.5 (10 bit)	0.002
0.01-0.1 Hz	fdbk ~ 100	0.4 (12 bit)	0.004
0.1-1 Hz	fdbk ~ 10	0.05 (15 bit)	0.005
1 – 10Hz	fdbk ~ 2	0.01 (17 bit)	0.005
10 – 1 kHz	none	0.005 (18 bit)	0.005
1 kHz -10 kHz	~ 10	0.03 (15-16 bit)	0.003
10 kHz - >100 kHz	~ 100	0.4 (12 bit)	0.004
100 kHz - ∞	~ 1000	1.5 (10 bit)	0.002

Total noise: with fdbk: 0.011 $\mu\text{rad rms}$
without fdbk: 1.6 $\mu\text{rad rms}$ (130% vert beam size)

NOTE: Integrated noise without feedback can be reduced to 10% of vertical beam size (0.3 $\mu\text{m rms}$) if low freq performance is improved to ~ 13 bits, dominated by power supply, not DAC

\Rightarrow 14-bit low freq DAC stability is desirable.

10. DAC considerations:

DAC should provide small fraction of DAC+supply noise budget if possible.

Oversampling low-bit DAC + reconstruction filter (e.g x 8, etc) can reduce quantization noise.

Oversampling + dither can get higher resolution, even for DC setting.

DAC differential non-linearity: < 1 LSB over temp range; monotonicity required for feedback

Absolute accuracy, integral non-linearity, THD not crucial. Stability is more important.

Low-freq DAC drift/offset and INL can be corrected with feedback + good ADC.

DAC convert time/delay/settling: $< \sim 100 \mu\text{s}$ for 4 kHz feedback.

A "sign magnitude" DAC has better noise properties around 0 than an "offset binary" DAC.

Serial DAC control desirable to minimize interface connections.

11. Conclusions:

- If beam noise from 54 vertical correctors limited to 1% of 30 μm vertical beam size, single corrector noise $\sim 0.012 \mu\text{rad rms}$ (17 bit; 1500 $\mu\text{rad FS}$).

Includes: - noise from DAC and power supply
- filtering from supply, magnet, chamber
- feedback attenuation of low frequency

- DAC specs (**DRAFT!**):

resolution for orbit change:	16 bit
monotonicity:	16 bit or better (?)
DNL:	< 1 bit
INL:	$< \sim 0.1\%$
drift+offset (24 h):	14 bit
total noise:	17 bit or better
speed:	> 10 kHz

NOTES: Feedback around DAC with good ADC may permit using DACs with high INL, drift, offset (audio DAC).

High update rate + filtering may give 18-bit quantization noise with 16-bit DAC

High update rate + filter + dither may give 18 bit resolution with 16-bit DAC

- For more accurate DAC specs, need:
 - Corrector power supply + crate performance measurements
 - Feedback model
 - Better understanding of DAC and ADC issues, specs