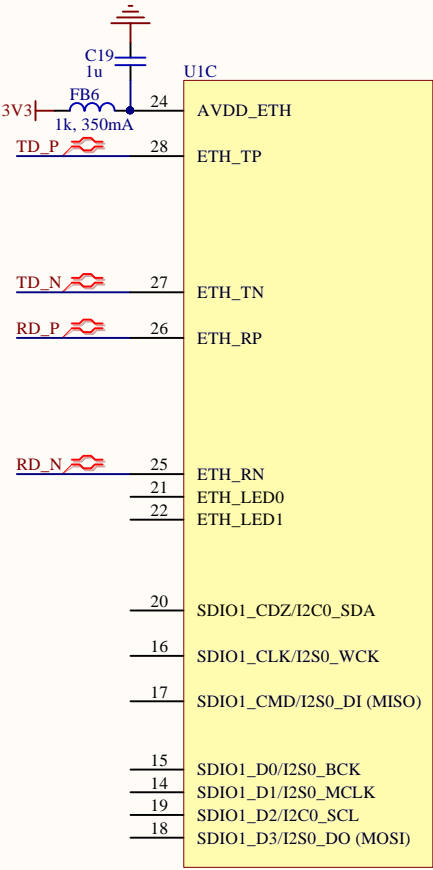


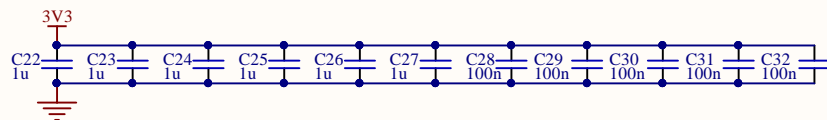
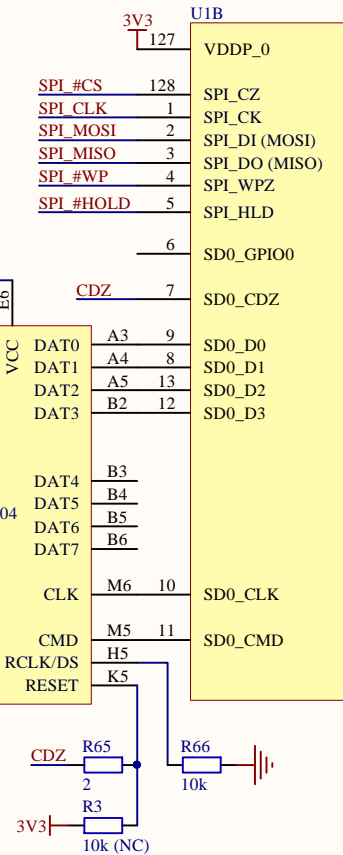
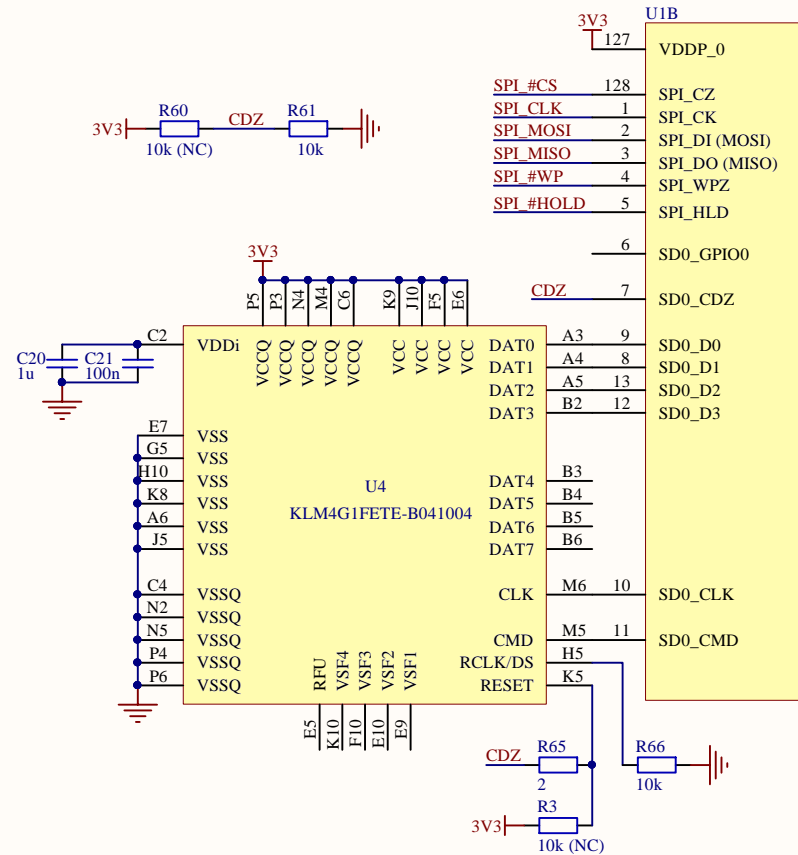
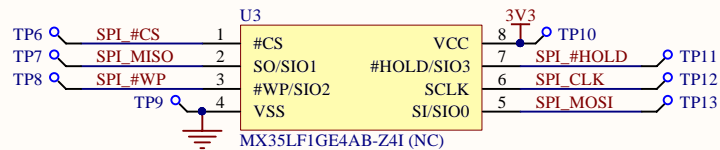


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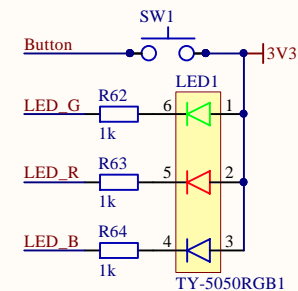
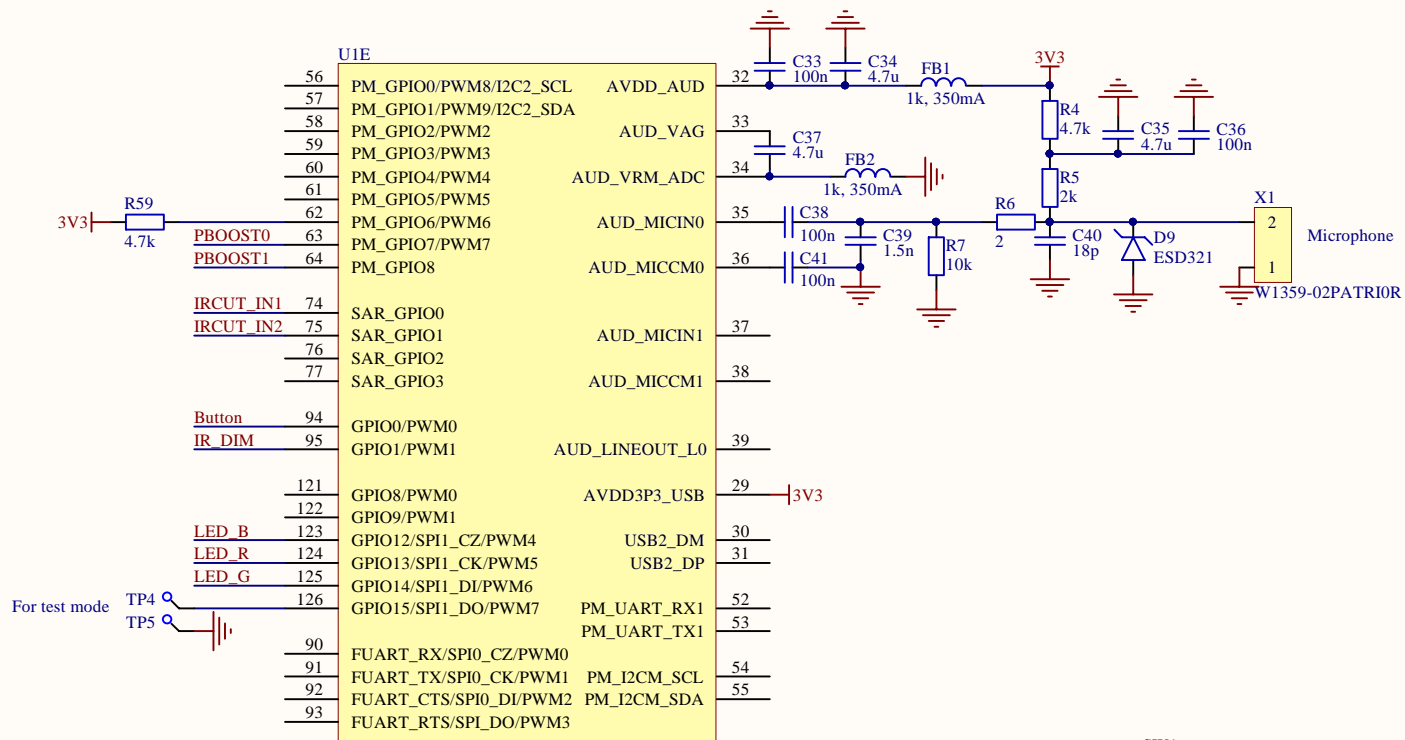
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# FLASH



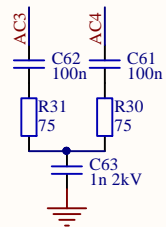
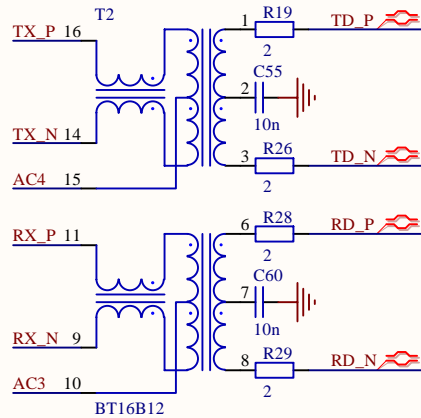
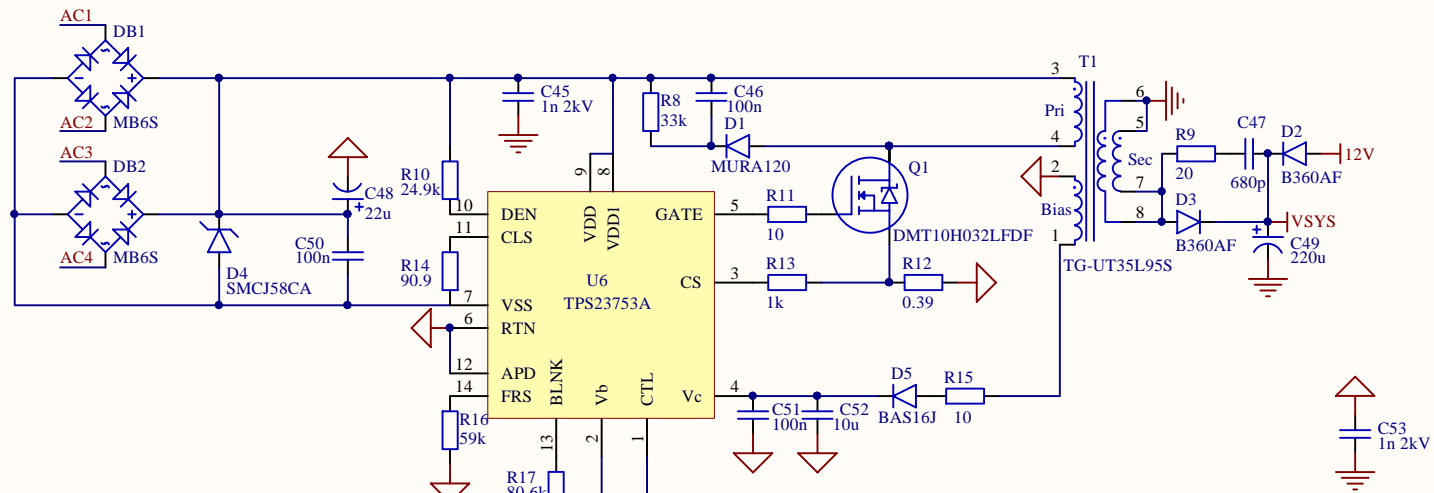
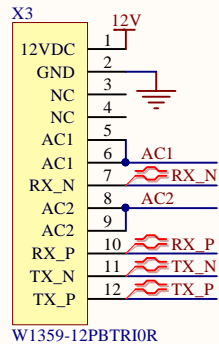
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# MISC



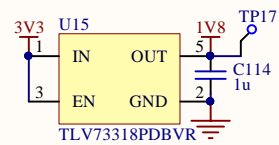
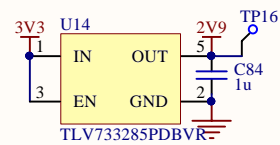
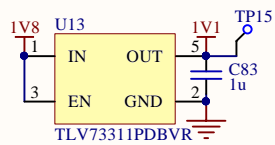
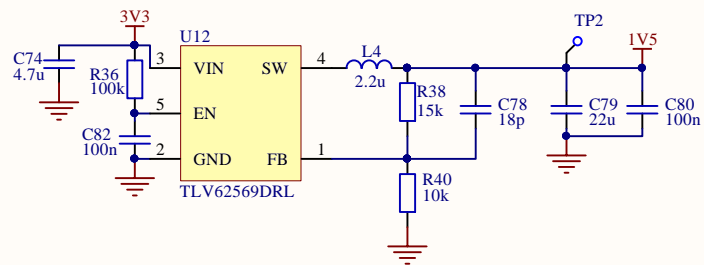
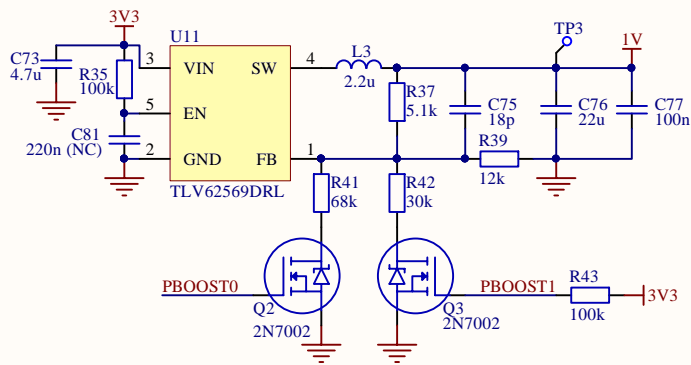
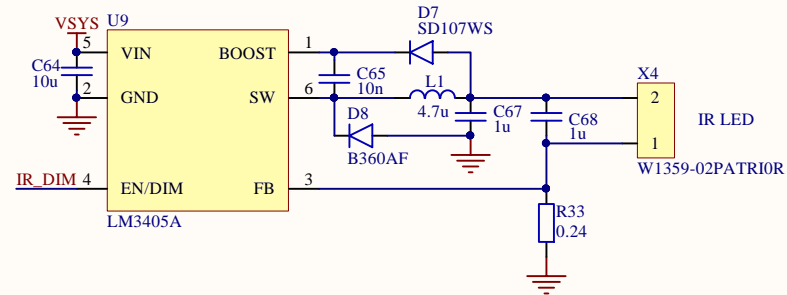
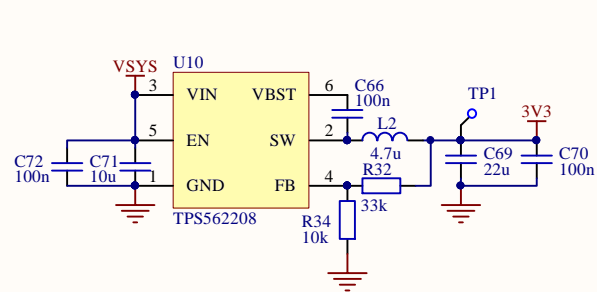
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# PoE



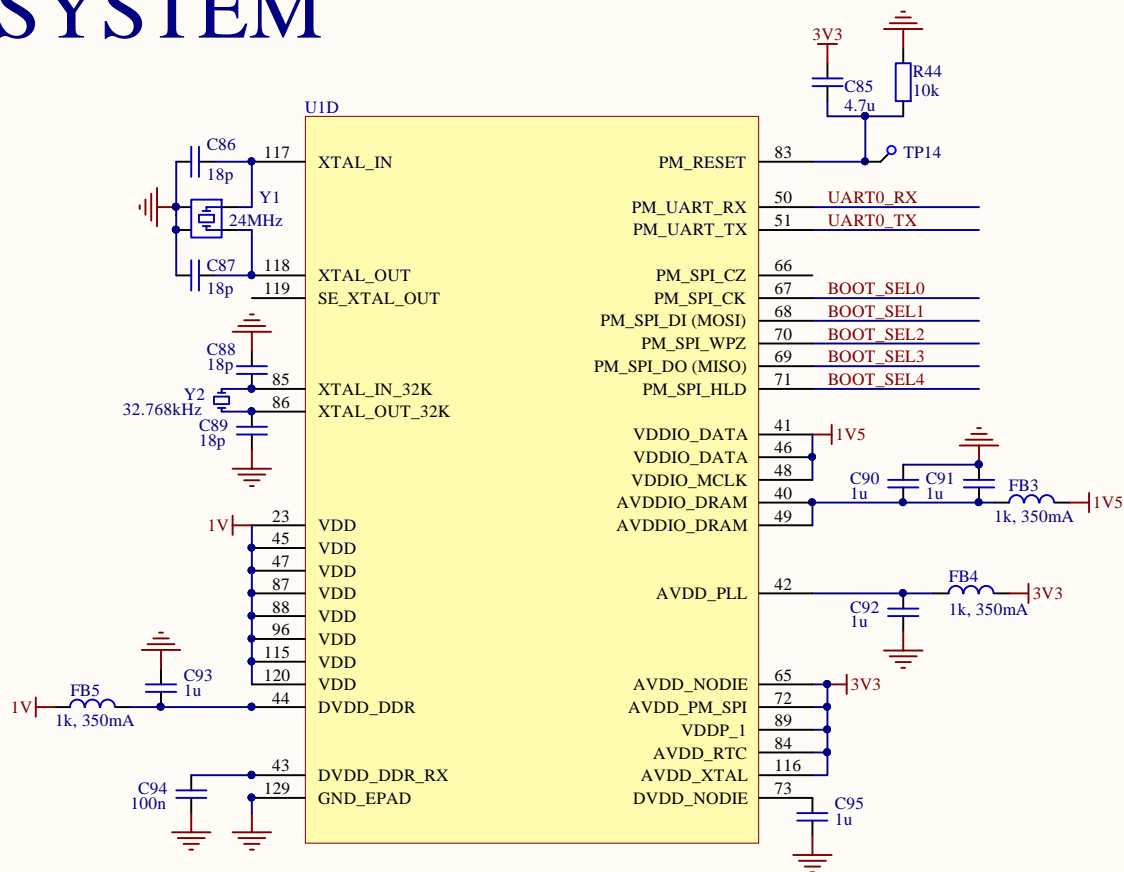
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# POWER

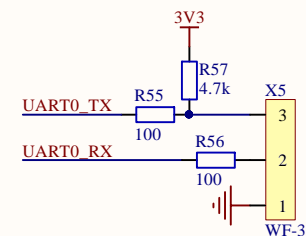
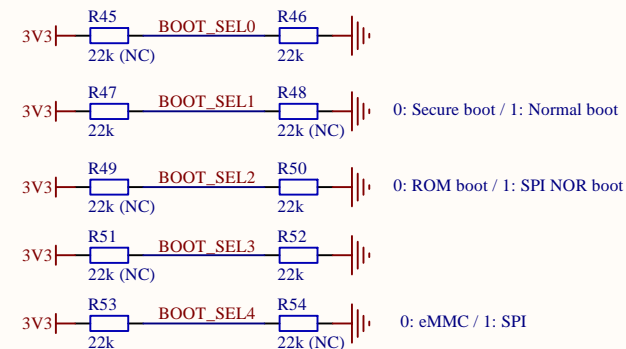


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# SYSTEM



## BOOT FROM SPI NAND



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