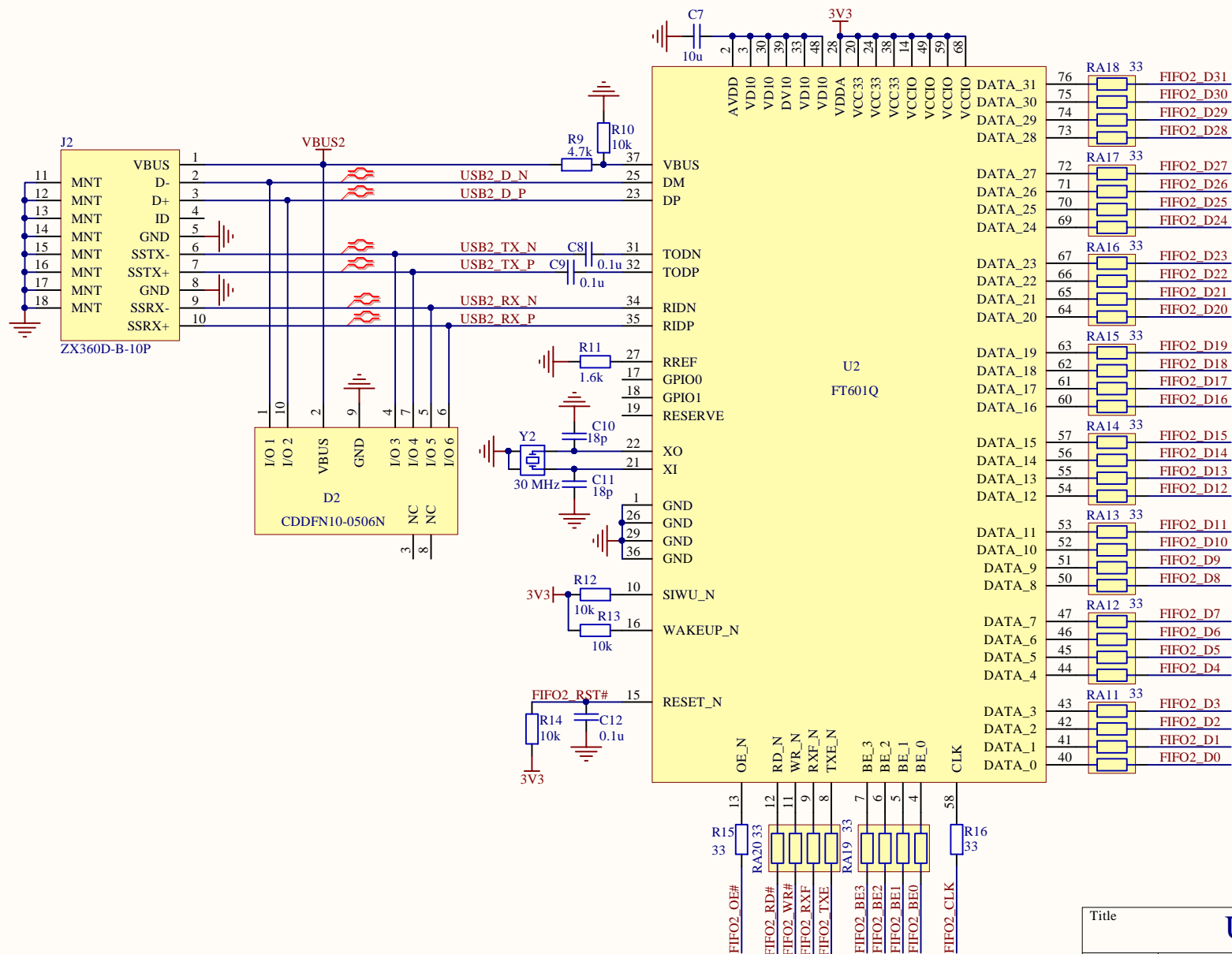


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Size	Number	Revision
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Title		
USB 3.0 channel 2		
Size	Number	Revision
A4		
Date:	10.11.2018	Sheet of
File:	Z:\Desktop\...\USB2.SchDoc	Drawn By:

U3C

BANK 13

IO_0_13

IO_L1P_T0_13	N16	K25	FIFO1_D30
IO_L1N_T0_13	K26	FIFO1_D15	
IO_L2P_T0_13	R26	FIFO1_D19	
IO_L2N_T0_13	P26	FIFO1_D18	
IO_L3P_T0_DQS_13	M25	FIFO1_D12	
IO_L3N_T0_DQS_13	L25	FIFO1_D13	
IO_L4P_T0_13	P24	FIFO1_D21	
IO_L4N_T0_13	N24	FIFO1_D7	
IO_L5P_T0_13	N26	FIFO1_D17	
IO_L5N_T0_13	M26	FIFO1_D16	
IO_L6P_T0_13	R25	FIFO1_D27	
IO_L6N_T0_VREF_13	P25	FIFO1_D20	
IO_L7P_T1_13	N19	FIFO1_D2	
IO_L7N_T1_13	M20	FIFO1_D5	
IO_L8P_T1_13	M24	FIFO1_D11	
IO_L8N_T1_13	L24	FIFO1_D14	
IO_L9P_T1_DQS_13	P19	FIFO1_D25	
IO_L9N_T1_DQS_13	P20	FIFO1_D24	
IO_L10P_T1_13	M21	FIFO1_D8	
IO_L10N_T1_13	M22	FIFO1_D9	
IO_L11P_T1_SRCC_13	P23	FIFO1_D22	
IO_L11N_T1_SRCC_13	N23	FIFO1_D6	
IO_L12P_T1_MRCC_13	N21	FIFO1_D3	
IO_L12N_T1_MRCC_13	N22	FIFO1_D4	
IO_L13P_T2_MRCC_13	R21	FIFO1_CLK	
IO_L13N_T2_MRCC_13	P21	FIFO1_D23	
IO_L14P_T2_SRCC_13	R22	FIFO1_D28	
IO_L14N_T2_SRCC_13	R23	FIFO1_D26	
IO_L15P_T2_DQS_13	T24	FIFO1_BE1	
IO_L15N_T2_DQS_13	T25	FIFO1_BE0	
IO_L16P_T2_13	T20	FIFO1_WR#	
IO_L16N_T2_13	R20	FIFO1_D29	
IO_L17P_T2_13	T22	FIFO1_RXF	
IO_L17N_T2_13	T23	FIFO1_TXE	
IO_L18P_T2_13	U19	FIFO1_BE3	
IO_L18N_T2_13	U20	FIFO1_BE2	
IO_L19P_T3_13	T18	FIFO1_OE#	
IO_L19N_T3_VREF_13	T19	FIFO1_RD#	
IO_L20P_T3_13	P16	FIFO1_D10	
IO_L20N_T3_13	N17	FIFO1_D0	
IO_L21P_T3_DQS_13	R16	FIFO1_RST#	
IO_L21N_T3_DQS_13	R17	FIFO1_D31	
IO_L22P_T3_13	N18	FIFO1_D1	
IO_L22N_T3_13	M19		
IO_L23P_T3_13	U17		
IO_L23N_T3_13	T17		
IO_L24P_T3_13	R18		
IO_L24N_T3_13	P18		
IO_25_13	U16		

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U3E

BANK 15

IO_0_15

IO_L1P_T0_AD0P_15	K15	C16	FIFO2_D13
IO_L1N_T0_AD0N_15	B16	FIFO2_D12	
IO_L2P_T0_AD8P_15	A18	FIFO2_D9	
IO_L2N_T0_AD8N_15	A19	FIFO2_D10	
IO_L3P_T0_DQS_AD1P_15	B17	FIFO2_D11	
IO_L3N_T0_DQS_AD1N_15	A17	FIFO2_D8	
IO_L4P_T0_AD9P_15	C19	FIFO2_D4	
IO_L4N_T0_AD9N_15	B19	FIFO2_D6	
IO_L5P_T0_AD2P_15	C17	FIFO2_D7	
IO_L5N_T0_AD2N_15	C18	FIFO2_D5	
IO_L6P_T0_15	D15	FIFO2_D14	
IO_L6N_T0_VREF_15	D16	FIFO2_D3	
IO_L7P_T1_AD10P_15	H16	FIFO2_RST#	
IO_L7N_T1_AD10N_15	G16	FIFO2_D26	
IO_L8P_T1_AD3P_15	G15	FIFO2_D25	
IO_L8N_T1_AD3N_15	F15	FIFO2_D24	
IO_L9P_T1_DQS_AD11P_15	J15	FIFO2_BE0	
IO_L9N_T1_DQS_AD11N_15	J16	FIFO2_BE1	
IO_L10P_T1_AD4P_15	E15	FIFO2_D15	
IO_L10N_T1_AD4N_15	E16	FIFO2_D28	
IO_L11P_T1_SRCC_AD12P_15	G17	FIFO2_D20	
IO_L11N_T1_SRCC_AD12N_15	F18	FIFO2_D23	
IO_L12P_T1_MRCC_AD5P_15	F17	FIFO2_D29	
IO_L12N_T1_MRCC_AD5N_15	E17	FIFO2_D30	
IO_L13P_T2_MRCC_15	E18	FIFO2_CLK	
IO_L13N_T2_MRCC_15	D18	FIFO2_D2	
IO_L14P_T2_SRCC_15	H17	FIFO2_D27	
IO_L14N_T2_SRCC_15	H18	FIFO2_D17	
IO_L15P_T2_DQS_15	D19	FIFO2_D1	
IO_L15N_T2_DQS_ADV_B_15	D20	FIFO2_D0	
IO_L16P_T2_A28_15	G19	FIFO2_D19	
IO_L16N_T2_A27_15	F20	FIFO2_D21	
IO_L17P_T2_A26_15	F19	FIFO2_D22	
IO_L17N_T2_A25_15	E20	FIFO2_D31	
IO_L18P_T2_A24_15	H19	FIFO2_D16	
IO_L18N_T2_A23_15	G20	FIFO2_D18	
IO_L19P_T3_A22_15	K20	FIFO2_BE2	
IO_L19N_T3_A21_VREF_15	J20	FIFO2_TXE	
IO_L20P_T3_A20_15	J18	FIFO2_BE3	
IO_L20N_T3_A19_15	J19	FIFO2_WR#	
IO_L21P_T3_DQS_15	L19	FIFO2_OE#	
IO_L21N_T3_DQS_A18_15	L20	FIFO2_RD#	
IO_L22P_T3_A17_15	K16	FIFO2_RXF	
IO_L22N_T3_A16_15	K17		
IO_L23P_T3_FOE_B_15	M17		
IO_L23N_T3_FWE_B_15	L18		
IO_L24P_T3_RS1_15	L17		
IO_L24N_T3_RS0_15	K18		
IO_25_15	M16		

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Title

FIFO channels 1-2

Size

A4

Number

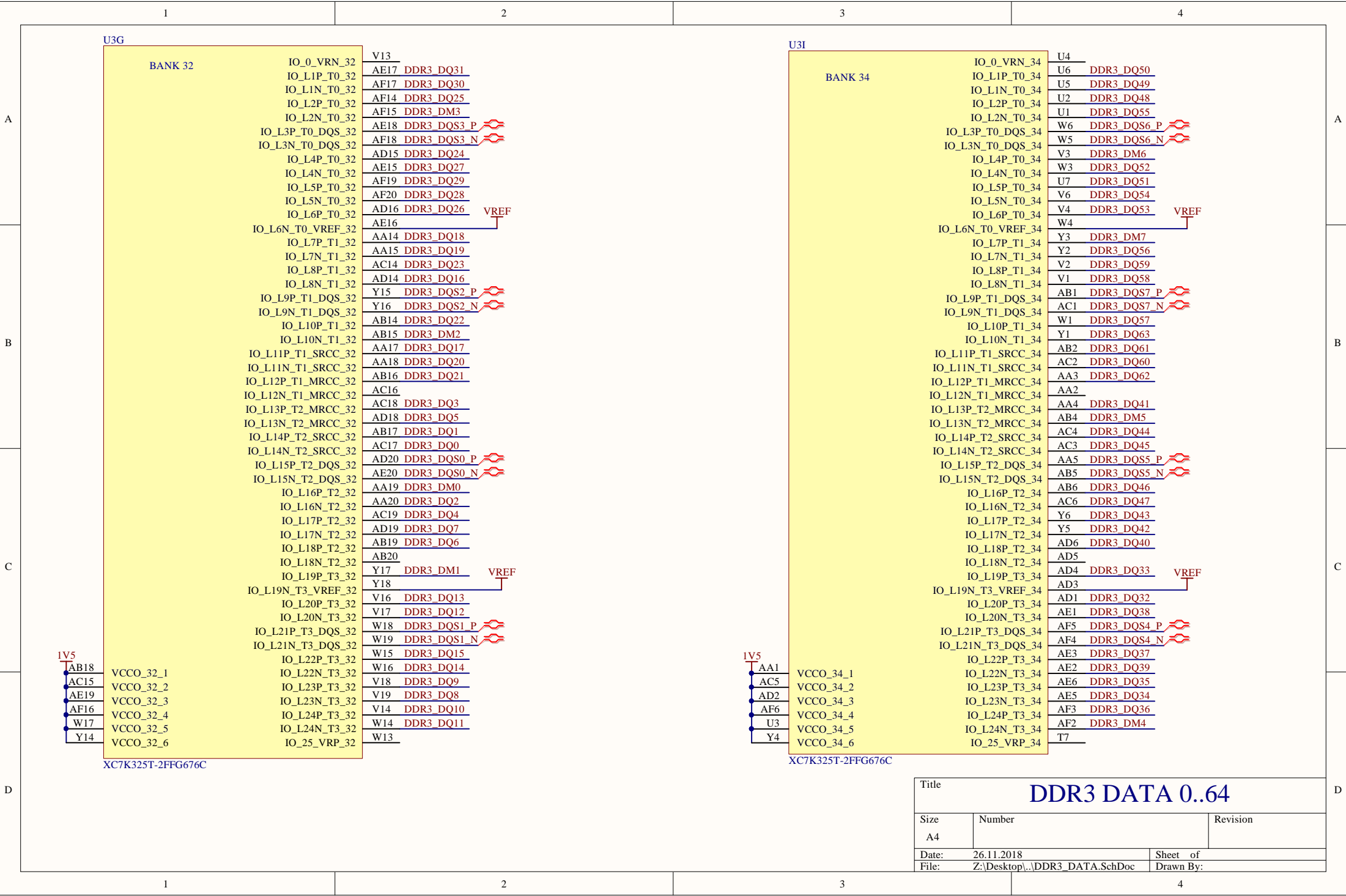
Revision

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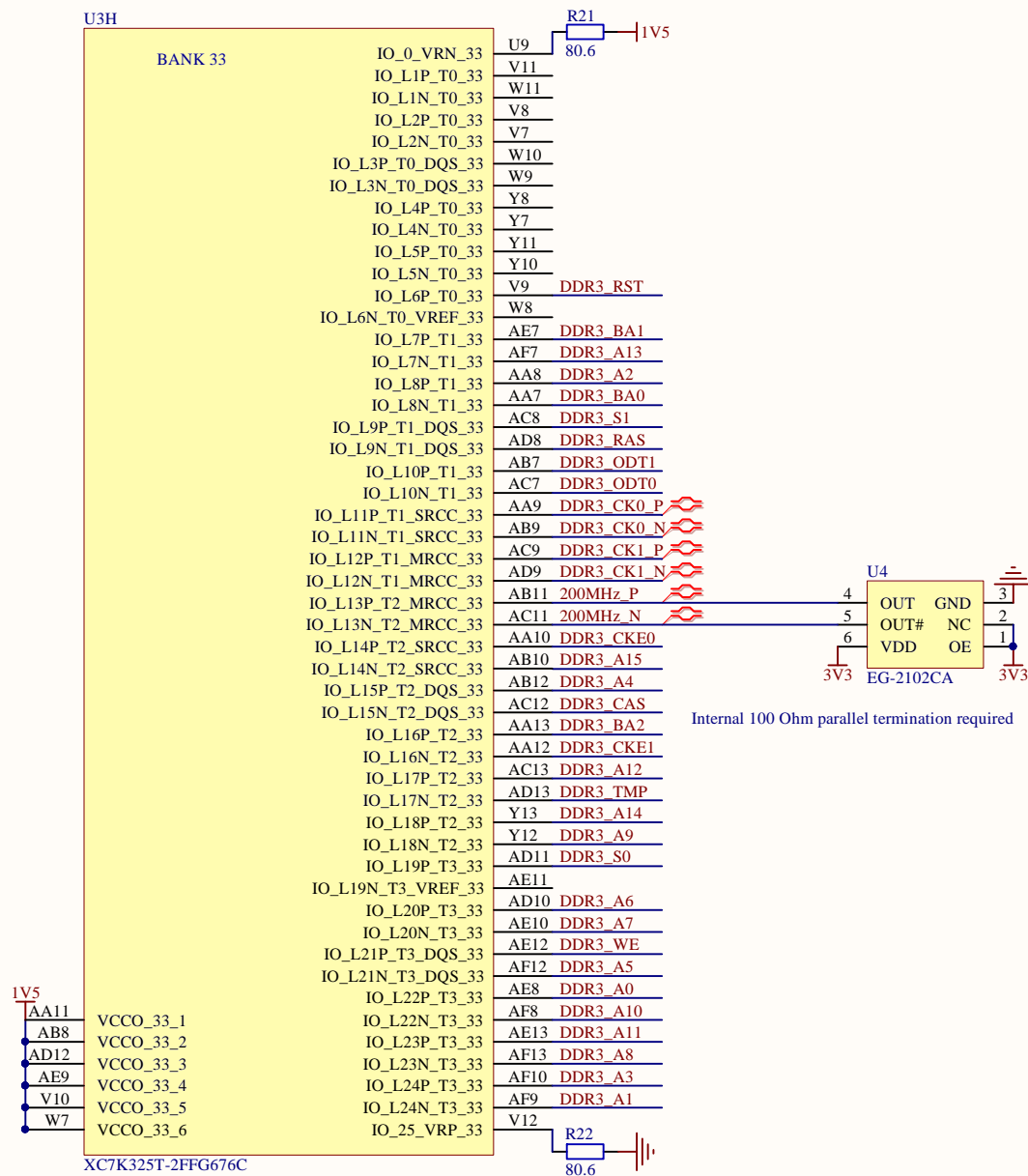
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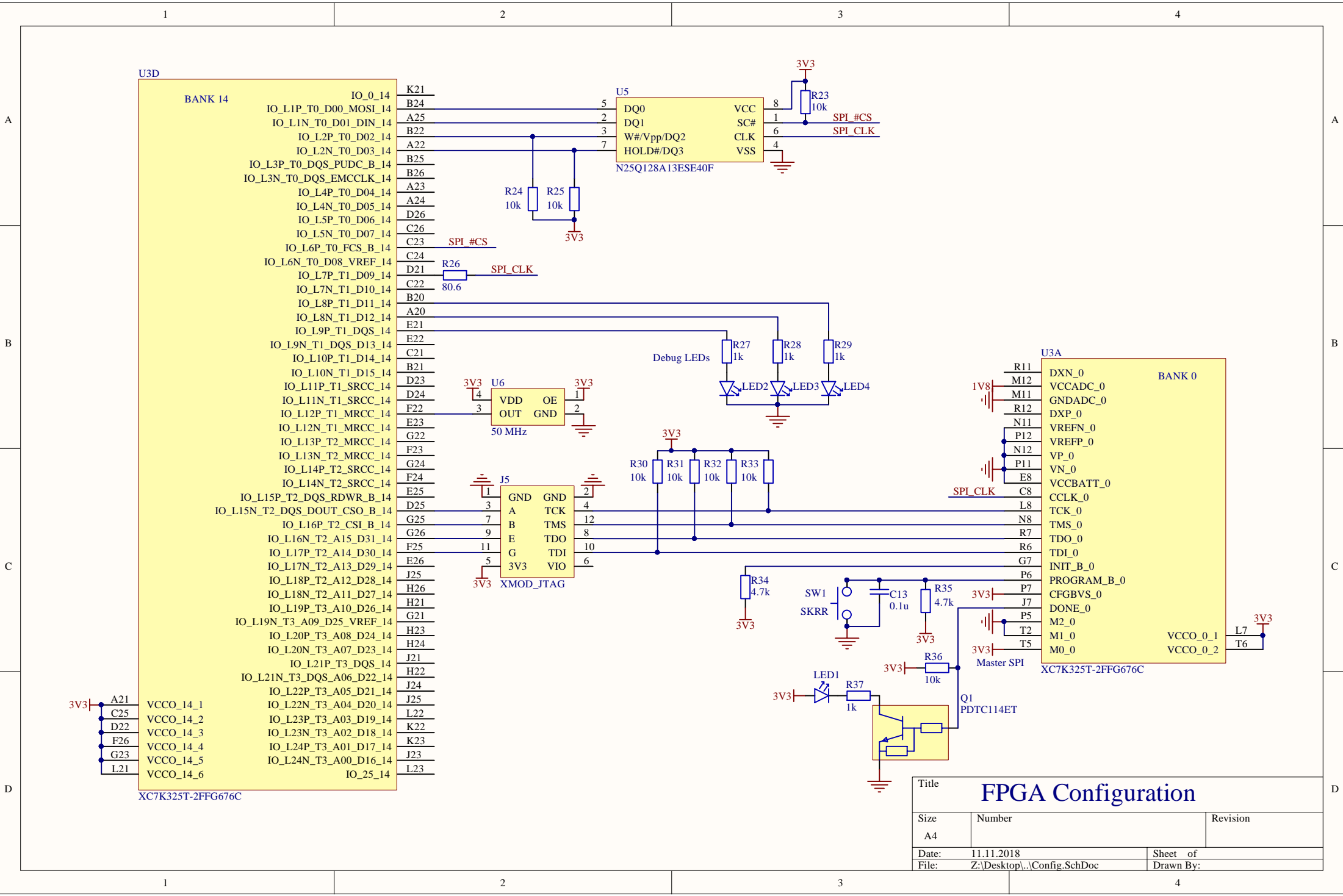
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Size	Number	Revision	
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Title			FPGA Configuration	
Size	Number		Revision	
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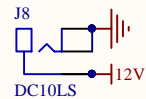
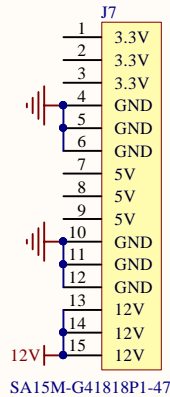
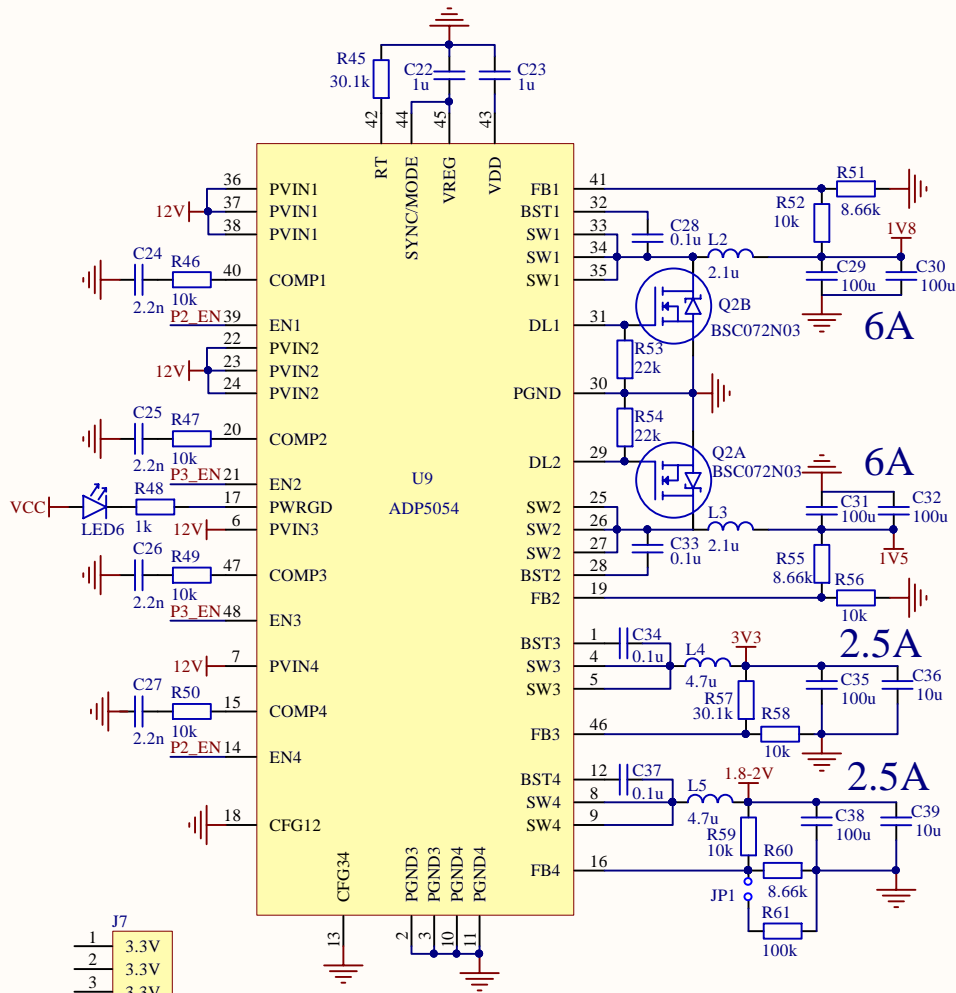
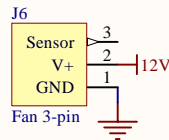
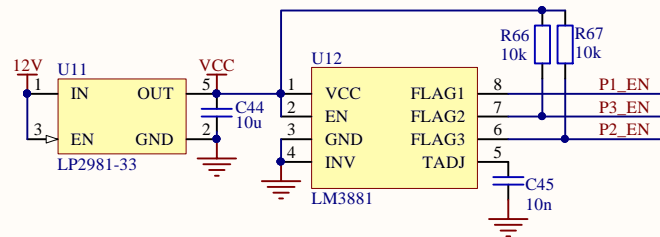
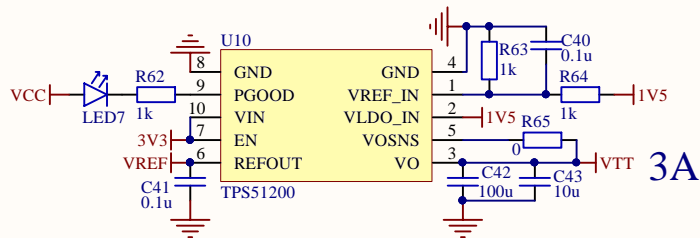
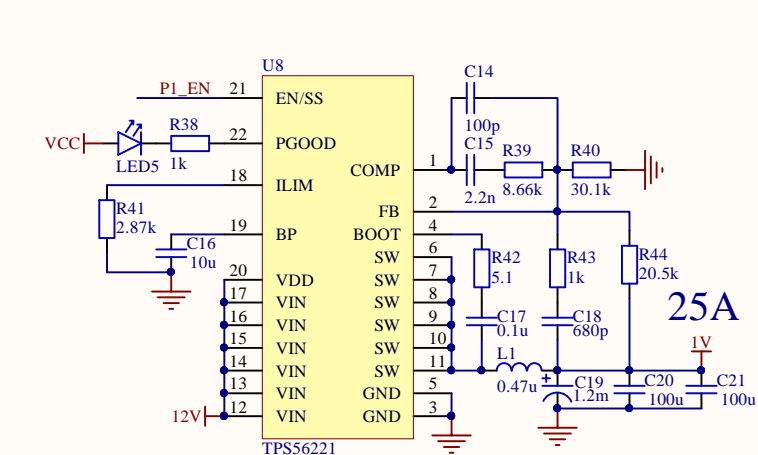
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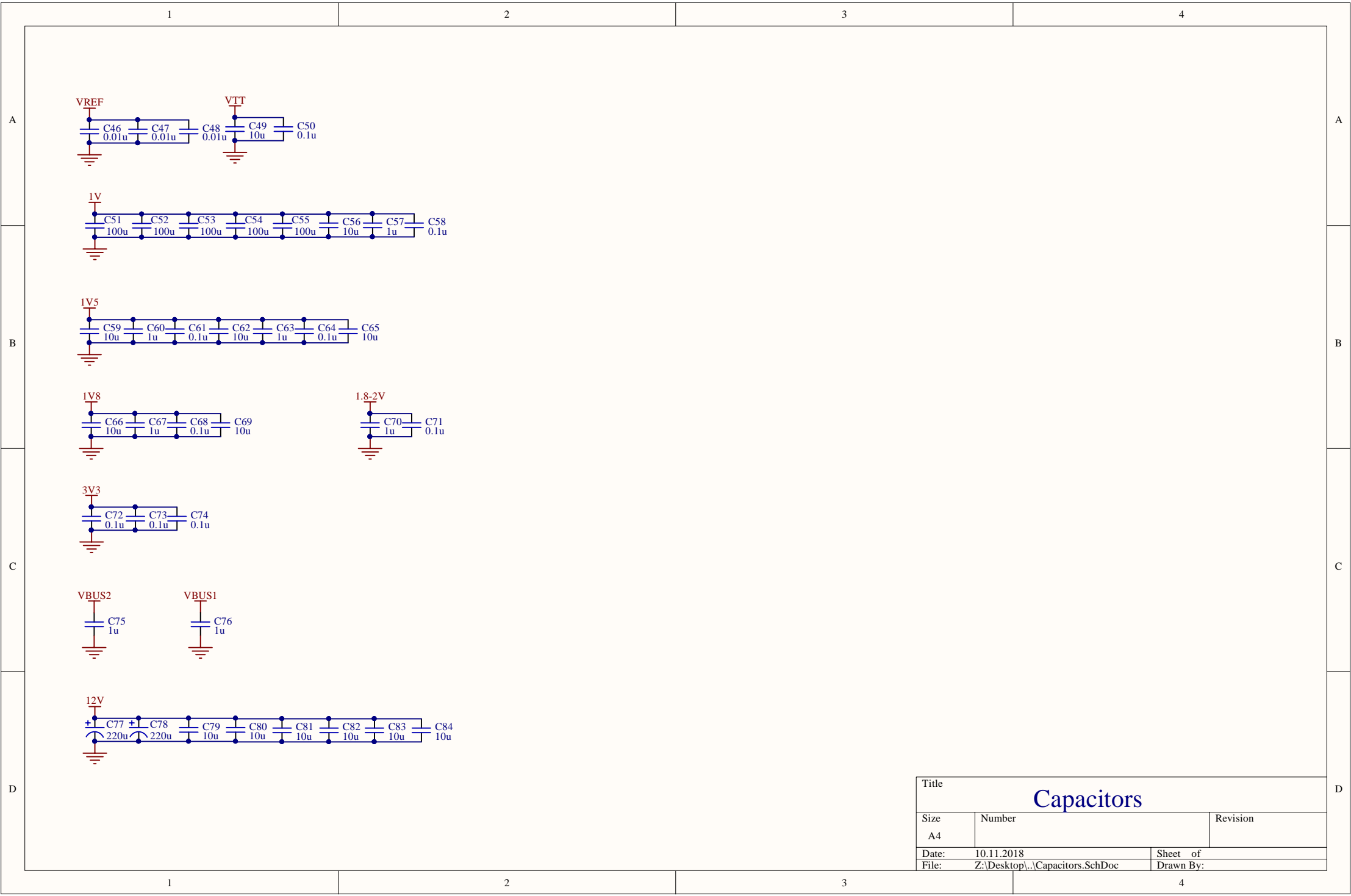
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Power Circuit		
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Title			Capacitors
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