

Department of Electrical and Electronic Engineering Imperial College London

Exhibition Road London SW7 2AZ

Tel: +44 (0) 20 7594 6263 Fax:+44 (0) 20 7823 8125

p.cheung@imperial.ac.uk www.ee.imperial.ac.uk/pcheung

Peter Y. K. Cheung Professor of Digital Systems Head of Department

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To whom it may concern:

Vikram Voleti

The above named carried out a two month internship here in the Electrical and Electronic Engineering Department of Imperial College London, in summer 2011. He worked in our Circuits and Systems research group, on "Intra-Die Power Variation Measurement in sub-nm FPGAs".

We were highly satisfied with Vikram's contribution. He proved to be a curious student, eager to learn and to acquire new skills. Although he did not have any background in the research topic, he gained a basic knowledge of FPGAs within a few days. He also picked up new languages (e.g python, matlab), and an understanding of operation of instruments pretty quickly.

Vikram has a pleasant personality, and a lot of potential. I would certainly recommend him for tasks/projects which require an aptitude for learning and diligence.

Peter Cheung