

5th July 2011

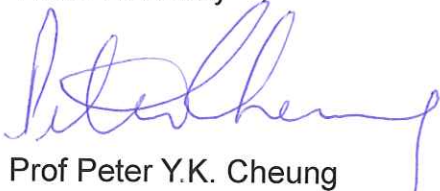
To Whom it May Concern

This is to certify that Mr. Vikram Voleti, from IIT Kharagpur, India, worked as an intern in the Circuits & Systems Group, EEE, Imperial College from 10/05/2011 to 06/07/2011 on the following topic:

"Measurement of Intra-die Power Variation in Sub-nm FPGAs"

During this Internship he developed an automated measurement flow using oscilloscopes to generate variation maps of FPGAs.

Yours sincerely



Prof Peter Y.K. Cheung
Head of Department, Electrical and Electronic Engineering

