

Faculty of Computer Science, Electrical Engineering and Mathematics Department of Computer Science

An LLVM based toolchain for transparent acceleration of digital image processing applications using FPGA overlay architectures

Master Thesis

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Supervisors:

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EMPTY version

Paderborn, July 2015

Abstract

THIS IS MY ABSTRACT

Declaration

I hereby declare that I prepared this thesis entirely on my own and have not used outside sources without declaration in the text. Any concepts or quotations applicable to these sources are clearly attributed to them. This thesis has not been submitted in the same or substantially similar version, not even in part, to any other authority for grading and has not been published elsewhere.

Lukas Funke

Paderborn, 29.4.2015

Preface

Please write all your preface text here. If you do so, don't forget to thank your supervisor, other committee members, your family, colleagues etc. etc.

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Listings

Introduction

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In this particular template special care has been taken to position the thesis title correctly on the front page for the TU/e see-through box in the default cover. However, please check (and double check!) if this is still correct before you send your thesis to the repro shop and have copies printed. Additionally, we defined the command \todo{} that produces a ToDo note in the margin (and introduces some white space in the text unfortunately).

The ToDo note

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Joos Buijs Eindhoven, March 2013.

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Preliminaries

This template has been used to publish the thesis of Buijs [1] and is originally used for the thesis of Nugteren [2].

One of the best resources for LATEX basics, and advanced constructs, is the LATEX wikibook¹. Of course colleagues and a good internet search using your favorite search engine can do wonders if you're stuck.

¹To be found at http://en.wikibooks.org/wiki/LaTeX/

First Real Chapter

This is the first real chapter, this depends on your thesis structure.

Second Real Chapter

And the second real chapter.

Conclusions

Write your conclusions here.

Bibliography

- [1] J.C.A.M. Buijs. Mapping Data Sources to XES in a Generic Way. Master's thesis, Eindhoven University of Technology, 2010. 3
- [2] G.M. Nugteren. Process Model Simplification. Master's thesis, Eindhoven University of Technology, 2010. 3

Appendix A

My First Appendix

In this file (appendices/main.tex) you can add appendix chapters, just as you did in the thesis.tex file for the 'normal' chapters. You can also choose to include everything in this single file, whatever you prefer.