POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	:	
SAMPLE CODE	:	SH720128T004-ZBC02
MASS PRODUCTION CODE	:	PH720128T004-ZBC02
SAMPLE VERSION	:	02
SPECIFICATIONS EDITION	:	005
DRAWING NO. (Ver.)	:	LMD-PH720128T004-ZBC02 (Ver.003)
PACKAGING NO. (Ver.)	:	PKG-PH720128T004-ZBC02 (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
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POWERTIP
2019.09.04
TW RD APR

□ Preliminary specification for design input

■ Specification for sample approval

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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
06/08/2017	01	001	New Drawing.	-	Yuan
07/28/2017	01	002	First Sample Add Shielding tape	- Appendix	Yuan
12/28/2017	02	003	Second Sample Modify Reliability Test Condition Modify Frame T/P FPC with EMI Shield	- 33 Appendix Appendix	Yuan
12/12/2018	02	004	Modify Contrast ratio	6	Yuan
08/16/2019	02	005	Modify LCD type from IPS to Full Viewing Angle ADD Refer Initial Code	4 21~31	Yuan

Total: 45 Page

Contents

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	720 * (RGB) * 1280
LCD Type	Full Viewing Angle , Normally Black , Transmissive type
Touch panel	True Multi-Touch Capacitive Touch Panel True Multi-touch with up to 10 Points of Absolution
Screen size(inch)	5.0 inch
Backlight Type	LED B/L
Weight	95g
Control IC	ILI9881C
Interface	MIPI Interface
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	91.46 (W) * 143.4 (L) * 5.17 (H)	mm
Viewing Area	63.1 (W) * 111.4 (L)	mm
Active Area	62.1 (W) * 110.4 (L)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Supply Voltage	VDD	GND=0	-0.3	+3.8	V	-
Supply Voltage	VCI	GND=0	-0.3	+7.0	V	
Operating Temperature	T_{OP}	-	-20	+70	℃	
Storage Temperature	T_{ST}	-	-30	+80	℃	

1.4 DC Electrical Characteristics

GND = 0V, Ta = 25℃

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Supply Voltage	VDD	3.0	3.3	3.6	V	-
Supply Voltage	VCI	3.0	3.3	3.6		
Input signal Voltage (I2C Interface)	V_{IH}	0.7*VDD	-	VDD		
	V_{IL}	-0.3	-	0.3*VDD		
	V_{OH}	0.8*VDD	-	VDD		
	V_{OL}	0	-	0.2*VDD		
Supply Current	I_{VDD}	-	20	35	mA	Note1
	I_{VCI}	-	35	50	mA	

Note1: Maximum current display.

1.5 Optical Characteristics

VDD =VCI= 3.3 V, Ta=25℃

Item		Symbol	Condition	Min.	Typ.	Max.	unit	
Response time		Tr +Tf	Ta = 25℃ θX, θY = 0°	-	35	40	ms	Note 2
Viewing angle	Top	θY+	CR ≥ 10	80	85	-	Deg.	Note 4
	Bottom	θY-		80	85	-		
	Left	θX-		80	85	-		
	Right	θX+		80	85	-		
Contrast ratio		CR	Ta = 25℃ θX , θY = 0°	650	800	-		Note 3
Color of CIE Coordinate (With B/L & touch panel)	White	X		0.26	0.31	0.36	-	Note1
		Y		0.31	0.36	0.41		
	Red	X		0.61	0.66	0.71		
		Y		0.27	0.32	0.37		
	Green	X		0.27	0.32	0.37		
		Y		0.57	0.62	0.67		
	Blue	X		0.09	0.14	0.19		
		Y		0.01	0.04	0.09		
Average Brightness Pattern=white display (With B/L & touch panel) *1		IV	IF=40mA	430	500	-	cd/m^2	Note1
Uniformity (With B/L & touch panel) *2		△B		70	-	-	%	Note1

Note 1:

*1：△B=B(min) / B(max) * 100%

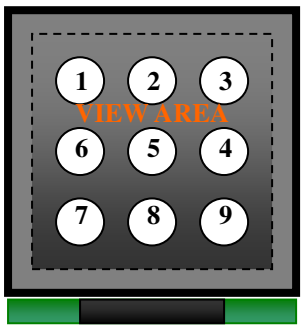
*2：Measurement Condition for Optical Characteristics:

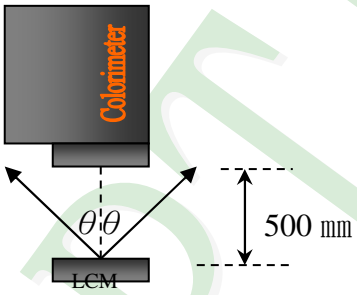
a：Environment: 25℃±5℃ / 60±20%R.H，no wind，dark room below 10 Lux at typical lamp current and typical operating frequency.

b：Measurement Distance: 500 ± 50 ㎜ ，(θ= 0°)

c：Equipment: TOPCON BM-7 fast，(field 1°)，after 10 minutes operation.

d：The uncertainty of the C.I.E coordinate measurement ±0.01，Average Brightness ± 4%





Colorimeter=BM-7 fast

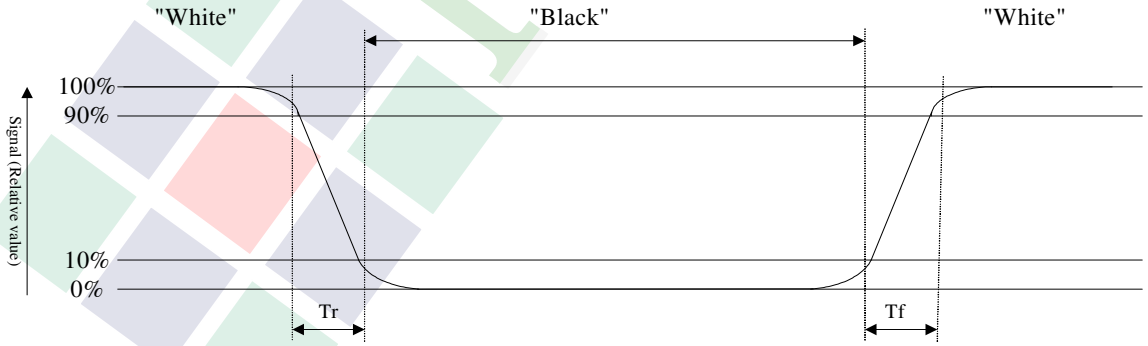
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

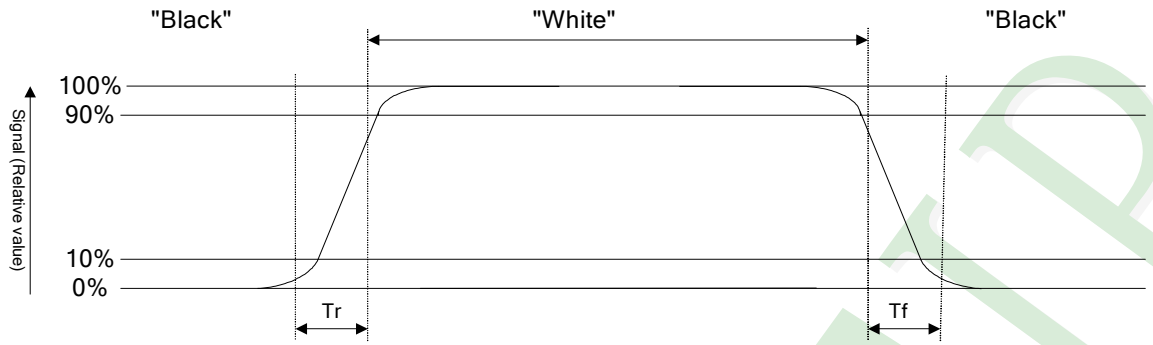
The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



Normally Black



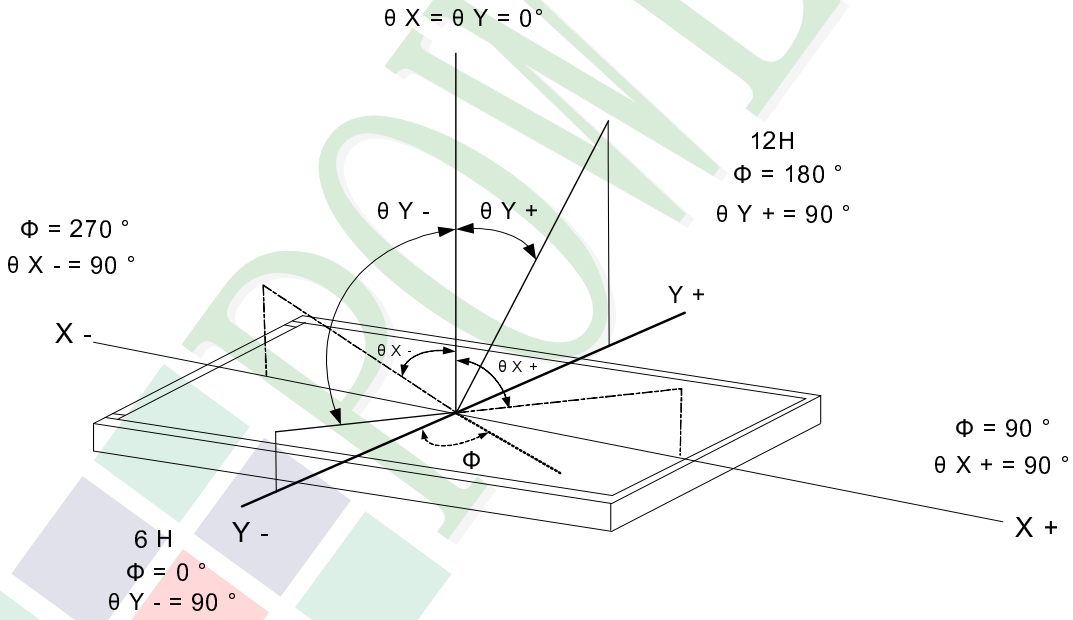
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at “White” state}}{\text{Photo detector output when LCD is at “Black” state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25℃	-	60	mA
LED Reverse Voltage	VR	Ta =25℃	-	5	V
Power Dissipation	PD	Ta =25℃	-	1120	W

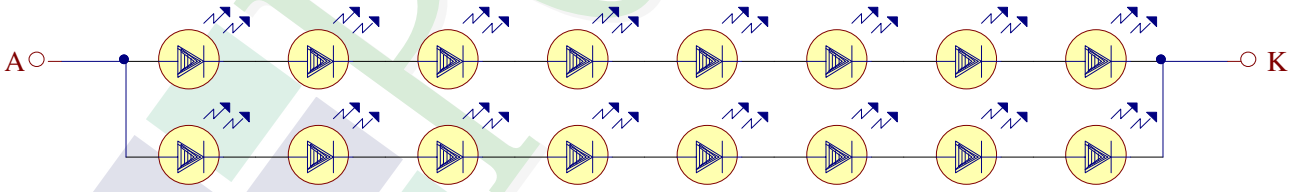
Backlight Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 40 mA	21.6	24.8	28.0	V
Average Brightness (Without LCD)	IV		12000	14500	16500	cd/m^2
CIE Color Coordinate (Without LCD)	X		0.27	0.30	0.33	-
	Y		0.27	0.30	0.33	
Uniformity *1	△B		80	-	-	*2
Color	White					

*1：This value will be changed while mass production.

*2：△B=B(min) / B(max)%

B/L Internal Circuit Diagram



Other Description

Item	Conditions	Description
Life Time	Ta =25℃ IF=40 mA	20000 hrs

1.7 Touch Panel Characteristics

Features

Item	Standard Value
Touch Panel Size	7”
Touch type	Projective capacitive touch panel
Input Method	True Multi-touch with up to 10 Points of Absolution X and Y Coordinates
Output Interface	I^2C
IC	FT5426

Mechanical Specifications

Item	Standard Value	Unit
Viewing Area	111.4 (W) * 63.1 (L)	mm
Number of sensing channel	22 x 12	mm

Absolute Maximum Ratings

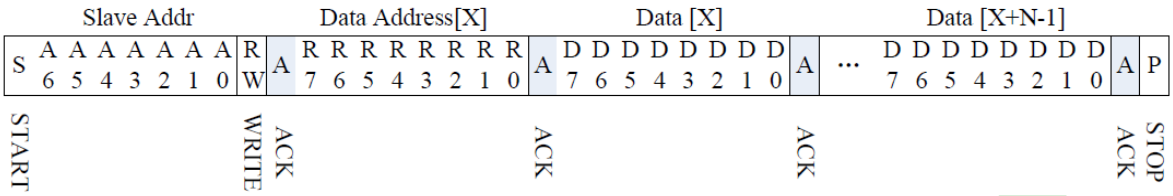
Item	Symbol	Condition	Min.	Max.	Unit
Supply voltage	TPVDD	-	-0.3	3.6	V
Operating Temperature	T_{OP}	-	-20	+70	℃
Storage Temperature	T_{ST}	-	-30	+80	℃

DC Electrical Characteristics

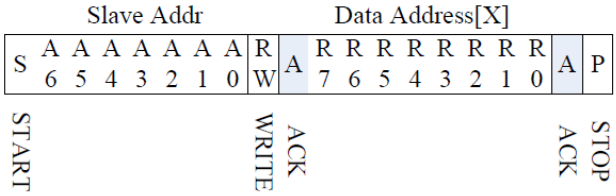
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage	TPVDD	-	3.0	3.3	3.6	V
Input High Voltage	VIH	-	0.7 TPVDD	-	TPVDD	V
Input Low Voltage	VIL	-	-0.3	-	0.3 TPVDD	V
Output High Voltage	VOH	IOH=-0.1mA	0.7 TPVDD	-	-	V
Output Low Voltage	VOL	IOL=+0.1mA	-	-	0.3 TPVDD	V

I²C Read/Write Interface description

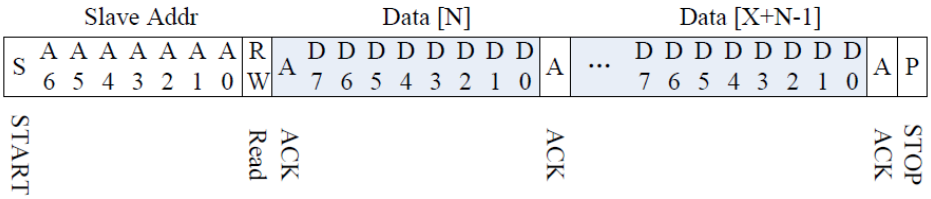
Write N bytes to I2C slave



Set Data Address



Read X bytes from I2C Slave

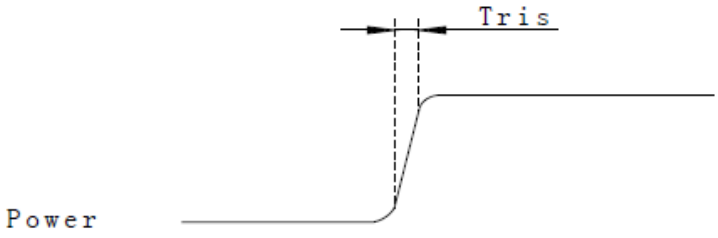


Mnemonics Description

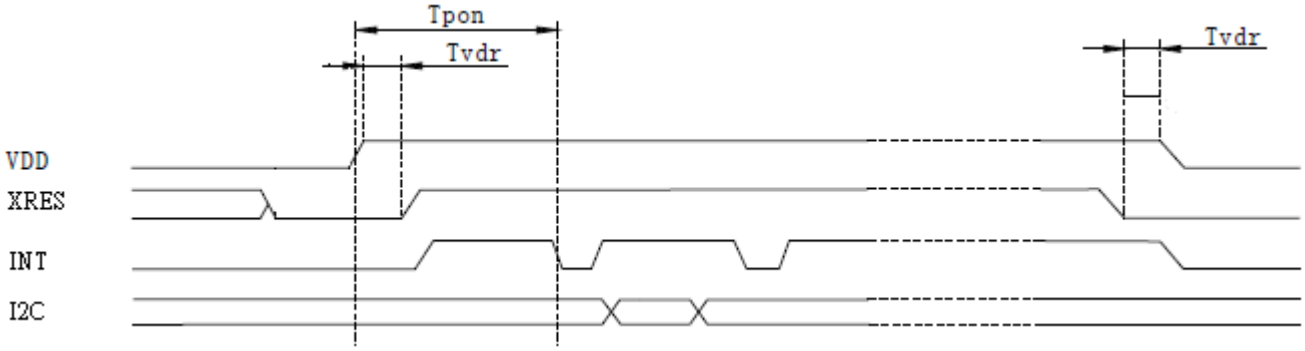
Mnemonics	Description
S	I2C Start or I2C Restart
A[6:0]	Slave address A[6:0]:0111000b
R/ W	'1' for read, '0'for write
A(N)	ACK(NACK)
P	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)

Timing Characteristics

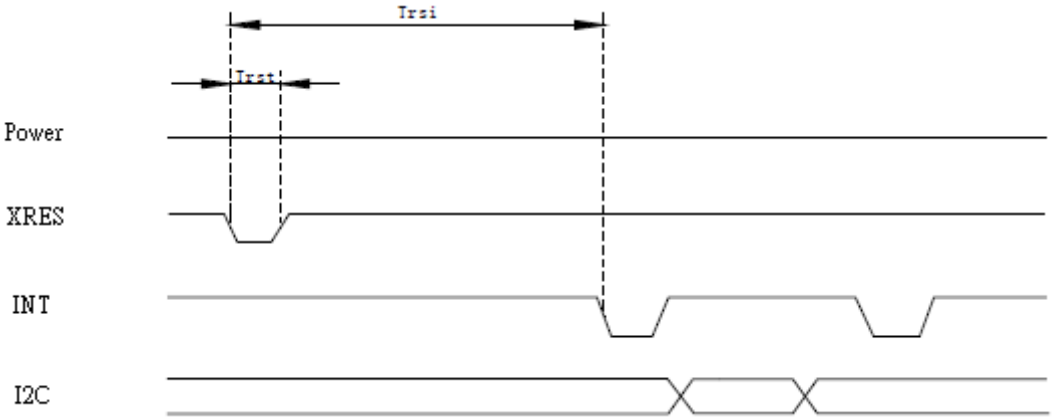
Parameter	Unit	Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) START condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup Time for STOP condition	us	4.0	\



Power on time



Power on Sequence



Reset Sequence

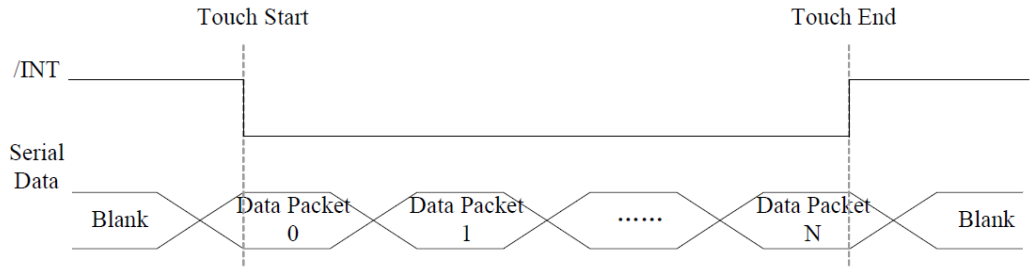
Power on / Reset Sequence Parameters

Parameter	Description	Min	Max	Units
Tris	Rise time from 0.1VDD to 0.9VDD	--	5	ms
Tpon	Time of starting to report point after powering on	200	--	ms
Tvdr	Reset time after VDD powering on	1	--	ms
Trsi	Time of starting to report point after resetting	200	--	ms
Trst	Reset time	1	--	ms

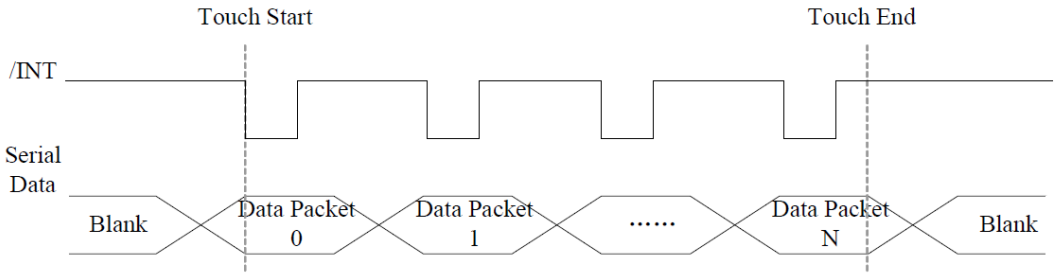
Interrupt signal from CTP to Host

As for standard CTP, host need to use both interrupt control signal and serial data interface to get the touch data. There are two kind of method to use interrupt: interrupt trigger and interrupt query.

Here is the timing to get touch data.



Interrupt query mode



Interrupt trigger mode

Host use general I2C protocol to read the touch data or the information from CTP . CTP will send host a interrupt signal when there is a valid touch. Then host can use the serial data interface to get the touch data. If there is no valid touch detected, the /INT will not be pulled up, the host do not need to read the touch data.

NOTE: “valid touch” may have different definition in various systems. For example, in some systems, the valid touch is defined as there is one more valid touch point. But in some other systems, the valid touch is defined as one more valid touch with valid gestures. In usual, /INT will be pulled up when there is a valid touch point, and to be low when a touch finishes.

As for interrupt trigger mode, /INT signal will be low if there is a touch detected. But for per update of valid touch data, CTP will produce a valid pulse for /INT signal, host can read the touch data periodically according to the frequency of this pulse. In this mode, the pulse frequency is the touch data update frequency.

CTP Register Mapping

Address	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0	Host Access
00h	DEVIDE_MODE	-	Device Mode[2:0]			-	-	-	-	WR
01h	GEST_ID	Gesture ID[7:0]								R
02h	TD_STATUS	-	-	-	-	Number of touch points[3:0]				R
03h	TOUCH1_XH	1st Event Flag		-	-	1st Touch X Position[11:8]				R
04h	TOUCH1_XL	1st Touch X Position[7:0]								R
05h	TOUCH1_YH	1st Touch ID[3:0]				1st Touch Y Position[11:8]				R
06h	TOUCH1_YL	1st Touch Y Position[7:0]								R
07h	-	-								R
08h	-	-								R
09h	TOUCH2_XH	2st Event Flag		-	-	2st Touch X Position[11:8]				R
0Ah	TOUCH2_XL	2st Touch X Position[7:0]								R
0Bh	TOUCH2_YH	2st Touch ID[3:0]				2st Touch Y Position[11:8]				R
0Ch	TOUCH2_YL	2st Touch Y Position[7:0]								R
0Dh	-	-								R
0Eh	-	-								R
0Fh	TOUCH3_XH	3st Event Flag		-	-	3st Touch X Position[11:8]				R
10h	TOUCH3_XL	3st Touch X Position[7:0]								R
11h	TOUCH3_YH	3st Touch ID[3:0]				3st Touch Y Position[11:8]				R
12h	TOUCH3_YL	3st Touch Y Position[7:0]								R
13h	-	-								R
14h	-	-								R
15h	TOUCH4_XH	4st Event Flag		-	-	4st Touch X Position[11:8]				R
16h	TOUCH4_XL	4st Touch X Position[7:0]								R
17h	TOUCH4_YH	4st Touch ID[3:0]				4st Touch Y Position[11:8]				R
18h	TOUCH4_YL	4st Touch Y Position[7:0]								R
19h	-	-								R
1Ah	-	-								R
1Bh	TOUCH5_XH	5st Event Flag		-		5st Touch X Position[11:8]				R
1Ch	TOUCH5_XL	5st Touch X Position[7:0]								R
1Dh	TOUCH5_YH	5st Touch ID[3:0]				5st Touch Y Position[11:8]				R
1Eh	TOUCH5_YL	5st Touch Y Position[7:0]								R
1Fh	-	-								R
20h	-	-								R

Address	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0	Host Access
21h	TOUCH6_XH	6st Event Flag		-	-	6st Touch X Position[11:8]				R
22h	TOUCH6_XL	6st Touch X Position[7:0]								R
23h	TOUCH6_YH	6st Touch ID[3:0]				6st Touch Y Position[11:8]				R
24h	TOUCH6_YL	6st Touch Y Position[7:0]								R
25h	-	-								R
26h	-	-								R
27h	TOUCH7_XH	7st Event Flag		-	-	7st Touch X Position[11:8]				R
28h	TOUCH7_XL	7st Touch X Position[7:0]								R
29h	TOUCH7_YH	7st Touch ID[3:0]				7st Touch Y Position[11:8]				R
2ah	TOUCH7_YL	7st Touch Y Position[7:0]								R
2bh	-	-								R
2ch	-	-								R
2dh	TOUCH8_XH	8st Event Flag		-	-	8st Touch X Position[11:8]				R
2eh	TOUCH8_XL	8st Touch X Position[7:0]								R
2fh	TOUCH8_YH	8st Touch ID[3:0]				8st Touch Y Position[11:8]				R
30h	TOUCH8_YL	8st Touch Y Position[7:0]								R
31h	-	-								R
32h	-	-								R
33h	TOUCH9_XH	9st Event Flag		-	-	9st Touch X Position[11:8]				R
34h	TOUCH9_XL	9st Touch X Position[7:0]								R
35h	TOUCH9_YH	9st Touch ID[3:0]				9st Touch Y Position[11:8]				R
36h	TOUCH9_YL	9st Touch Y Position[7:0]								R
37h	-	-								R
38h	-	-								R
39h	TOUCH10_XH	10st Event Flag		-	-	10st Touch X Position[11:8]				R
3ah	TOUCH10_XL	10st Touch X Position[7:0]								R
3bh	TOUCH10_YH	10st Touch ID[3:0]				10st Touch Y Position[11:8]				R
3ch	TOUCH10_YL	10st Touch Y Position[7:0]								R
3dh	-	-								R
3eh	-	-								R
3fh	-	-								R

DEVICE_MODE

This register is the device mode register, configure it to determine the current mode of the chip.

Address	Bit Address	Register Name	Description
00h	6 : 4	Device Mode [2:0]	000b Work Mode 100b Factory Mode – read raw data

GEST_ID

This register describes the gesture of a valid touch.

Address	Bit Address	Register Name	Description
01h	7 : 0	Gesture ID [7:0]	0x10 Move UP 0x14 Move Left 0x18 Move Down 0x1C Move Right 0x48 Zoom In 0x49 Zoom Out

TD_STATUS

This register is the Touch Data status register.

Address	Bit Address	Register Name	Description
02h	7 : 4	Reserved	
	3 : 0	Number of touch points[3:0]	How many points detected. 1-5 is valid.

TOUCHn_XH

This register describes MSB of the X coordinate of the nth touch point and the corresponding event flag.

Address	Bit Address	Register Name	Description
03h ~ 39h	7 : 6	Event Flag	00b: Put Down 01b: Put Up 10b: Contact 11b: Reserved
	5 : 4		Reserved
	3 : 0	Touch X Position [11:8]	MSB of Touch X Position in pixels

TOUCHn_XL

This register describes LSB of the X coordinate of the nth touch point

Address	Bit Address	Register Name	Description
04h ~ 3Ah	7 : 0	Touch X Position [7:0]	LSB of the Touch X Position in pixels

TOUCHn_YH

This register describes MSB of the Y coordinate of the nth touch point and corresponding touch ID.

Address	Bit Address	Register Name	Description
05h ~ 3Bh	7 : 4	Touch ID[3:0]	Touch ID of Touch Point
	3 : 0	Touch Y Position [11:8]	MSB of Touch Y Position in pixels

TOUCHn_YL

This register describes LSB of the Y coordinate of the nth touch point.

Address	Bit Address	Register Name	Description
06h ~ 3Ch	7:0	Touch Y Position[7:0]	LSB of The Touch Y Position in pixels

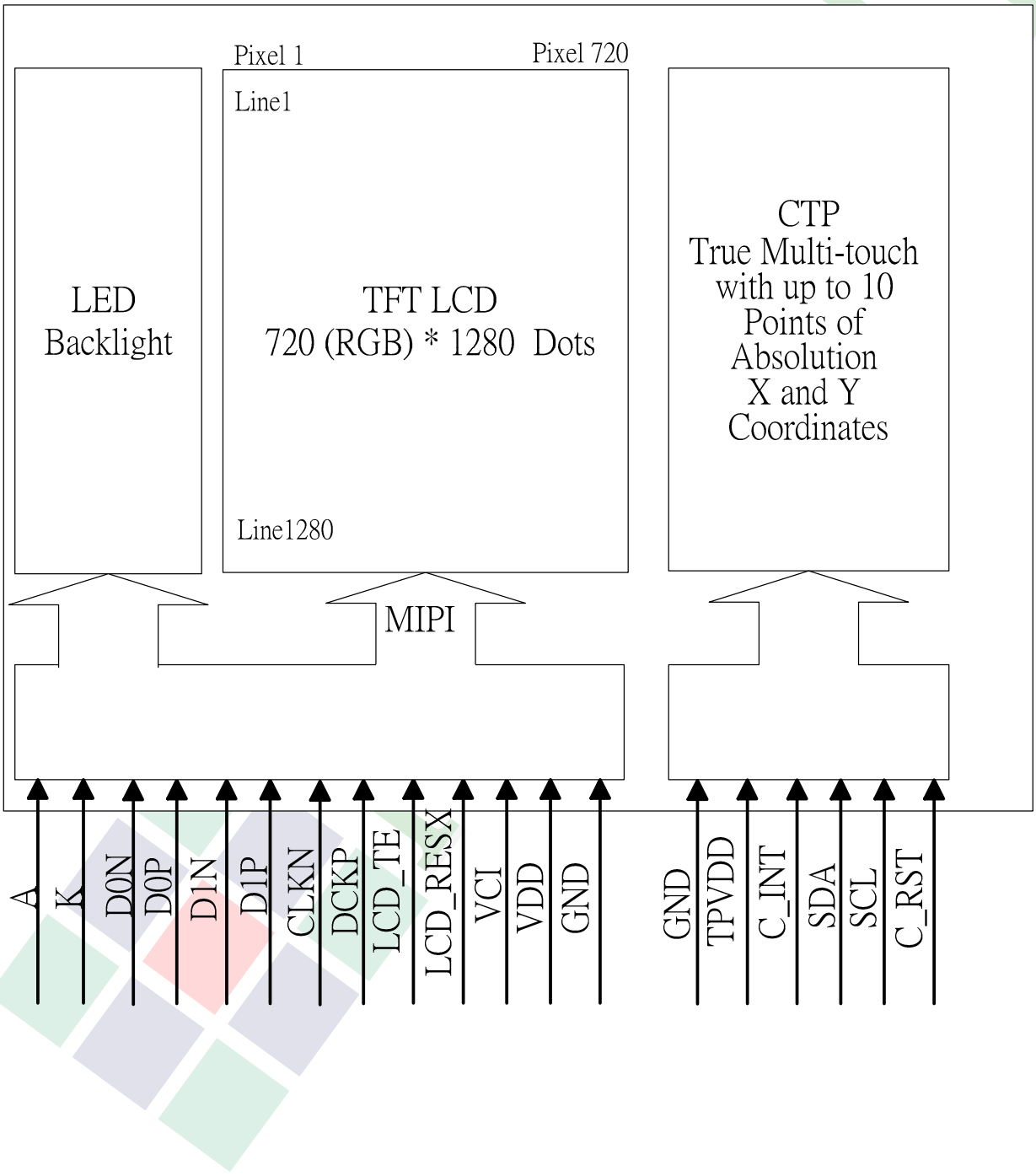
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

LCM interface

Pin#	Name	DESCRIPTION
1	NC	No Connection
2	K	Power Supply for LED Backlight cathode input
3	A	Power Supply for LED Backlight anode input
4	NC	No Connection
5	NC	No Connection
6	GND	Ground.
7	NC	No Connection
8	NC	No Connection
9	VDD	Power Supply for internal logic regulator.
10	VCI	Power Supply for analog circuit
11	GND	Ground.
12	LCD_RESX	The external reset pin.
13	LCD_TE	Tearing effect output pin .
14	GND	Ground.
15	CLKP	MIPI Differential Clock Pair.
16	CLKN	MIPI Differential Clock Pair.
17	GND	Ground.
18	D1P	MIPI Differential Data Pair. (Data Lane 1)
19	D1N	MIPI Differential Data Pair. (Data Lane 1)
20	GND	Ground.
21	D0P	MIPI Differential Data Pair. (Data Lane 0)
22	D0N	MIPI Differential Data Pair. (Data Lane 0)
23	GND	Ground.
24	NC	No Connection

Capacitive Touch Panel (CTP) Interface

Pin No.	Symbol	Function
1	C_RST	RESET.
2	SCL	I^2C Clock.
3	SDA	I^2C Data.
4	C_INT	The interrupt from the CTP to the Host H: CTP interrupt not requested L: CTP request interrupt
5	TPVDD	Power.
6	GND	Ground.

2.3 Refer Initial Code :

```
//************* Reset LCD Driver ****************//
LCD_nRESET = 1;
Delayms(1); // Delay 1ms
LCD_nRESET = 0;
Delayms(1); // Delay 1ms // This Delay time is necessary
LCD_nRESET = 1;
Delayms(10); // Delay 120 ms
//************* Start Initial Sequence **********//
LCD_ILI9881C_CMD(0xFF);
LCD_ILI9881C_INDEX(0x98);
LCD_ILI9881C_INDEX(0x81);
LCD_ILI9881C_INDEX(0x03);

LCD_ILI9881C_CMD(0x01);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x02);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x03);
LCD_ILI9881C_INDEX(0x73);

LCD_ILI9881C_CMD(0x04);
LCD_ILI9881C_INDEX(0x73);

LCD_ILI9881C_CMD(0x05);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x06);
LCD_ILI9881C_INDEX(0x06);

LCD_ILI9881C_CMD(0x07);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x08);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x09);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x0a);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x0b);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x0c);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x0d);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x0e);
LCD_ILI9881C_INDEX(0x01);
```

```
LCD_ILI9881C_CMD(0x0f);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x10);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x11);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x12);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x13);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x14);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x15);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x16);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x17);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x18);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x19);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x1a);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x1b);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x1c);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x1d);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x1e);
LCD_ILI9881C_INDEX(0xC0);

LCD_ILI9881C_CMD(0x1f);
LCD_ILI9881C_INDEX(0x80);

LCD_ILI9881C_CMD(0x20);
LCD_ILI9881C_INDEX(0x04);
```

```
LCD_ILI9881C_CMD(0x21);
LCD_ILI9881C_INDEX(0x03);

LCD_ILI9881C_CMD(0x22);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x23);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x24);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x25);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x26);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x27);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x28);
LCD_ILI9881C_INDEX(0x33);

LCD_ILI9881C_CMD(0x29);
LCD_ILI9881C_INDEX(0x03);

LCD_ILI9881C_CMD(0x2a);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x2b);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x2c);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x2d);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x2e);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x2f);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x30);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x31);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x32);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x33);
```

```
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x34);
LCD_ILI9881C_INDEX(0x03);

LCD_ILI9881C_CMD(0x35);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x36);
LCD_ILI9881C_INDEX(0x03);

LCD_ILI9881C_CMD(0x37);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x38);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x39);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x3a);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x3b);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x3c);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x3d);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x3e);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x3f);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x40);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x41);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x42);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x43);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x44);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x50);
LCD_ILI9881C_INDEX(0x01);
```

```
LCD_ILI9881C_CMD(0x51);
LCD_ILI9881C_INDEX(0x23);

LCD_ILI9881C_CMD(0x52);
LCD_ILI9881C_INDEX(0x45);

LCD_ILI9881C_CMD(0x53);
LCD_ILI9881C_INDEX(0x67);

LCD_ILI9881C_CMD(0x54);
LCD_ILI9881C_INDEX(0x89);

LCD_ILI9881C_CMD(0x55);
LCD_ILI9881C_INDEX(0xab);

LCD_ILI9881C_CMD(0x56);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x57);
LCD_ILI9881C_INDEX(0x23);

LCD_ILI9881C_CMD(0x58);
LCD_ILI9881C_INDEX(0x45);

LCD_ILI9881C_CMD(0x59);
LCD_ILI9881C_INDEX(0x67);

LCD_ILI9881C_CMD(0x5a);
LCD_ILI9881C_INDEX(0x89);

LCD_ILI9881C_CMD(0x5b);
LCD_ILI9881C_INDEX(0xab);

LCD_ILI9881C_CMD(0x5c);
LCD_ILI9881C_INDEX(0xcd);

LCD_ILI9881C_CMD(0x5d);
LCD_ILI9881C_INDEX(0xef);

LCD_ILI9881C_CMD(0x5e);
LCD_ILI9881C_INDEX(0x10);

LCD_ILI9881C_CMD(0x5f);
LCD_ILI9881C_INDEX(0x09);

LCD_ILI9881C_CMD(0x60);
LCD_ILI9881C_INDEX(0x08);

LCD_ILI9881C_CMD(0x61);
LCD_ILI9881C_INDEX(0x0F);

LCD_ILI9881C_CMD(0x62);
LCD_ILI9881C_INDEX(0x0E);

LCD_ILI9881C_CMD(0x63);
```

```
LCD_ILI9881C_INDEX(0x0D);

LCD_ILI9881C_CMD(0x64);
LCD_ILI9881C_INDEX(0x0C);

LCD_ILI9881C_CMD(0x65);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x66);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x67);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x68);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x69);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x6a);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x6b);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x6c);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x6d);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x6e);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x6f);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x70);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x71);
LCD_ILI9881C_INDEX(0x06);

LCD_ILI9881C_CMD(0x72);
LCD_ILI9881C_INDEX(0x07);

LCD_ILI9881C_CMD(0x73);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x74);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x75);
LCD_ILI9881C_INDEX(0x06);
```

```
LCD_ILI9881C_CMD(0x76);
LCD_ILI9881C_INDEX(0x07);

LCD_ILI9881C_CMD(0x77);
LCD_ILI9881C_INDEX(0x0E);

LCD_ILI9881C_CMD(0x78);
LCD_ILI9881C_INDEX(0x0F);

LCD_ILI9881C_CMD(0x79);
LCD_ILI9881C_INDEX(0x0C);

LCD_ILI9881C_CMD(0x7a);
LCD_ILI9881C_INDEX(0x0D);

LCD_ILI9881C_CMD(0x7b);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x7c);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x7d);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x7e);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x7f);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x80);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x81);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x82);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x83);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x84);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x85);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x86);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x87);
LCD_ILI9881C_INDEX(0x09);
```

```
LCD_ILI9881C_CMD(0x88);
LCD_ILI9881C_INDEX(0x08);

LCD_ILI9881C_CMD(0x89);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0x8A);
LCD_ILI9881C_INDEX(0x02);

LCD_ILI9881C_CMD(0xFF);
LCD_ILI9881C_INDEX(0x98);
LCD_ILI9881C_INDEX(0x81);
LCD_ILI9881C_INDEX(0x04);

LCD_ILI9881C_CMD(0x6C);
LCD_ILI9881C_INDEX(0x15);

LCD_ILI9881C_CMD(0x6E);
LCD_ILI9881C_INDEX(0x2A);

LCD_ILI9881C_CMD(0x6F);
LCD_ILI9881C_INDEX(0x57);

LCD_ILI9881C_CMD(0x3A);
LCD_ILI9881C_INDEX(0xA4);

LCD_ILI9881C_CMD(0x8D);
LCD_ILI9881C_INDEX(0x1A);

LCD_ILI9881C_CMD(0x87);
LCD_ILI9881C_INDEX(0xBA);

LCD_ILI9881C_CMD(0x26);
LCD_ILI9881C_INDEX(0x76);

LCD_ILI9881C_CMD(0xB2);
LCD_ILI9881C_INDEX(0xD1);

LCD_ILI9881C_CMD(0xFF);
LCD_ILI9881C_INDEX(0x98);
LCD_ILI9881C_INDEX(0x81);
LCD_ILI9881C_INDEX(0x01);

LCD_ILI9881C_CMD(0x22);
LCD_ILI9881C_INDEX(0x0A);

LCD_ILI9881C_CMD(0x31);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x53);
LCD_ILI9881C_INDEX(0x35);

LCD_ILI9881C_CMD(0x55);
LCD_ILI9881C_INDEX(0x50);
```

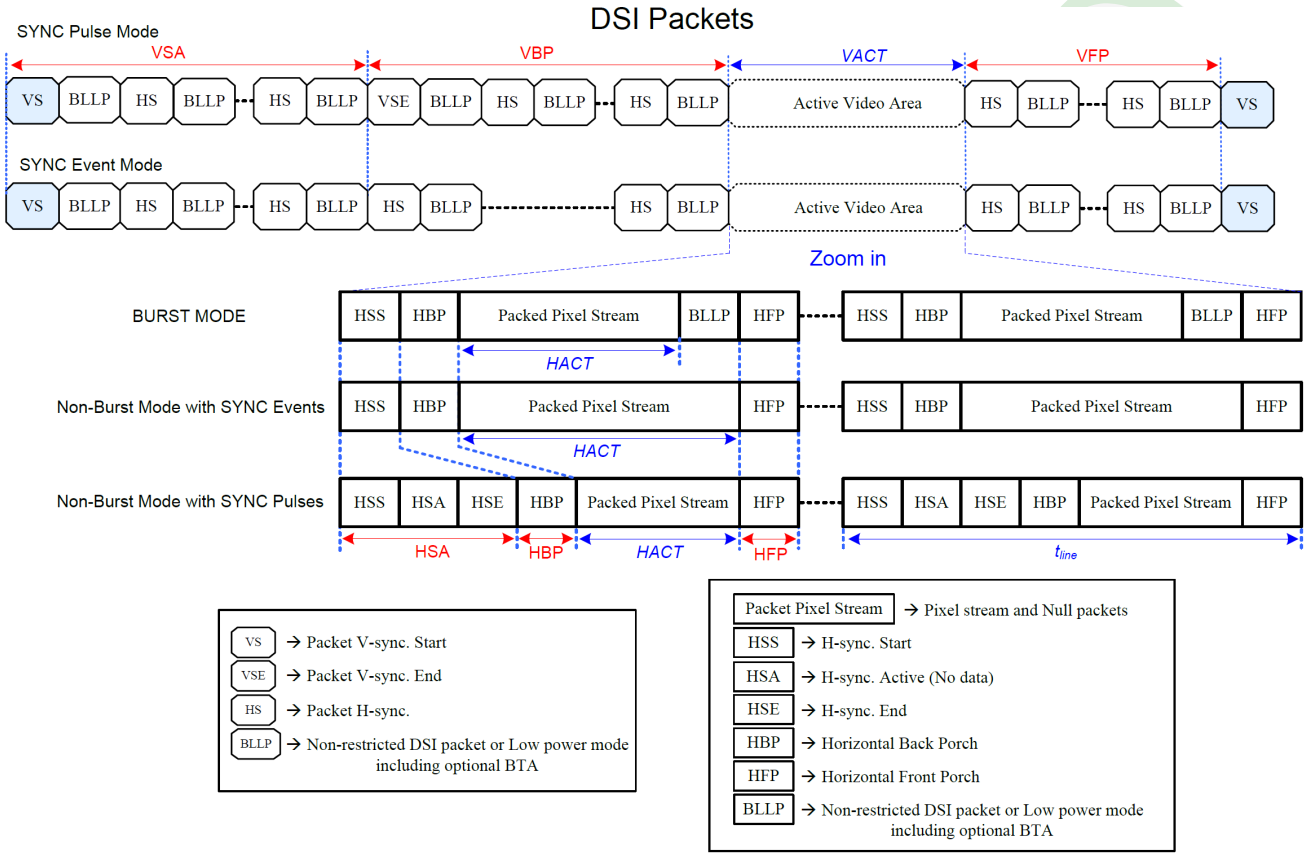
```
LCD_ILI9881C_CMD(0x50);
LCD_ILI9881C_INDEX(0xAF);

LCD_ILI9881C_CMD(0x51);
LCD_ILI9881C_INDEX(0xAF);

LCD_ILI9881C_CMD(0x60);
LCD_ILI9881C_INDEX(0x14);

LCD_ILI9881C_CMD(0xA0);
LCD_ILI9881C_INDEX(0x08);

LCD_ILI9881C_CMD(0xA1);
LCD_ILI9881C_INDEX(0x1D);

LCD_ILI9881C_CMD(0xA2);
LCD_ILI9881C_INDEX(0x2C);

LCD_ILI9881C_CMD(0xA3);
LCD_ILI9881C_INDEX(0x14);

LCD_ILI9881C_CMD(0xA4);
LCD_ILI9881C_INDEX(0x19);

LCD_ILI9881C_CMD(0xA5);
LCD_ILI9881C_INDEX(0x2E);

LCD_ILI9881C_CMD(0xA6);
LCD_ILI9881C_INDEX(0x22);

LCD_ILI9881C_CMD(0xA7);
LCD_ILI9881C_INDEX(0x23);

LCD_ILI9881C_CMD(0xA8);
LCD_ILI9881C_INDEX(0x97);

LCD_ILI9881C_CMD(0xA9);
LCD_ILI9881C_INDEX(0x1E);

LCD_ILI9881C_CMD(0xAA);
LCD_ILI9881C_INDEX(0x29);

LCD_ILI9881C_CMD(0xAB);
LCD_ILI9881C_INDEX(0x7B);

LCD_ILI9881C_CMD(0xAC);
LCD_ILI9881C_INDEX(0x18);

LCD_ILI9881C_CMD(0xAD);
LCD_ILI9881C_INDEX(0x17);

LCD_ILI9881C_CMD(0xAE);
LCD_ILI9881C_INDEX(0x4B);

LCD_ILI9881C_CMD(0xAF);
```

```
LCD_ILI9881C_INDEX(0x1F);

LCD_ILI9881C_CMD(0xB0);
LCD_ILI9881C_INDEX(0x27);

LCD_ILI9881C_CMD(0xB1);
LCD_ILI9881C_INDEX(0x52);

LCD_ILI9881C_CMD(0xB2);
LCD_ILI9881C_INDEX(0x63);

LCD_ILI9881C_CMD(0xB3);
LCD_ILI9881C_INDEX(0x39);

LCD_ILI9881C_CMD(0xC0);
LCD_ILI9881C_INDEX(0x08);

LCD_ILI9881C_CMD(0xC1);
LCD_ILI9881C_INDEX(0x1D);

LCD_ILI9881C_CMD(0xC2);
LCD_ILI9881C_INDEX(0x2C);

LCD_ILI9881C_CMD(0xC3);
LCD_ILI9881C_INDEX(0x14);

LCD_ILI9881C_CMD(0xC4);
LCD_ILI9881C_INDEX(0x19);

LCD_ILI9881C_CMD(0xC5);
LCD_ILI9881C_INDEX(0x2E);

LCD_ILI9881C_CMD(0xC6);
LCD_ILI9881C_INDEX(0x22);

LCD_ILI9881C_CMD(0xC7);
LCD_ILI9881C_INDEX(0x23);

LCD_ILI9881C_CMD(0xC8);
LCD_ILI9881C_INDEX(0x97);

LCD_ILI9881C_CMD(0xC9);
LCD_ILI9881C_INDEX(0x1E);

LCD_ILI9881C_CMD(0xCA);
LCD_ILI9881C_INDEX(0x29);

LCD_ILI9881C_CMD(0xCB);
LCD_ILI9881C_INDEX(0x7B);

LCD_ILI9881C_CMD(0xCC);
LCD_ILI9881C_INDEX(0x18);

LCD_ILI9881C_CMD(0xCD);
LCD_ILI9881C_INDEX(0x17);
```

```
LCD_ILI9881C_CMD(0xCE);
LCD_ILI9881C_INDEX(0x4B);

LCD_ILI9881C_CMD(0xCF);
LCD_ILI9881C_INDEX(0x1F);

LCD_ILI9881C_CMD(0xD0);
LCD_ILI9881C_INDEX(0x27);

LCD_ILI9881C_CMD(0xD1);
LCD_ILI9881C_INDEX(0x52);

LCD_ILI9881C_CMD(0xD2);
LCD_ILI9881C_INDEX(0x63);

LCD_ILI9881C_CMD(0xD3);
LCD_ILI9881C_INDEX(0x39);

LCD_ILI9881C_CMD(0xFF);
LCD_ILI9881C_INDEX(0x98);
LCD_ILI9881C_INDEX(0x81);
LCD_ILI9881C_INDEX(0x00);

LCD_ILI9881C_CMD(0x35);

Void ILI9881C_EnterSleep_Code(Void)
{
LCD_ILI9881C_CMD(0x28) //Display oFF
Delayms(20);
LCD_ILI9881C_CMD(0x10); // Internal oscillator will be stopped
Delayms(120);
}
Void ILI9881C_ExitSleep_Code(Void)
{
LCD_ILI9881C_CMD(0x11); // Sleep Out
Delayms(120);
LCD_ILI9881C_CMD(0x29) //Display on
Delayms(20);
}
```

2.3 AC Electrical Characteristics

2.3.1 Timing for DSI video mode



Parameters	Symbols	Min.	Typ.	Max.	Units
Vertical sync. active	VSA	2 (Note 6)	-	-	Line
Vertical Back Porch	VBP	14 (Note 6)	-	-	Line
Vertical Front Porch	VFP	8 (Note 6)	-	-	Line
Active lines per frame	VACT	-	1280	-	Line
Horizontal sync. active	HSA	2	-	-	Pixel
Horizontal Porch period	HSA + HBP + HFP	1.6	-	-	us
Active pixels per line	HACT	-	720	-	Pixel
Bit rate	BR_{bps}	385		Note 5	Mbps/lane

1 UI=1/Bit rate

HSA(pixel)= (tHSA*lane number) / (UI* pixel format)

HBP(pixel)= (tHBP*lane number) / (UI* pixel format)

HFP(pixel)= (tHFP*lane number) / (UI* pixel format)

$$\text{Frame Rate} = \frac{BR_{bps} \times Lane_{num}}{(VACT+VSA+VBP+VFP) \times (HACT+HSA+HBP+HFP) \times \text{Pixel Format}}$$

Example : BR_{bps} = 457Mbps/lane, 1UI=2.1883ns, Frame rate=60Hz, VACT=1280, VSA=2, VBP=30, VFP=20, HACT=720, HSA=33, HBP=100, HFP=100, $Lane_{num}$=4(lane), Pixel Format=24(bit).

Note:

1. $Lane_{num}$: Date lane of MIPI-DSI.
2. Pixel Format: Please reference to “4.1DSI System Interface”.
3. The formula exists slightly error because of the host-transmission way.
4. The best frame rate setting： 2 data lanes：50~60 Hz / 3 data lanes：50~70 Hz / 4 data lanes：50~70 Hz.
5. Please reference to “Table 39: Limited Clock Channel Speed”.
6. The minimum values of this table mean the limitation of IC without considering the panel GIP. The actual values of VSA, VBP and VFP will be changed by different panel GIP setting.

2.3.2 Reset Timing

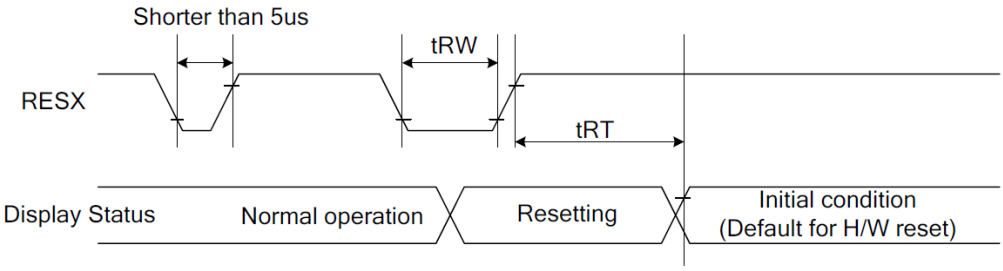


Figure 124: Reset Timing

Table 47: Reset Timing

Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5)	mS
				120 (note 1,6,7)	mS

Notes:

1. *The reset cancel also includes required time for loading ID bytes, VCOM setting and other settings from EEPROM to registers. This loading is done every time when there is H/W reset cancel time (tRT) within 5 ms after a rising edge of RESX.*
2. *Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the Table 48.*

Table 48: Reset Descript

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 10us	Reset
Between 5us and 10us	Reset starts

3. During the Resetting period, the display will be blanked (The display enters the blanking sequence, which maximum time is 120 ms, when Reset Starts in the Sleep Out mode. The display remains the blank state in the Sleep In mode.) and then return to Default condition for Hardware Reset.
4. Spike Rejection can also be applied during a valid reset pulse, as shown below:

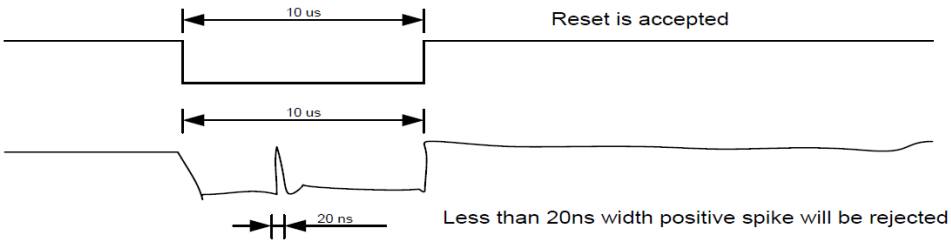
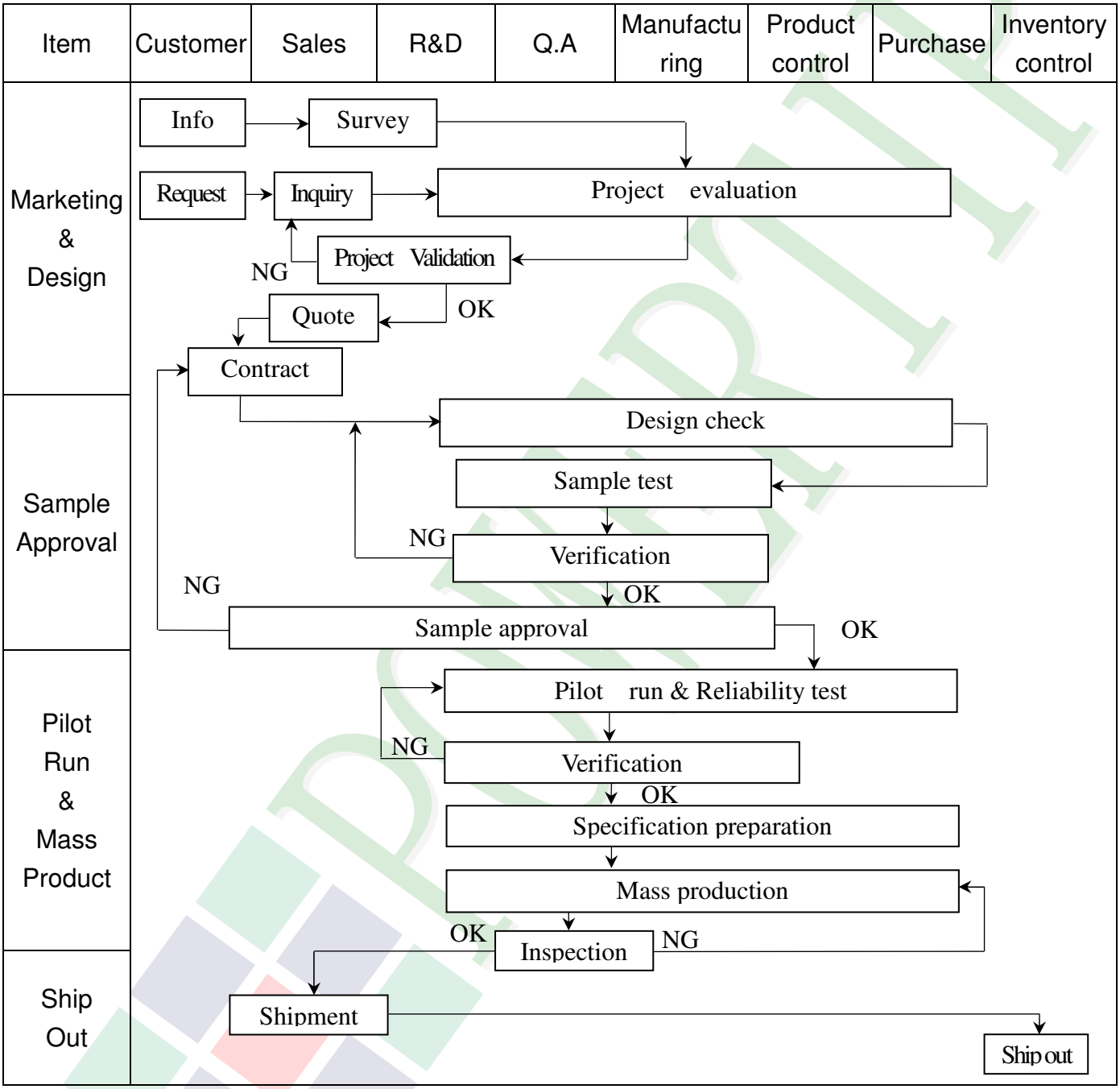


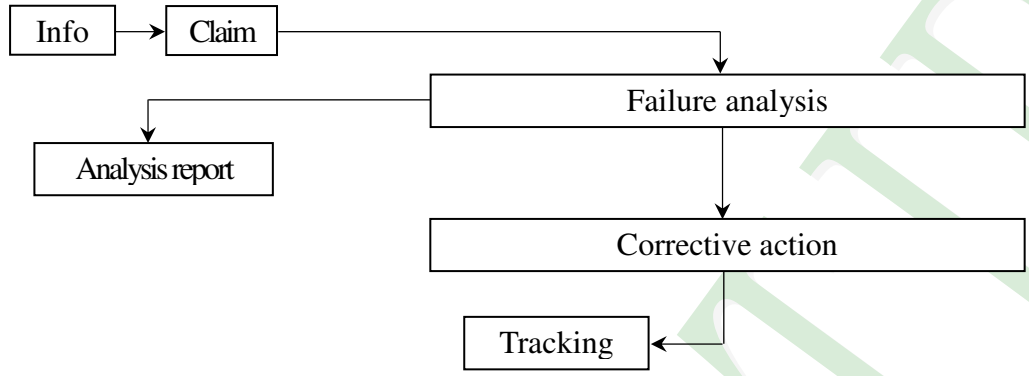
Figure 125: Positive Noise Pulse during Reset Low

5. When Reset applied during Sleep In Mode.
6. When Reset applied during Sleep Out Mode.
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2. Inspection Specification

◆Scope：The document shall be applied to TFT-LCD Module for 3.5″ ~15″ (Ver.B01).

◆Inspection Standard：MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment：Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level：Major Defect AQL： 0.4 ； Minor Defect AQL： 1.5

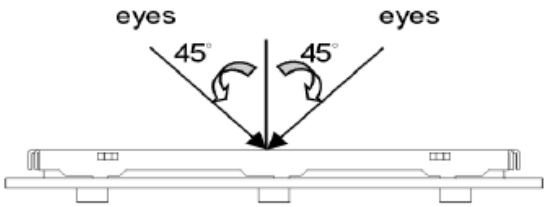
◆OUT Going Defect Level：Sampling.

◆Standard of the product appearance test：

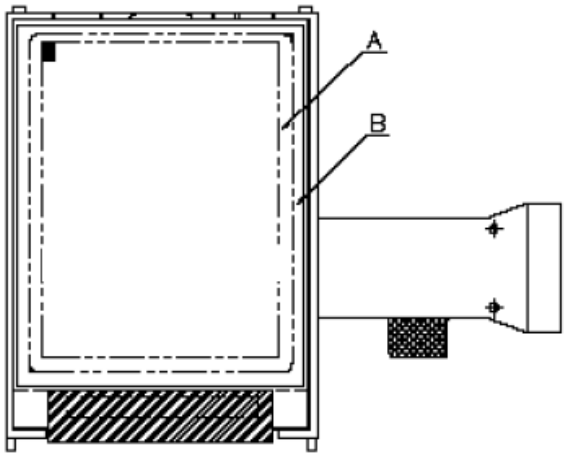
a. Manner of appearance test：

(1). The test best be under 20W×2 fluorescent light，and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection： (Unit：mm)

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

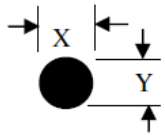
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="3">01</td><td rowspan="3">Product condition</td><td>1.1 The part number is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>1.2 Mixed product types.</td><td>Major</td></tr>
<tr><td>1.3 Assembled in inverse direction.</td><td>Major</td></tr>
<tr><td>02</td><td>Quantity</td><td>2.1 The quantity is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>03</td><td>Outline dimension</td><td>3.1 Product dimension and structure must conform to structure diagram.</td><td>Major</td></tr>
<tr><td rowspan="6">04</td><td rowspan="6">Electrical Testing</td><td>4.1 Missing line character and icon.</td><td>Major</td></tr>
<tr><td>4.2 No function or no display.</td><td>Major</td></tr>
<tr><td>4.3 Display malfunction.</td><td>Major</td></tr>
<tr><td>4.4 LCD viewing angle defect.</td><td>Major</td></tr>
<tr><td>4.5 Current consumption exceeds product specifications.</td><td>Major</td></tr>
<tr><td>4.6 Mura can not be seen through 5% ND filter.
(Mura : Under the normal examination angle of view,the picture has the non-uniform phenomenon.)</td><td>Minor</td></tr>
<tr><td>05</td><td>Dot defect

(Bright dot、Dark dot)

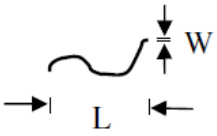
On -display</td><td>
<table>
<tr><th colspan="2">Item</th><th>Acceptance (Q'ty)</th></tr>
<tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≦ 4</td></tr>
<tr><td>Dark Dot</td><td>≦ 5</td></tr>
<tr><td>Joint Dot</td><td>≦ 3</td></tr>
<tr><td>Total</td><td>≦ 7</td></tr>
</table>
5.1 Inspection pattern：full white , full black , Red , Green and blue screens.
5.2 It is defined as dot defect if defect area ＞1/2 dot.
5.3 The distance between two dot defect ≧5 mm.
5.4 Bright dot that can not be seen through 5% ND filter.
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

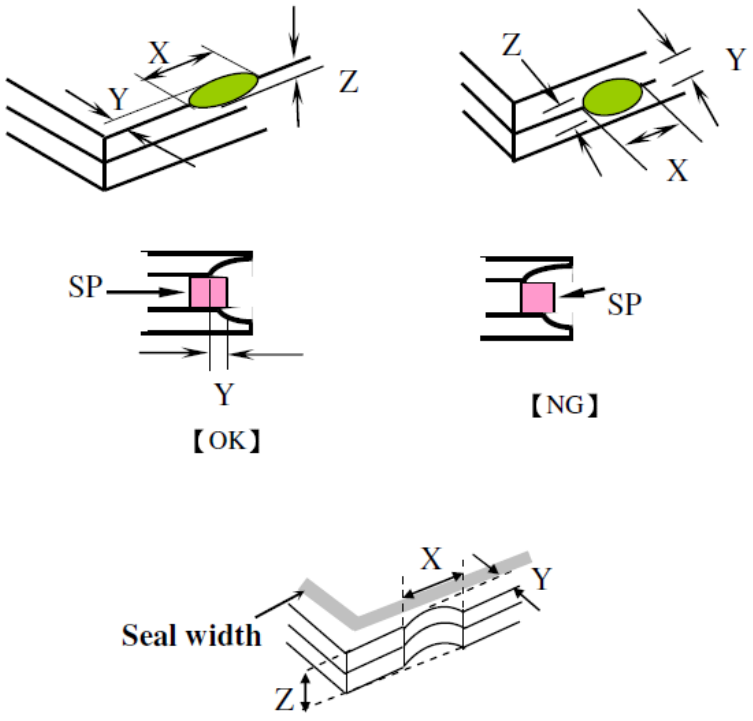
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>06</td><td>Black or white dot、scratch、contamination

Round type
 

Φ=(x+y) / 2

Line type
 
</td><td>6.1 Round type (Non-display or display) :
<table><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>5</td></tr><tr><td>Φ > 0.50</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table>
6.2 Line type(Non-display or display) :
<table><tr><th rowspan="2">module size</th><th rowspan="2">Length (L)</th><th rowspan="2">Width (W)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td rowspan="5">3.5" to less 9"</td><td>---</td><td>W ≦ 0.03</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>L ≦10.0</td><td>0.03 <W ≦ 0.05</td><td>4</td></tr><tr><td>L ≦5.0</td><td>0.05 <W ≦ 0.10</td><td>2</td></tr><tr><td>---</td><td>W >0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr><tr><td rowspan="5">9" to 15"</td><td>---</td><td>W ≦ 0.05</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>L ≦10.0</td><td>0.05 <W ≦ 0.10</td><td>5</td></tr><tr><td>---</td><td>W >0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
<tr><td>07</td><td>Polarizer Bubble</td><td><table><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>4</td></tr><tr><td>0.50 < Φ ≦ 0.80</td><td>1</td></tr><tr><td>Φ > 0.80</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

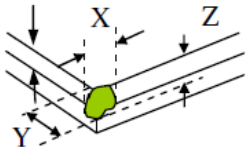
NO	Item	Criterion	Level
08	The crack of glass	Symbols : X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length 8.1 General glass chip : 8.1.1 Chip on panel surface and crack between panels: 	Minor

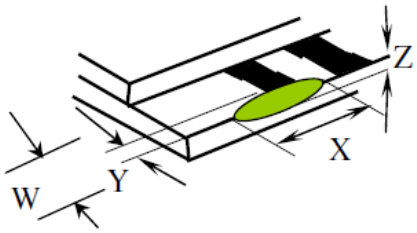
X	Y	Z
≦ a	Crack can't enter viewing area	≦1/2 t
≦ a	Crack can't exceed the half of SP width.	1/2 t < Z ≦2 t

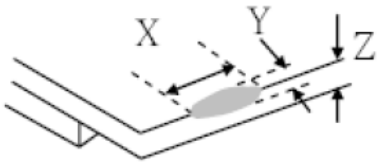
◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

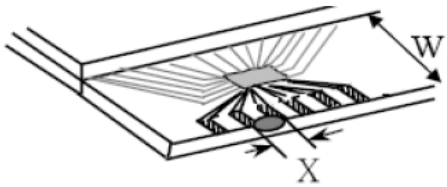
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="2">08</td><td rowspan="2">The crack of glass</td><td>
Symbols :

X : The length of crack　　Y : The width of crack.
Z : The thickness of crack　　W : terminal length
t : The thickness of glass　　a : LCD side length

8.1.2 Corner crack :


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5 a</td><td>Crack can't enter viewing area</td><td>Z ≦ 1/2 t</td></tr>
<tr><td>≦1/5 a</td><td>Crack can't exceed the half of SP width.</td><td>1/2 t < Z ≦ 2 t</td></tr>
</table>
</td><td rowspan="2">Minor</td></tr>
<tr><td>
8.2 Protrusion over terminal :
8.2.1 Chip on electrode pad :




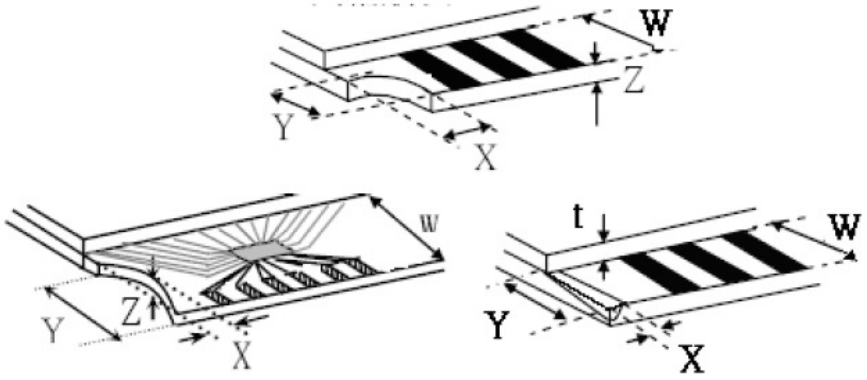


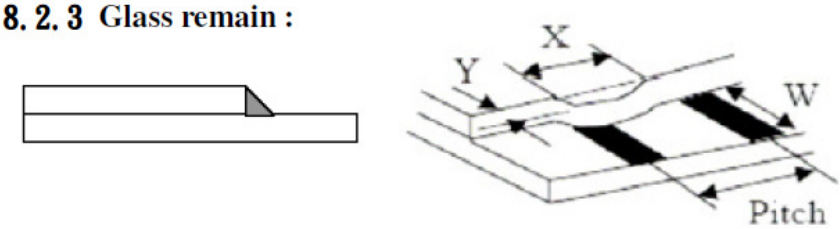
<table>
<tr><th></th><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>Front</td><td>≦ a</td><td>≦ 1/2 W</td><td>≦ t</td></tr>
<tr><td>Back</td><td>≦ a</td><td>≦ W</td><td>≦ 1/2 t</td></tr>
</table>
</td></tr>
</table>

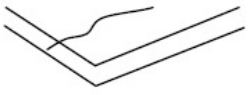
◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>08</td><td>The crack of glass</td><td>
Symbols :

X : The length of crack Y : The width of crack.
Z : The thickness of crack W : terminal length
t : The thickness of glass a : LCD side length

8.2.2 Non-conductive portion :


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ 1/3 a</td><td>≦ W</td><td>≦t</td></tr>
</table>
⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.
8.2.3 Glass remain :


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ a</td><td>≦ 1/3 W</td><td>≦t</td></tr>
</table>
8.2.4 Cracking

Not Allowed
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type、quantity、dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC .	Major
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≦1.5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

<table>
<tr><th>NO.</th><th>TEST ITEM</th><th>TEST CONDITION</th></tr>
<tr><td>1</td><td>High Temperature
Storage Test</td><td>Keep in +80 ±2℃ 240 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>2</td><td>Low Temperature
Storage Test</td><td>Keep in -30 ±2℃ 240 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>3</td><td>High Temperature /
High Humidity
Storage Test</td><td>Keep in +60℃ / 90% R.H duration for 240 hrs
Surrounding temperature, then storage at normal condition 4hrs.
(Excluding the polarizer)</td></tr>
<tr><td>4</td><td>Temperature Cycling
Storage Test</td><td>-30℃ → +25℃ → +80℃ → +25℃
(30mins) (5mins) (30mins) (5mins)
10 Cycle
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td rowspan="2">5</td><td rowspan="2">ESD Test</td><td>Air Discharge:
Apply 2 KV with 5 times
Discharge for each polarity +/-
Contact Discharge:
Apply 250 V with 5 times
discharge for each polarity +/-</td></tr>
<tr><td>1. Temperature ambiance : 15℃～35℃
2. Humidity relative : 30%～60%
3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%
4. Discharge Resistance(Rd) : 330Ω±10%
5. Discharge, mode of operation :
Single Discharge (time between successive discharges at least 1 sec)
(Tolerance if the output voltage indication : ±5%)</td></tr>
<tr><td>6</td><td>Vibration Test
(Packaged)</td><td>1. Sine wave 10～55 Hz frequency (1 min)
2. The amplitude of vibration :1.5 mm
3. Each direction (X、Y、Z) duration for 2 Hrs</td></tr>
<tr><td>7</td><td>Drop Test
(Packaged)</td><td><table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table>Drop direction :※1 corner / 3 edges / 6 sides each 1times</td></tr>
</table>

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands , this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320±10℃and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25℃ ±5℃ and the humidity is below 65% RH.

5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush , shake , or jolt the module.

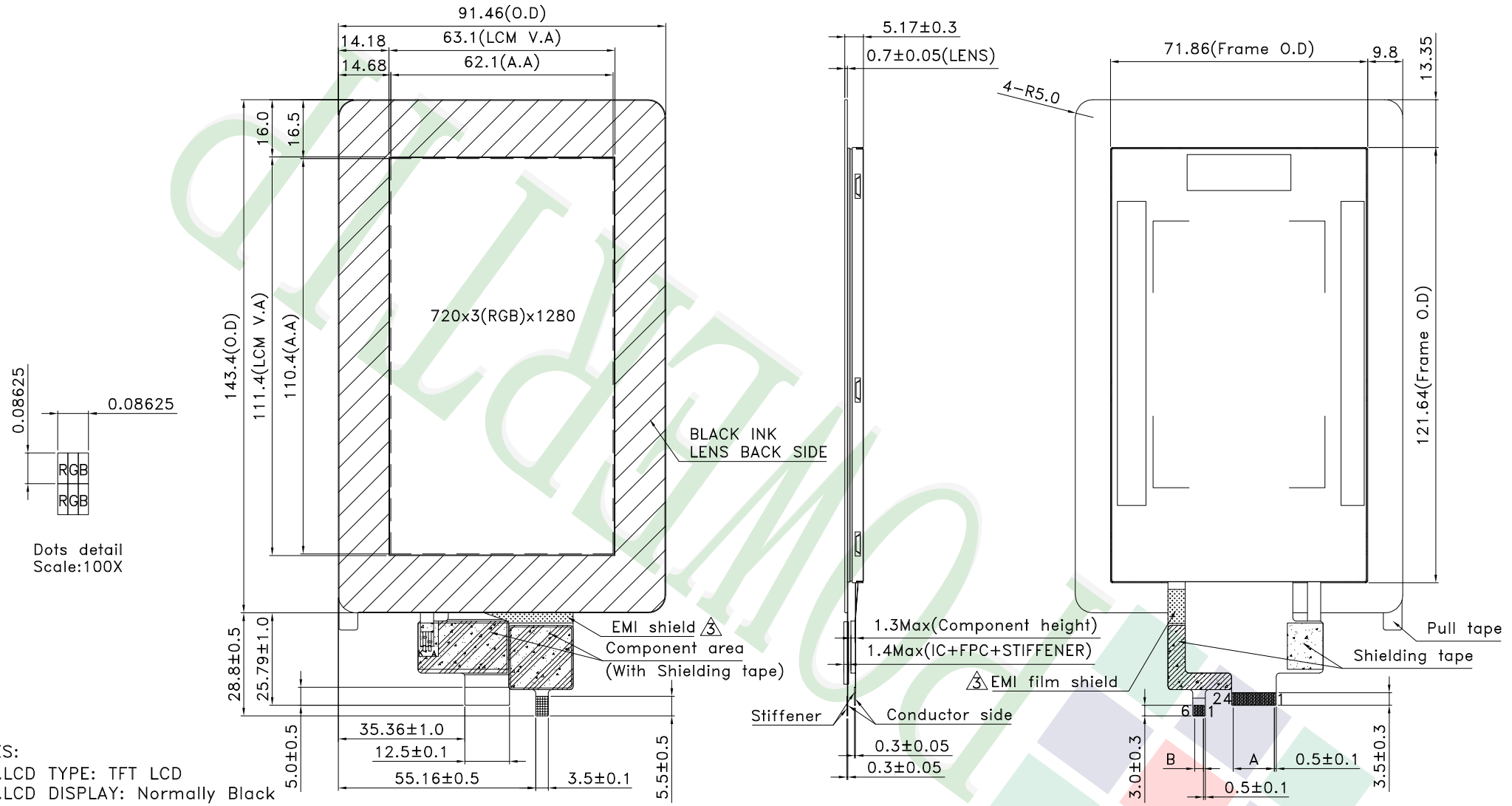
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



NOTES:

1. LCD TYPE: TFT LCD
2. LCD DISPLAY: Normally Black
3. The tolerance unless classified ±0.3mm
4. A: P0.5*23=11.5±0.05,W=0.35±0.05
   B: P0.5*5=2.5±0.05,W=0.35±0.05
5. T/P suggested connector: "CviLux" CF31061D0R2-05-NH OR EQUIVALENT
6. TFT suggested connector: "CHYAO SHIUNN" JS-1191DR1-24 OR EQUIVALENT

REV	REV BY	REVISER	DATE
007			
006			
005			
004			
003	T/P FPC with EMI shield	Eva	2017/12/22
002	Add Shielding tape	Eva	2017/07/21
001	NEW DRAWING	Eva	2017/06/08

PART NO: PH720128T004-ZBC02

DRAWING NAME: LMD-PH720128T004-ZBC02

TITLE: LCD MODULE DRAWING

久正光電股份有限公司
POWERTIP TECHNOLOGY CORPORATION

Design	Eva Liao	(3)	Surface		
Check	Tina Chen	Unit	MM	Material	
Approve	Jimmy Chen	Scale	1:1	Thickness	
		Page	1/1	Quantity	

Length(Min) (mm) \ Tolerance \ Precision	Precision Level
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

Ver.001

Documents NO.	PKG-PH720128T004-ZBC02

LCM包裝規格書
LCM Packaging Specifications
(For Tray)

Approve	Check	Contact
Jimmy	Tina	Eva

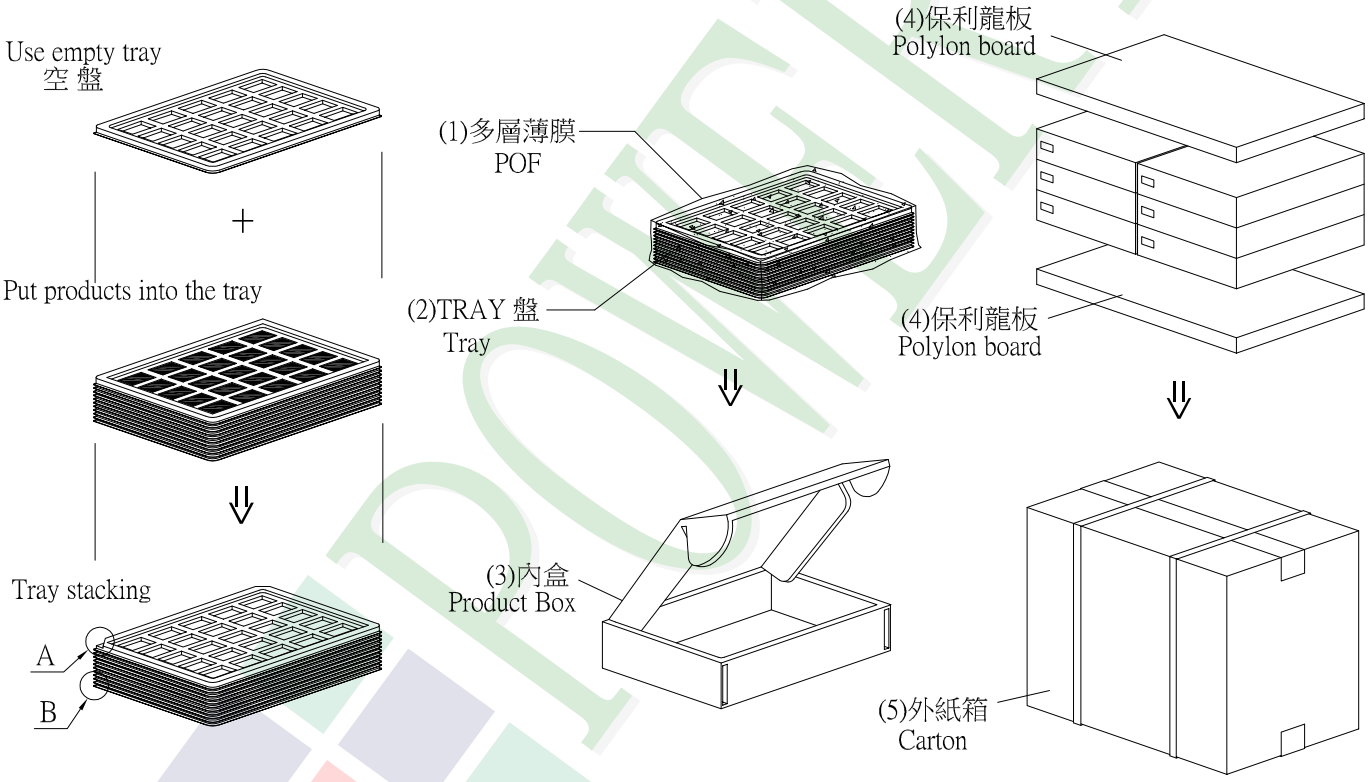
1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH720128T004-ZBC02	143.4X91.46X5.17	0.094	120	11.28
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	——	6	——
3	TRAY 盤 (2)Tray	TYSG000000135	352 X 260 X 14.2	0.099	36	3.564
4	內盒(3)Product Box	BX36627063ABBA	383 X 270 X 66	0.182	6	1.092
5	保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1	1.0
7						
8						
9						

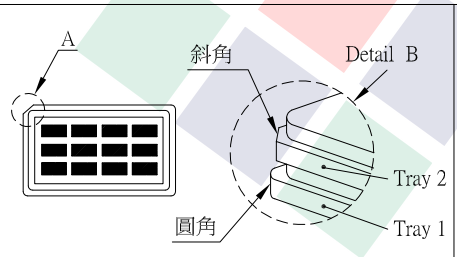
2.一 整箱總重量 (Total LCD Weight in carton) : 16.99 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

(1)LCM quantity per box : no per tray	4	x no of tray	5	=	20
(2)Total LCM quantity in carton : quantity per box	20	x no of boxes	6	=	120



特 記 事 項 (REMARK)



4.TRAY盤相疊時,需旋轉180度,請詳見B視圖
Rotate tray 180 degrees and place on top of stack.
Check the tray stack using Fig. B.