

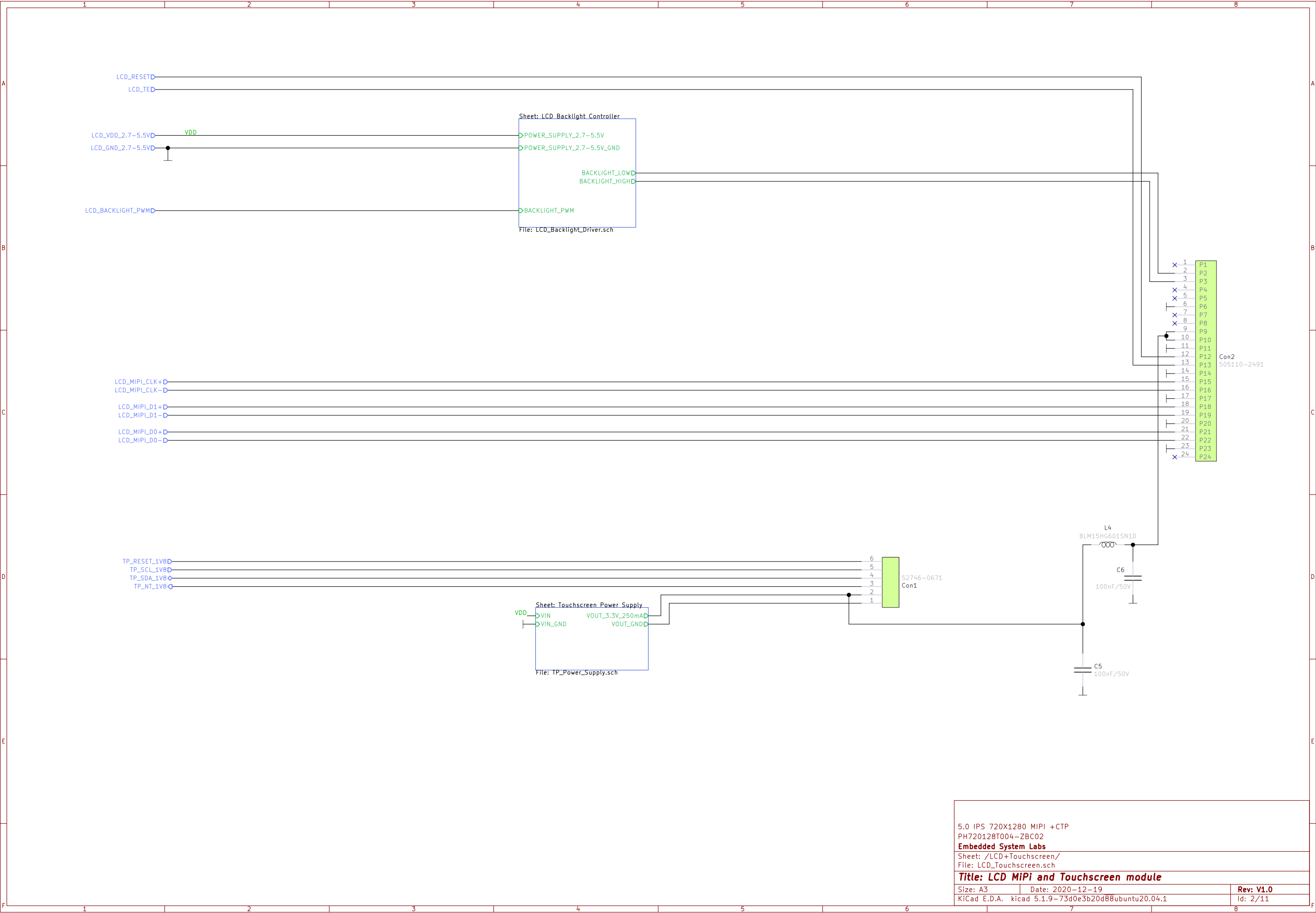
# Embedded System Labs

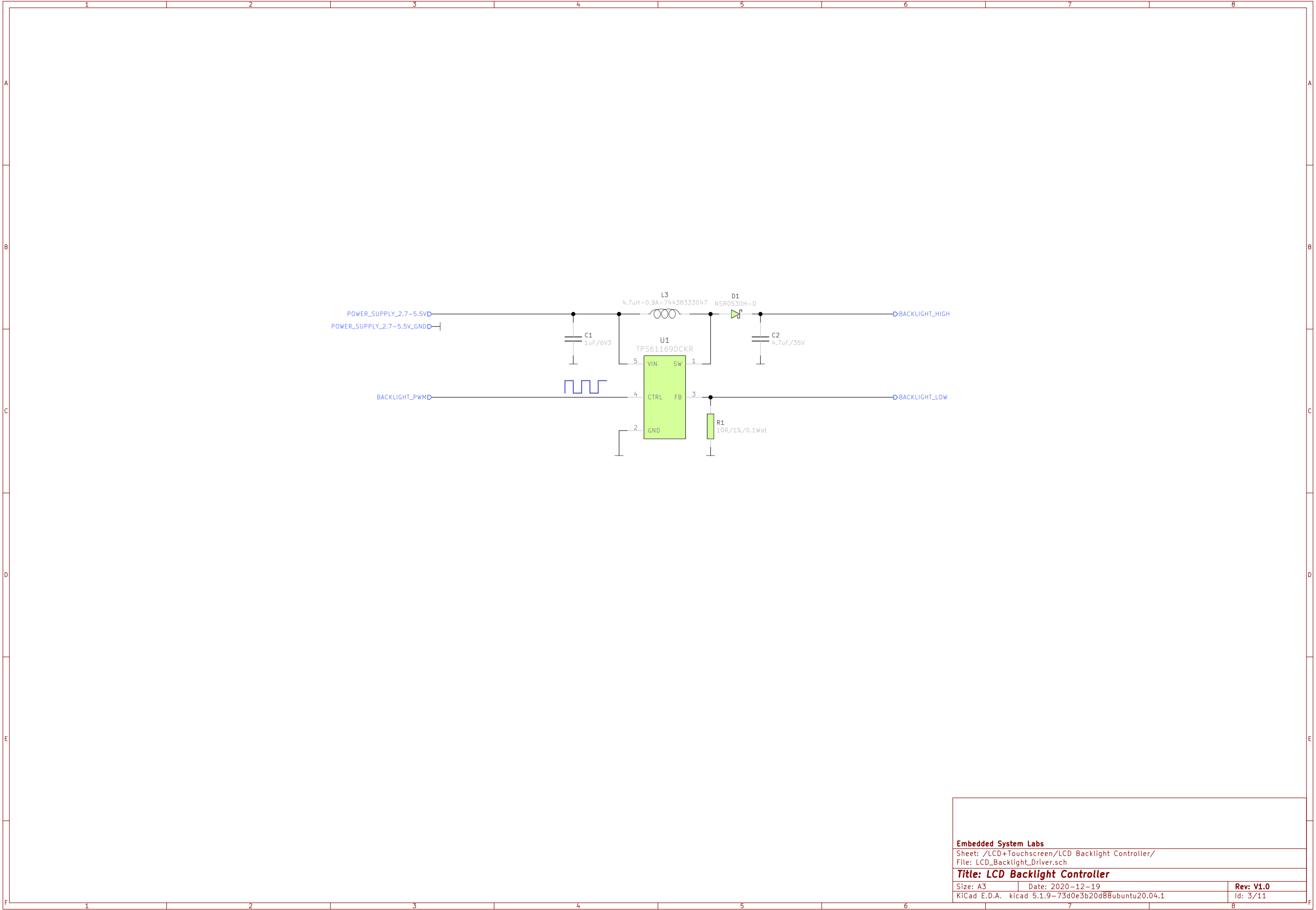
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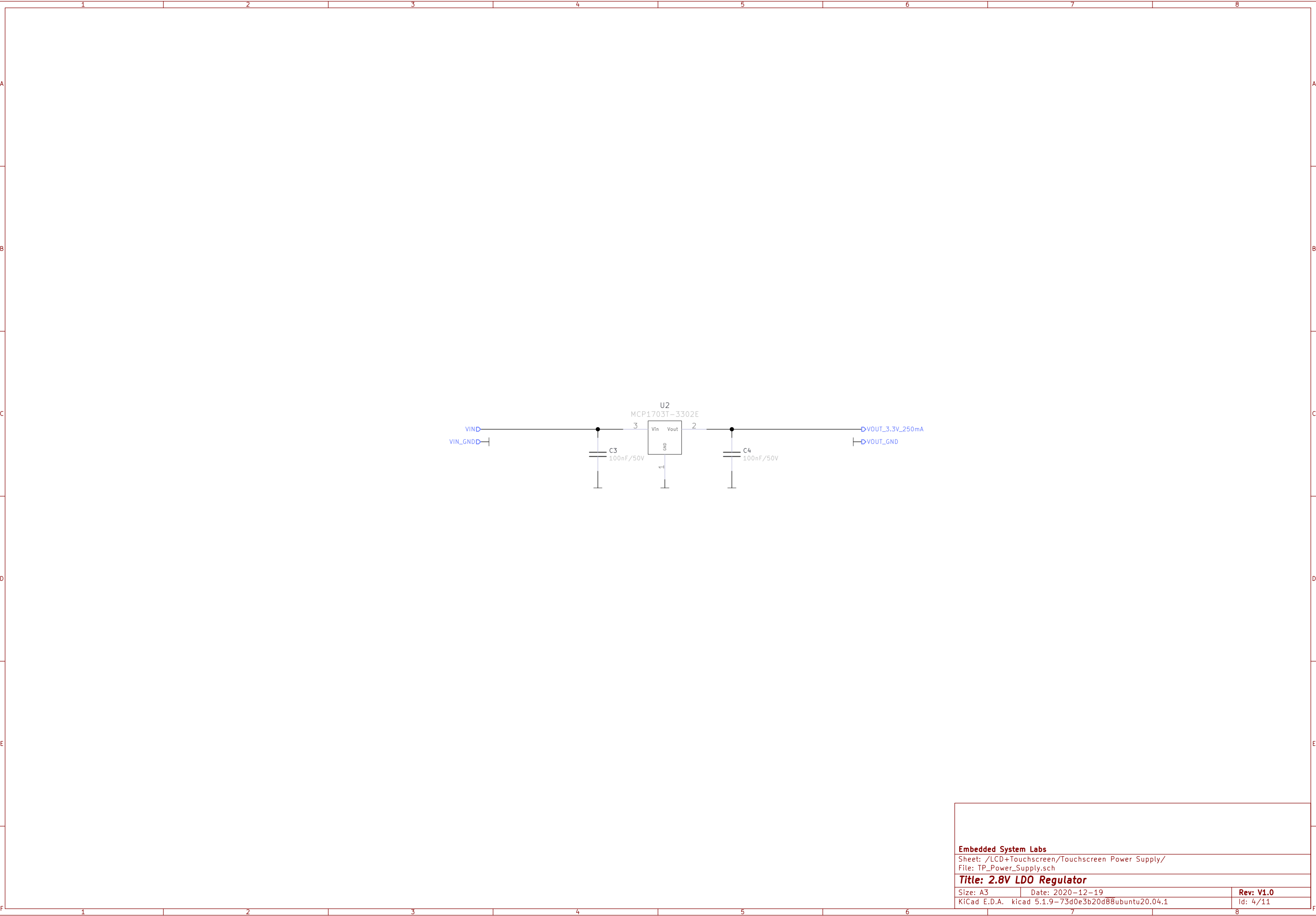
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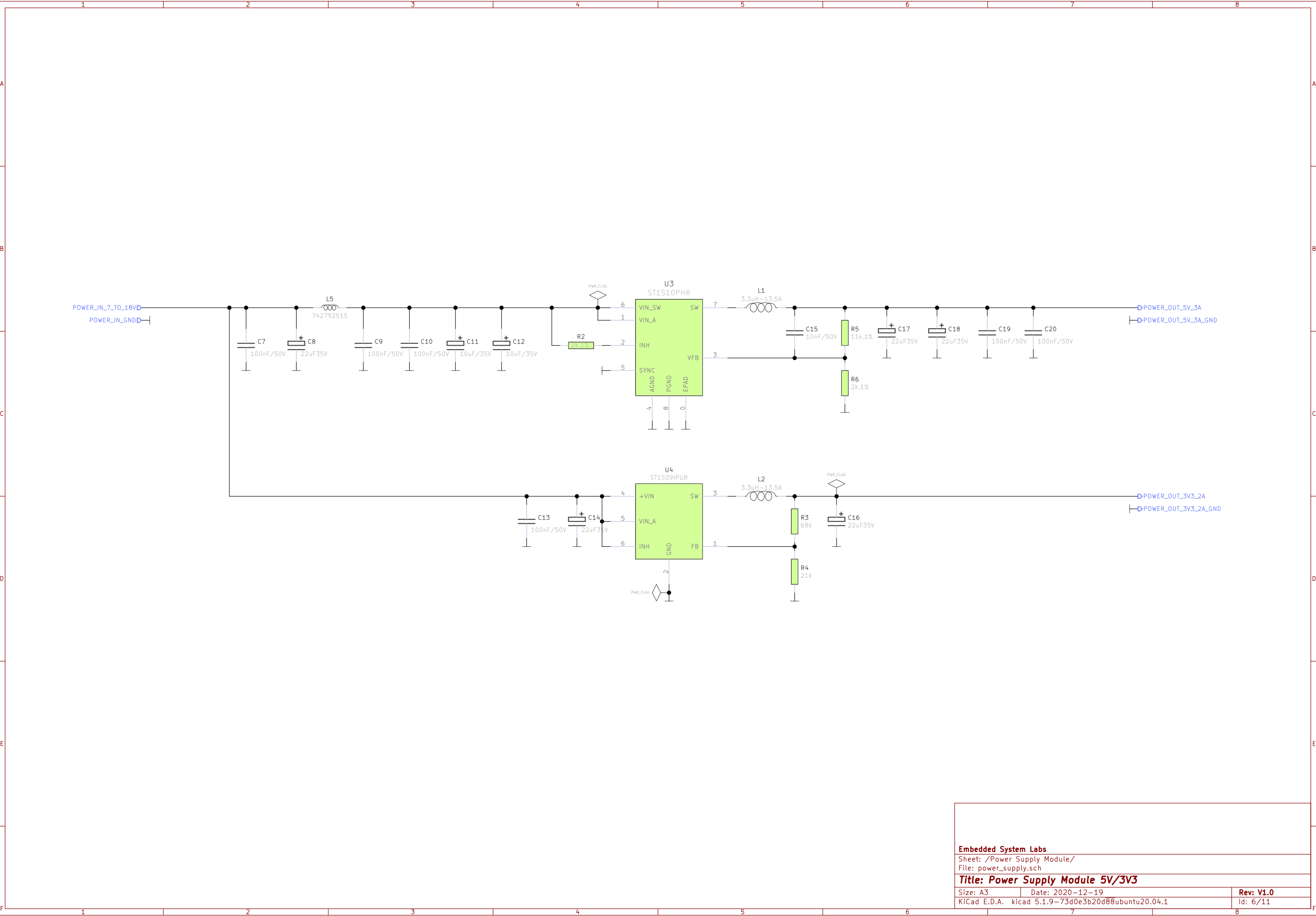
Rev: V1.0  
Id: 1/11

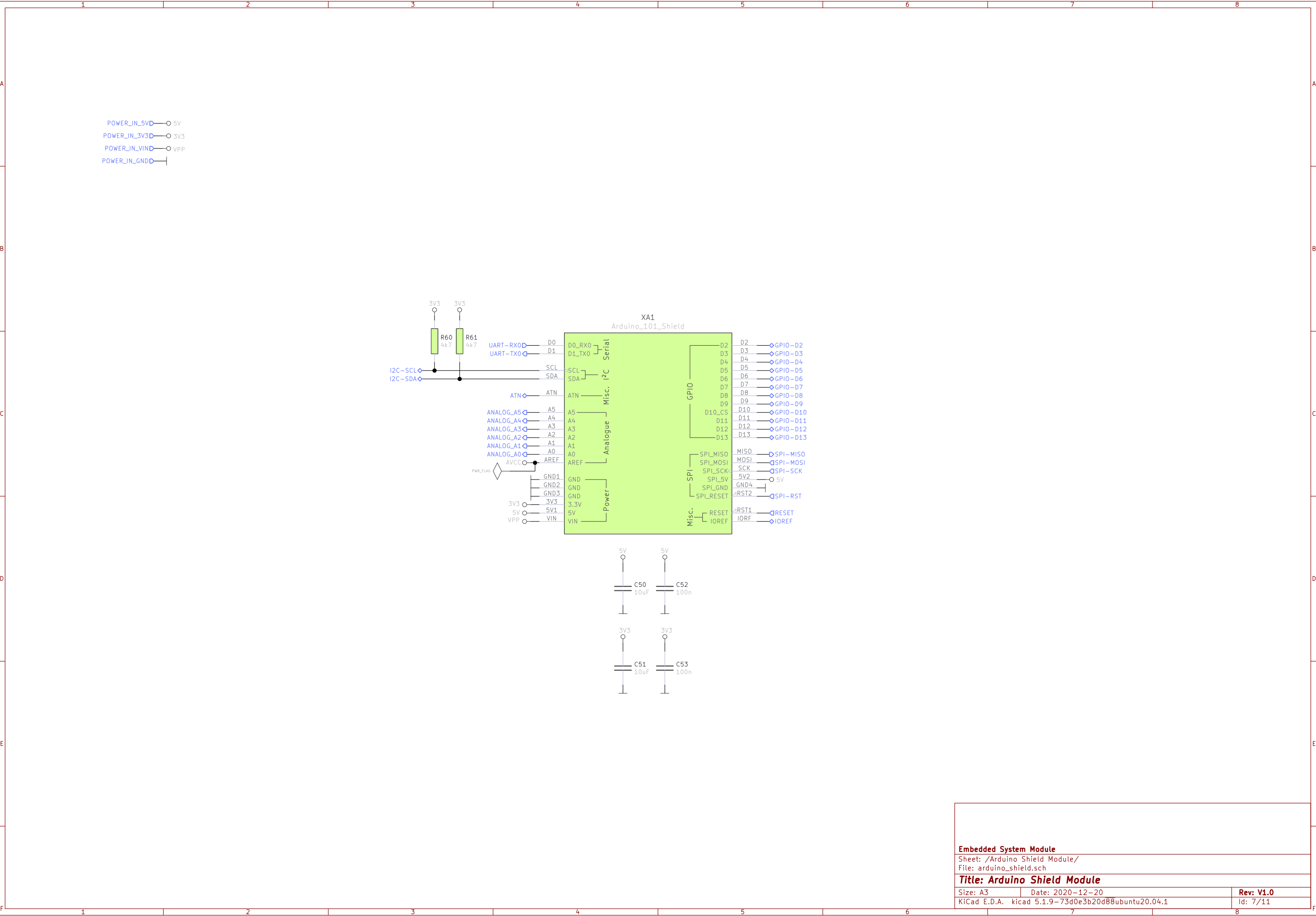




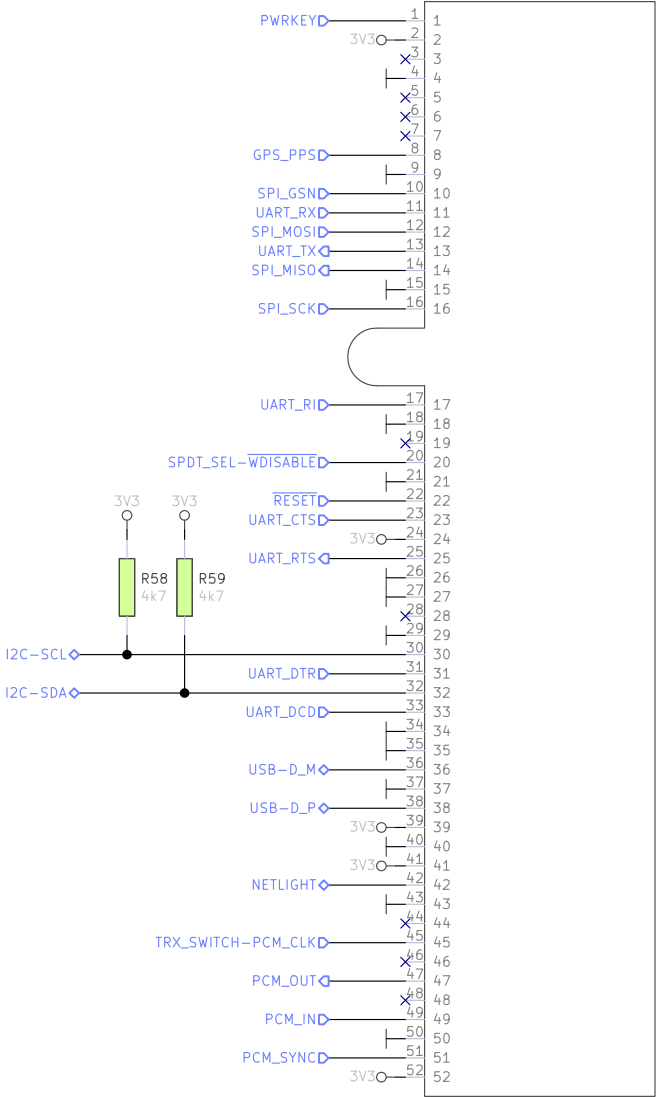






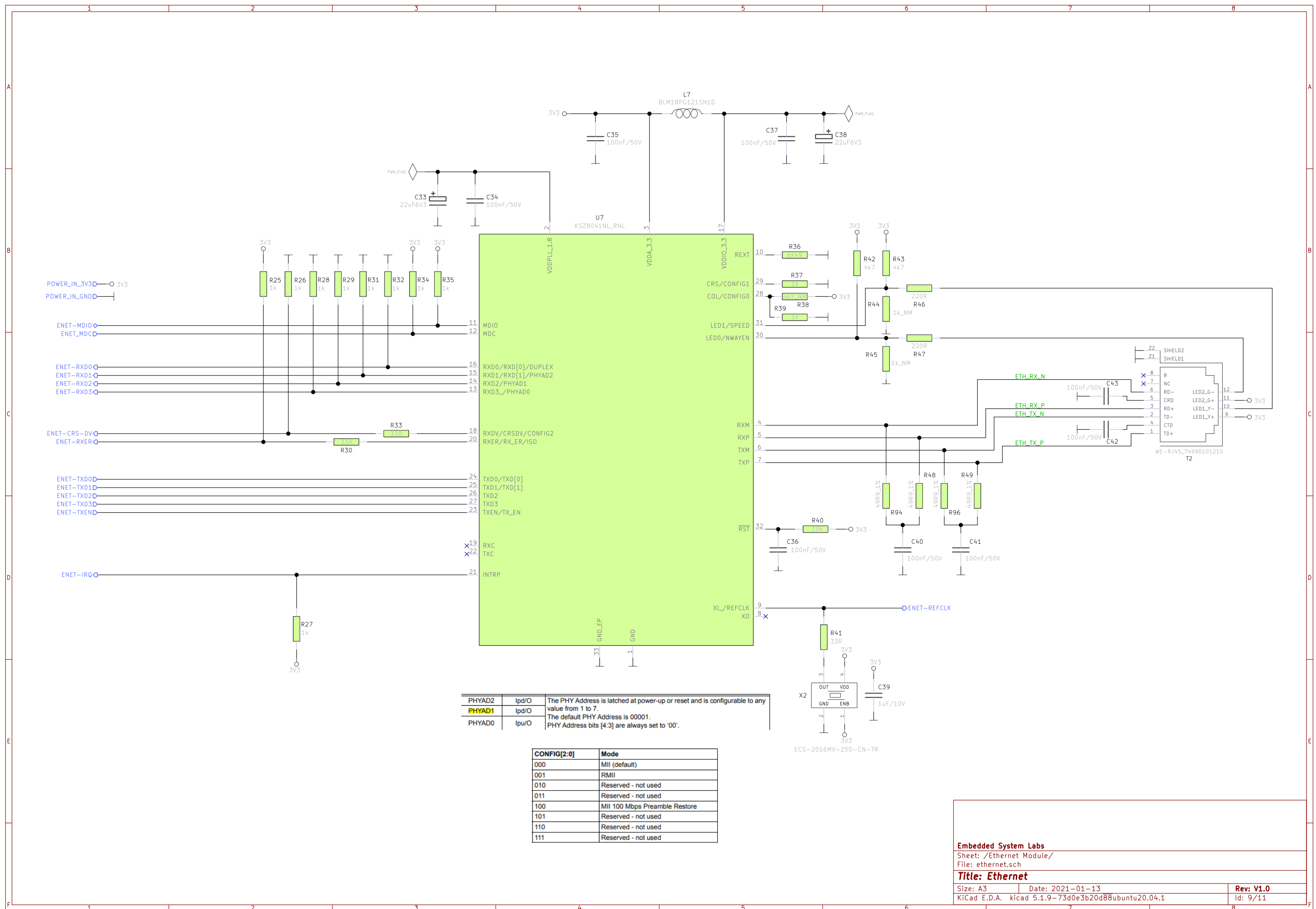


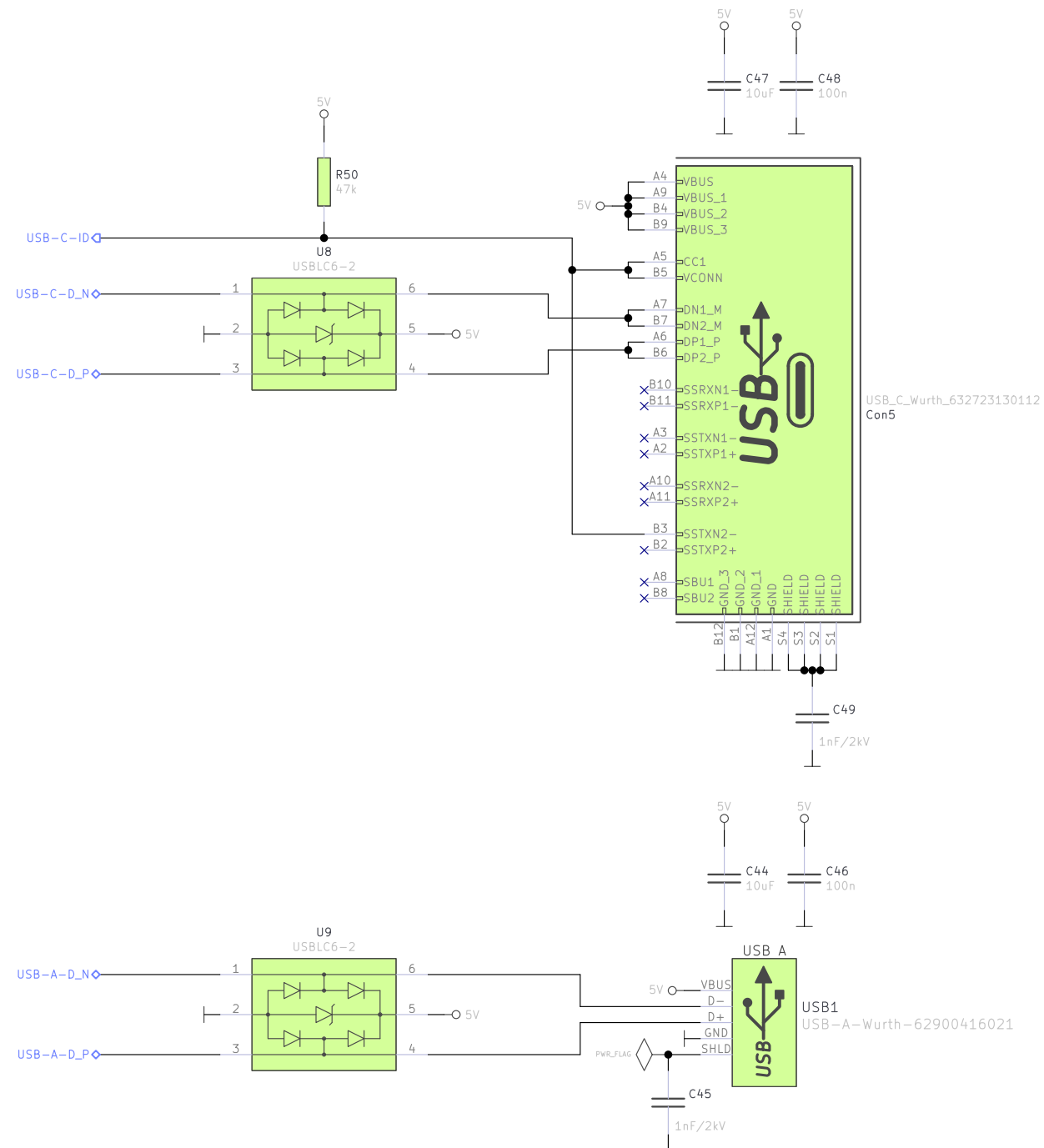
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POWER\_IN\_GND

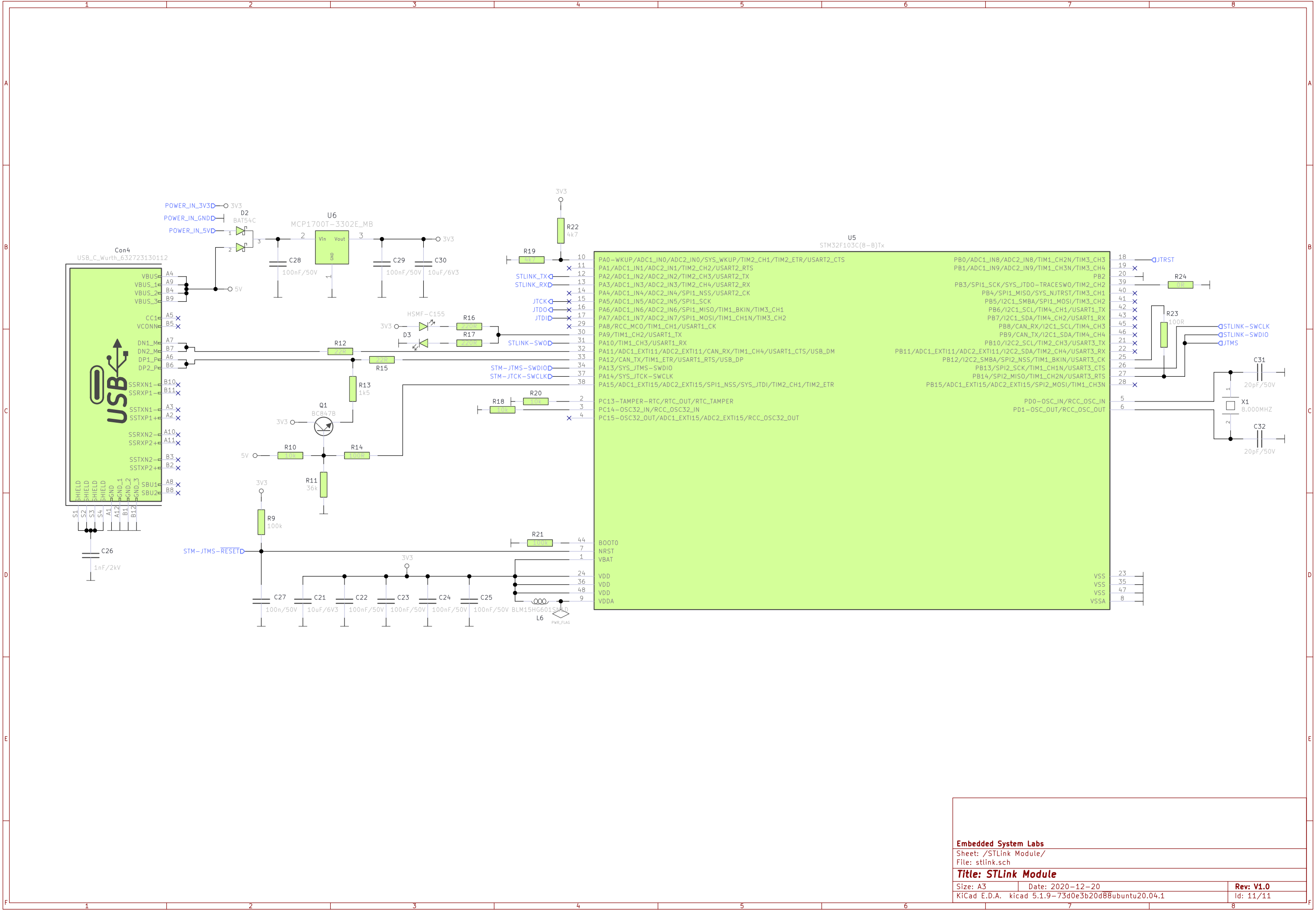


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**Embedded System Labs**

Sheet: /STLink Module/  
File: stlink.sch

**Title: STLink Module**

Size: A3	Date: 2020-12-20	Rev: V1.0
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