



**Partner:QSC-QSYS**  
**Model:Qsys Cores**  
**Device Type:DSP**

## GENERAL INFORMATION

<b>SIMPLWINDOWS NAME:</b>	QSYS Control-[Snapshot] V3.0
<b>CATEGORY:</b>	QSYS V3
<b>VERSION:</b>	V3.0
<b>SUMMARY:</b>	This Module is used for controlling a Snapshot component as dictated in the QSYS Designer Software.
<b>GENERAL NOTES:</b>	<p>This Module provides control and feedback of a "Snapshot" component in the QSYS Designer software. This module is specifically designed to be used on a Snapshot component in the QSYS design.</p> <p>Must use with QSYS Core module.</p> <p>A "Snapshot" component must be created in QSYS Designer software.</p>
<b>CRESTRON HARDWARE REQUIRED:</b>	Ethernet Card
<b>SETUP OF CRESTRON HARDWARE:</b>	NA
<b>VENDOR FIRMWARE:</b>	Unknown
<b>VENDOR SETUP:</b>	Setup of QSYS Design file, a Snapshot must be created in the QSYS Design software. (See QSYS help files for more info).

## CONTROL:

<b>Initialize_From_Core</b>	D	Pulse to Initialize control to the QSYS Core component. Should not be driven high until QSYS core has successfully initialized. Usually driven from "Initialize_Control_Modules" signal from respective Core module.
<b>[Save_Snapshot]</b>	D	[Optional] Value to save current Snapshot values. 1d = snapshot bank 1.
<b>[Recall_Snapshot]</b>	A	[Optional] Value to recall Snapshot values. 1d = snapshot bank 1.



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## PARAMETERS:

Core ID	Dec	Used to Address Core Modules, to Control Modules. This will be the same Core ID given to the Core Module that contains this "Snapshot" component.
Snapshot Name	String	String defining the "Snapshot" component, defined in QSYS Designer software.
Ramp Time	Seconds	Seconds to indicate internal ramp time when recalling a Snapshot bank

## TESTING:

OPS USED FOR TESTING:	v4.001.1012
SIMPL WINDOWS USED FOR TESTING:	4.04.03
DEVICE DB USED FOR TESTING:	78.05.001.00
CRES DB USED FOR TESTING:	58.05.002.00
SYMBOL LIBRARY USED FOR TESTING:	1010
SAMPLE PROGRAM:	QSYS V3 Demo Program
REVISION HISTORY:	v1.0 – Initial Release