





Partner:QSC-QSYS Model:Qsys Cores Device Type:DSP

GENERAL INFORMATION			
SIMPLWINDOWS NAME:	QSYS Control-[Button] V3.0		
CATEGORY:	QSYS V3		
VERSION:	V3.0		
SUMMARY:	This Module is used for controlling a Button component as dictated in the QSYS Designer Software.		
GENERAL NOTES:	This Module provides control and feedback of a "Named Control" component in the QSYS Designer software. This module is specifically designed to be used on a button component in the QSYS design, but can be used for other components as well. Example, "Trigger Button" (On Only). Must use with QSYS Core module. A "Named Control" component must be created in QSYS Designer software.		
CRESTRON HARDWARE REQUIRED:	Ethernet Card		
SETUP OF CRESTRON HARDWARE:	NA		
VENDOR FIRMWARE:	Unknown		
VENDOR SETUP:	Setup of QSYS Design file, "Named Controls" of desired components for control.		





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CONTROL:		
Initialize_From_Core	D	Pulse to Initialize control to the QSYS Core component. Should not be driven high until QSYS core has successfully initialized. Usually driven from "Initialize_Control_Modules" signal from respective Core module.
[Auto_Poll]	D	[Optional] Hold high to enable automatic polling of desired component ¹
[Button_On]	D	[Optional] Pulse to force button component high/on. (as a trigger button this will just trigger the QSYS component and can be pressed again to trigger again)
[Button_Off]	D	[Optional] Pulse to force button component low/off.
Button_Toggle	D	[Optional] Pulse to toggle button component on or off.

FEEDBACK:		
[Button_On_FB]	D	High to indicate button component is high/on.
[Button_Off_FB]	D	High to indicate button component is low/off.

PARAMETERS:		
Core ID	Dec	Used to Address Core Modules, to Control Modules. This will be the same Core ID given to the Core Module that contains this "Named Control" component.
Named Control	String	String defining the "Named Control" component, defined in QSYS Designer software.

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¹ Poll – Adds "Named Control" component to QSYS "Poll group" to be polled automatically within QSYS. This has a default poll time of 100ms.







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TESTING:		
OPS USED FOR TESTING:	v4.001.1012	
SIMPL WINDOWS USED FOR TESTING:	4.04.03	
DEVICE DB USED FOR TESTING:	78.05.001.00	
CRES DB USED FOR TESTING:	58.05.002.00	
SYMBOL LIBRARY USED FOR TESTING:	1010	
SAMPLE PROGRAM:	QSYS V3 Demo Program	
REVISION HISTORY:	v1.0 – Initial Release	