	op	rs	rt	rd	shamt	funct	ALU Operation
	31 26	25 21	20 16	15 11	10 6	5 0	
Add rd, rs, rt	000000	#reg rs	#reg rt	#reg rd	00000	100000	010
Sub rd, rs, rt	000000	#reg rs	#reg rt	#reg rd	00000	100010	110
And rd, rs, rt	000000	#reg rs	#reg rt	#reg rd	00000	100100	000
Or rd, rs, rt	000000	#reg rs	#reg rt	#reg rd	00000	100101	001
Slt rd, rs, rt	000000	#reg rs	#reg rt	#reg rd	00000	101010	111