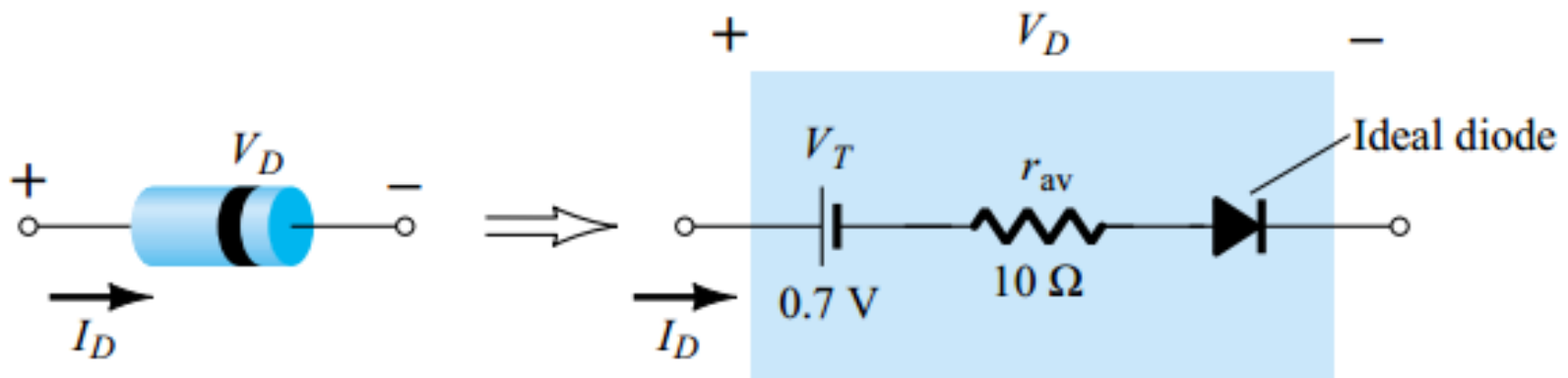


C02015

Exercise on Diode



Diode Equivalent Circuit

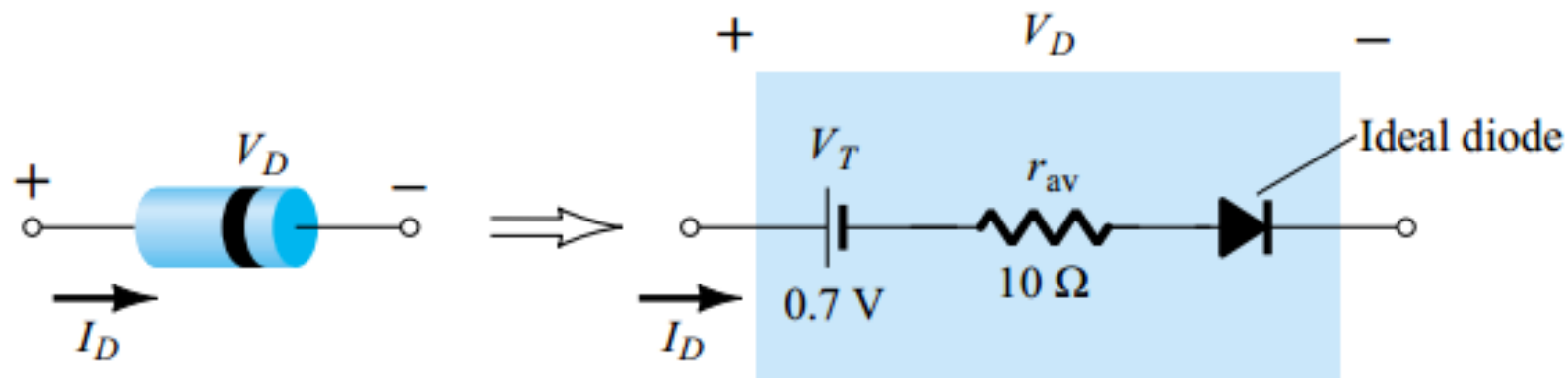
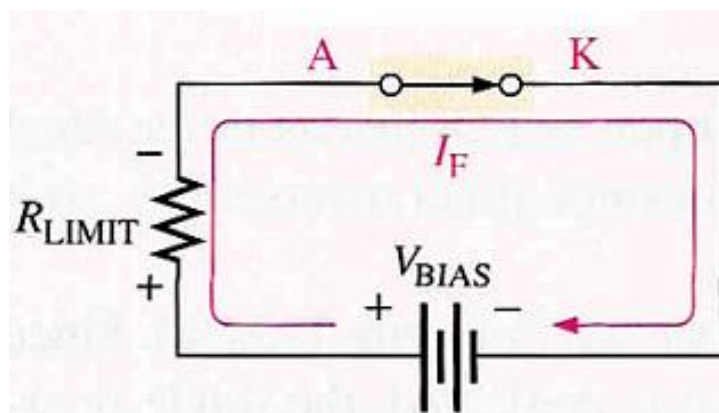


Figure 1.32 Components of the piecewise-linear equivalent circuit.

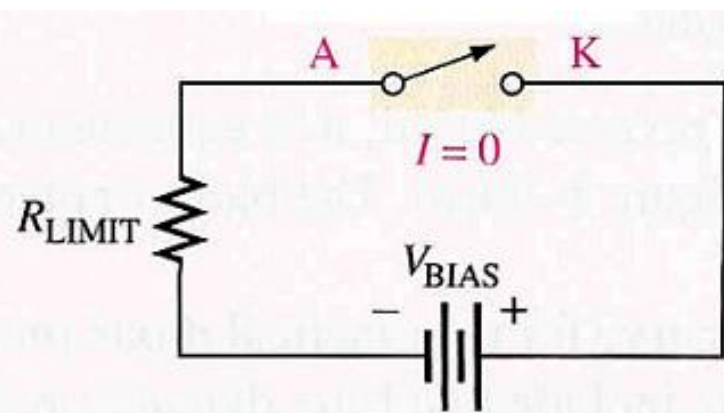
Ideal Diode Model

- Works as a switch
 - Forward bias (switch is close)
 - Reverse bias (switch is open)
- Threshold battery voltage and internal resistance are ignored.



$$I_F = \frac{V_{BIAS}}{R_{LIMIT}}$$

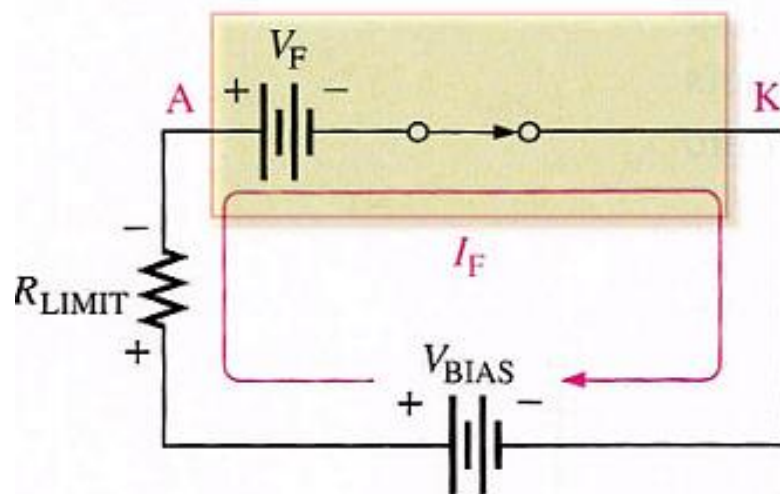
Forward bias



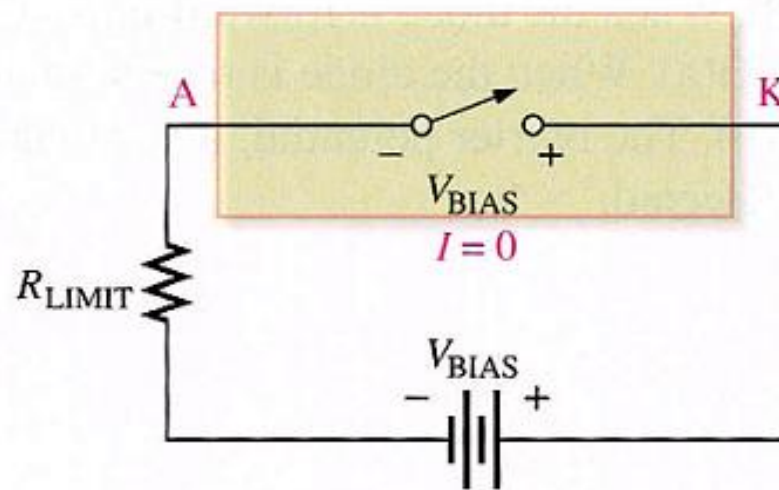
Reverse bias

Practical Diode Model

$$I_F = \frac{V_{BIAS} - V_F}{R_{LIMIT}}$$



Forward bias



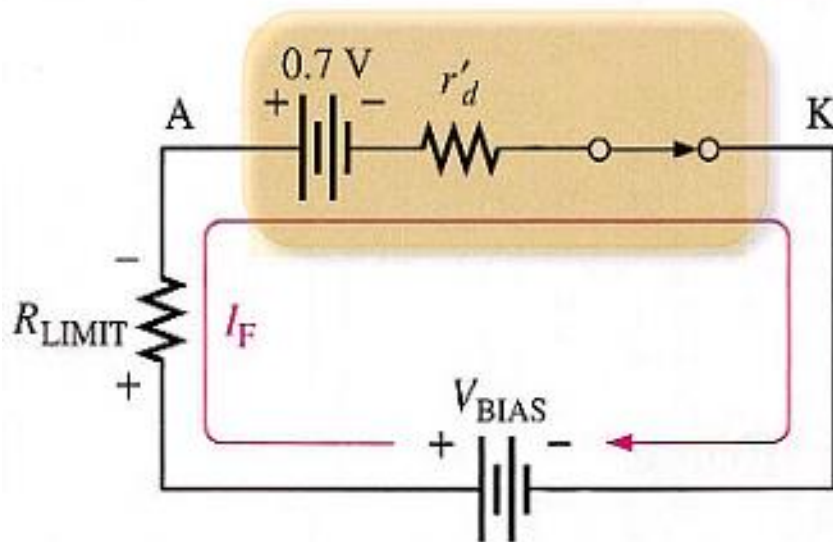
Reverse bias

■ Constants

- Silicon Diode: $V_F = 0.7V$ ($V_F = V_{BIAS}$ if $V_{BIAS} < 0.7V$)
- Germanium Diode: $V_F = 0.3V$ ($V_F = V_{BIAS}$ if $V_{BIAS} < 0.3V$)

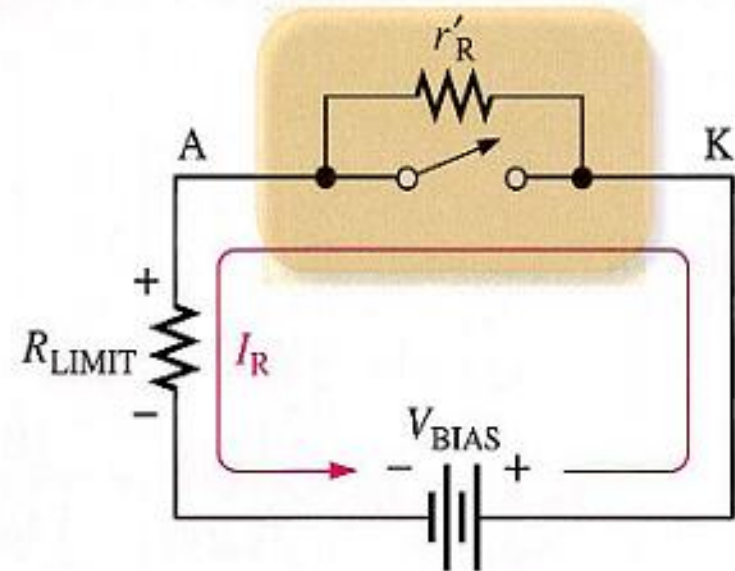
Complete Diode Model

$$I_F = \frac{V_{\text{BIAS}} - 0,7 \text{ V}}{R_{\text{LIMIT}} + r'_d}$$



Forward bias

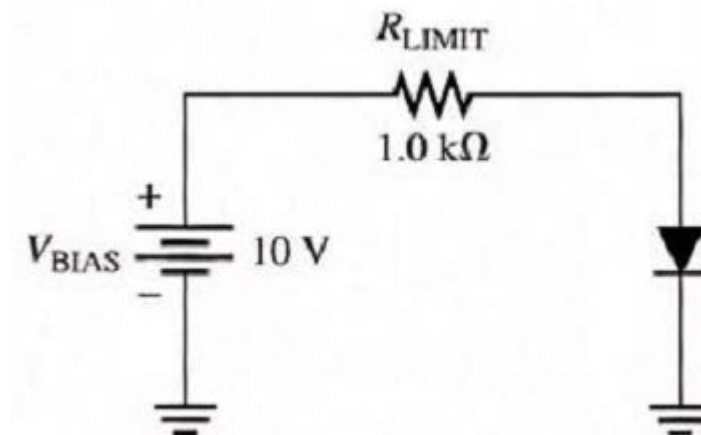
$$I_R = \frac{V_{\text{BIAS}}}{R_{\text{LIMIT}} + r'_R}$$



Reverse bias

Exercise 3

- Given circuit
 - Forward bias voltage $V_F = 0.7V$
 - Internal resistance $r'_d = 10\Omega$
- Determine V_F , I_F and V_{RLIMIT} for three diode models
 - Ideal diode model
 - Practical diode model
 - Complete diode model



Solution 3

- Ideal diode model

$$V_F = 0 \text{ V}$$

$$I_F = \frac{V_{\text{BIAS}}}{R_{\text{LIMIT}}} = \frac{10 \text{ V}}{1 \text{ k}\Omega} = 10 \text{ mA}$$

$$V_{R_{\text{LIMIT}}} = I_F \cdot R_{\text{LIMIT}} = (10 \text{ mA}) \cdot (1 \text{ k}\Omega) = 10 \text{ V}$$

- Practical diode model

$$V_F = 0,7 \text{ V}$$

$$I_F = \frac{V_{\text{BIAS}} - V_F}{R_{\text{LIMIT}}} = \frac{10 \text{ V} - 0,7 \text{ V}}{1 \text{ k}\Omega} = 9,3 \text{ mA}$$

$$V_{R_{\text{LIMIT}}} = I_F \cdot R_{\text{LIMIT}} = (9,3 \text{ mA}) \cdot (1 \text{ k}\Omega) = 9,3 \text{ V}$$

- Complete diode model

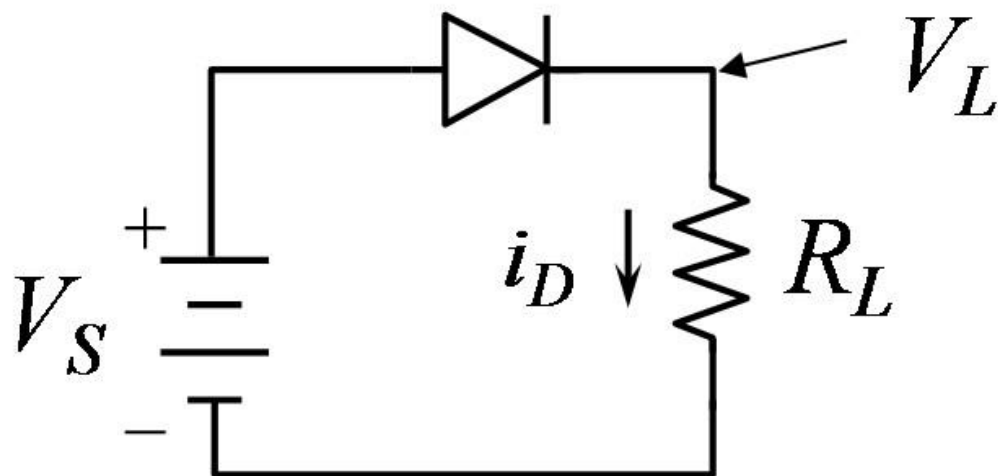
$$I_F = \frac{V_{\text{BIAS}} - 0,7}{R_{\text{LIMIT}} + r'_d} = \frac{10 \text{ V} - 0,7 \text{ V}}{1 \text{ k}\Omega + 10 \Omega} = \frac{9,3 \text{ V}}{1010 \Omega} = 0,00921 \text{ A} = 9,21 \text{ mA}$$

$$V_F = 0,7 \text{ V} + r'_d \cdot I_F = 0,7 \text{ V} + (10 \Omega) \cdot (9,21 \text{ mA}) = 792 \text{ mV}$$

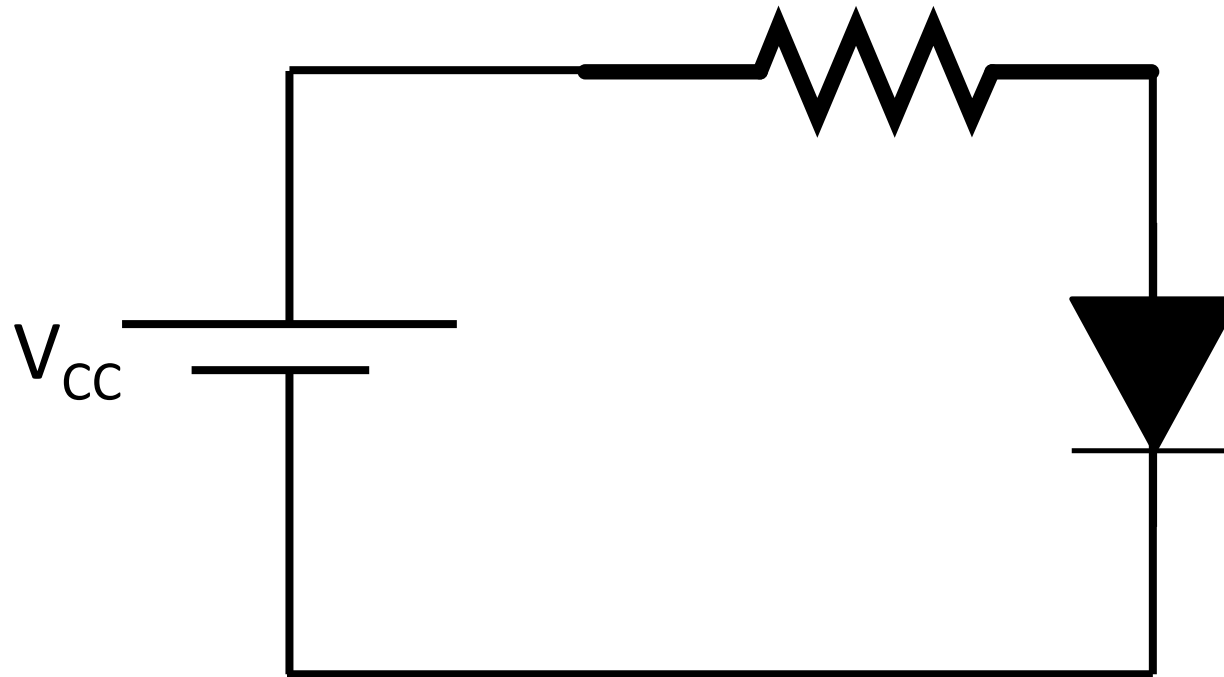
$$V_{R_{\text{LIMIT}}} = I_F \cdot R_{\text{LIMIT}} = (9,21 \text{ mA}) \cdot (1 \text{ k}\Omega) = 9,21 \text{ V}$$

Exercise

- Analyze the circuit by using the diode practical model.
- $V_S = 5V$ and the current (i_D) in the circuit is 1mA.
- What is the value of R_L .

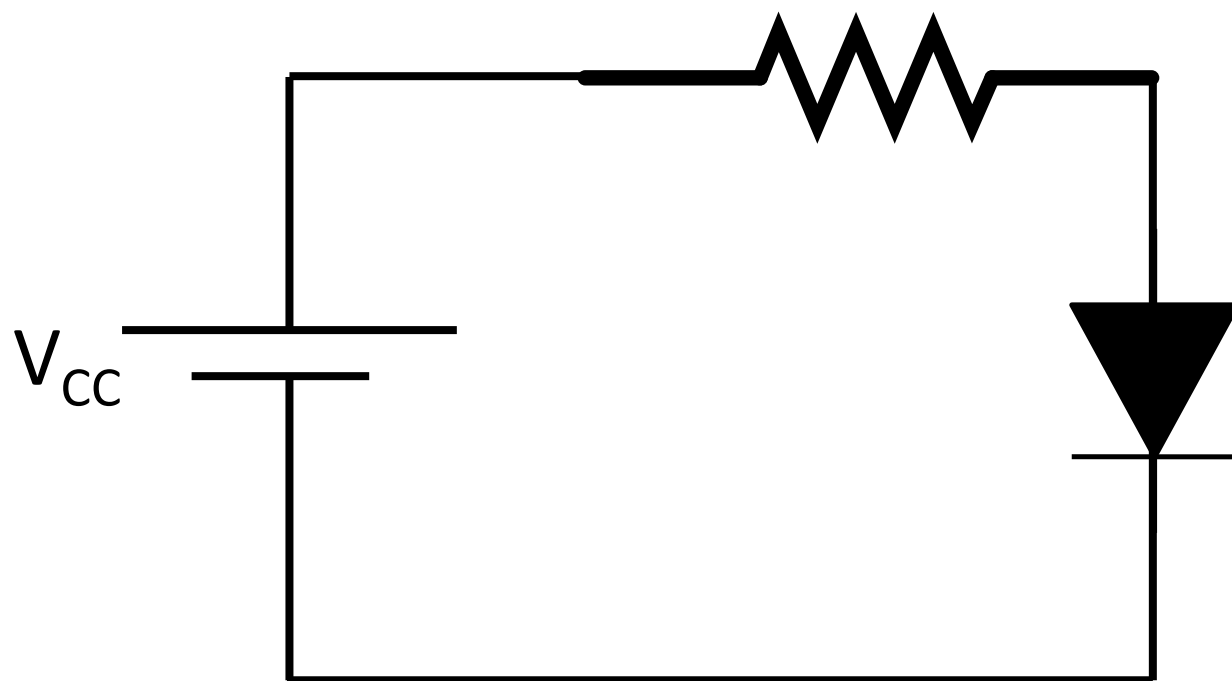


Exercise



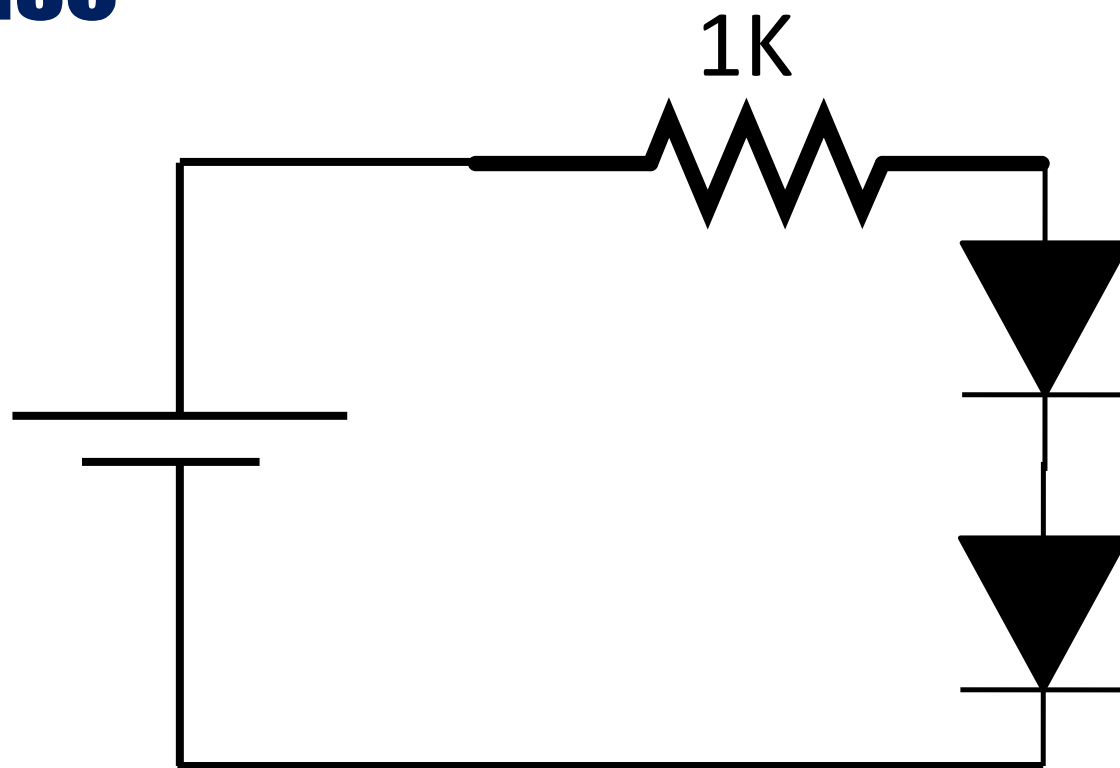
- When $R = 230 \text{ Ohm}$, $V = 0.68\text{V}$
- When $R = 150 \text{ Ohm}$, $V = 0.69\text{V}$
- Determine the internal resistance of the diode.
- $V_{CC} = 5\text{V}$ for both cases

Exercise



- Determine the dropdown voltage of the diode and its internal resistance with $V_{CC} = 12\text{V}$, $R = 220\ \Omega$ and $I = 51.63\text{mA}$.

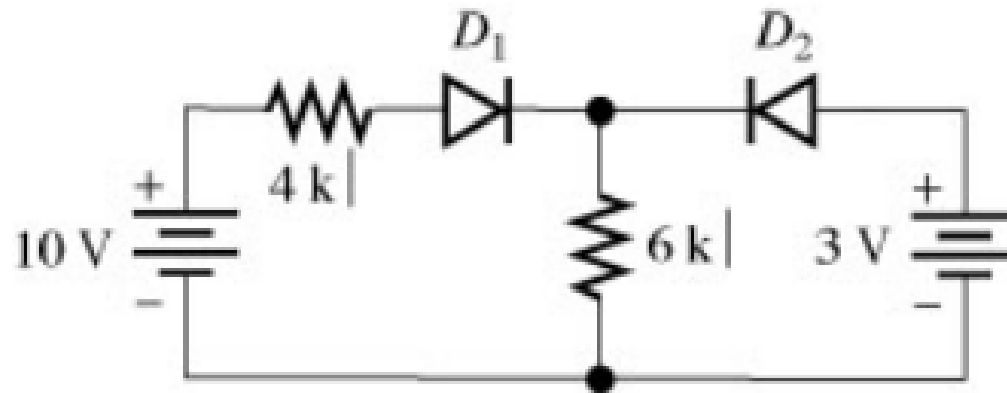
Exercise



- The voltage supply is 9V, the dropdown voltage of each diode is 0.7V. Determine the current in the circuit

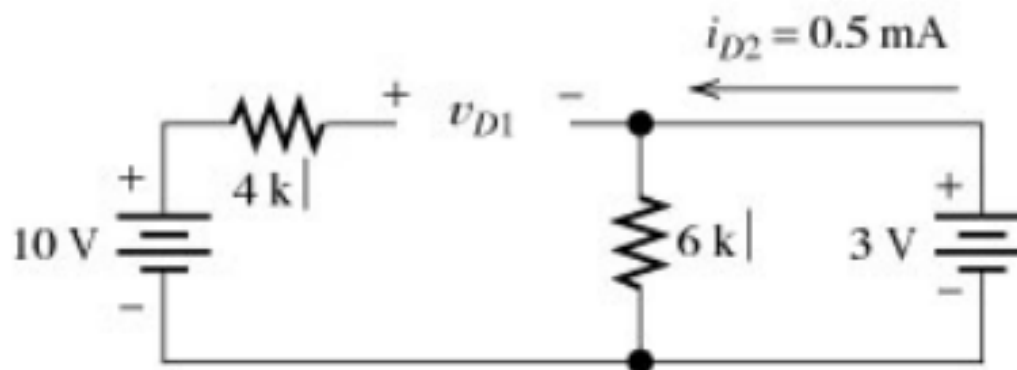
Exercise

- Analyze the circuit using ideal diode model

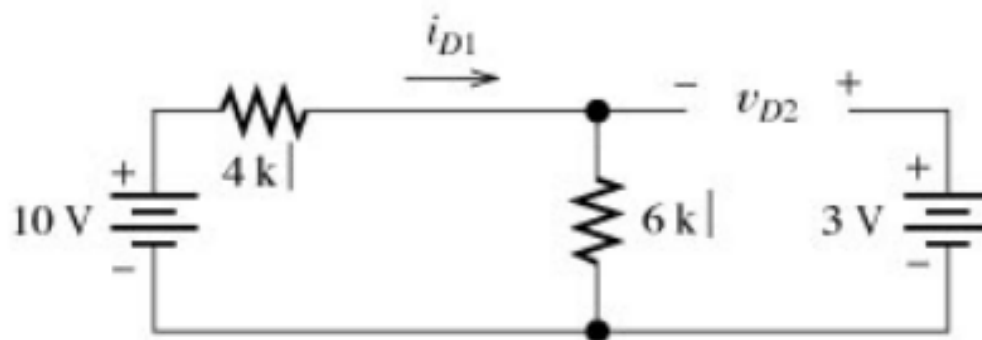


Answer

- Assume that D1 is OFF and D2 is ON

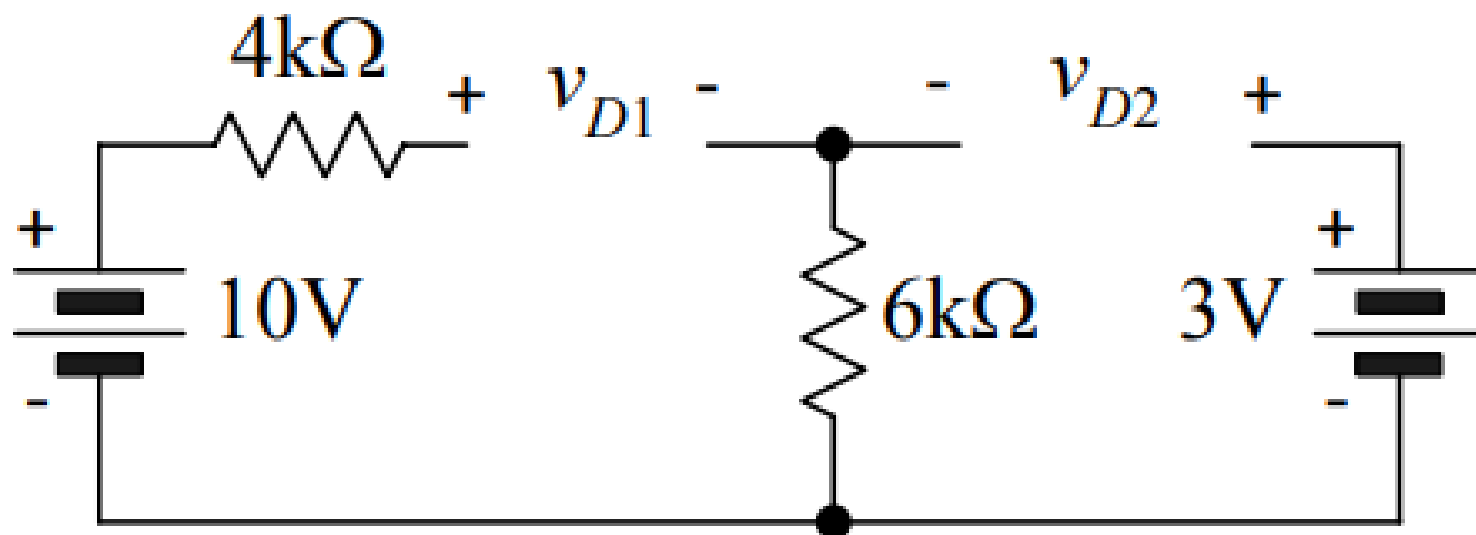


- Assume that D1 is ON and D2 is OFF



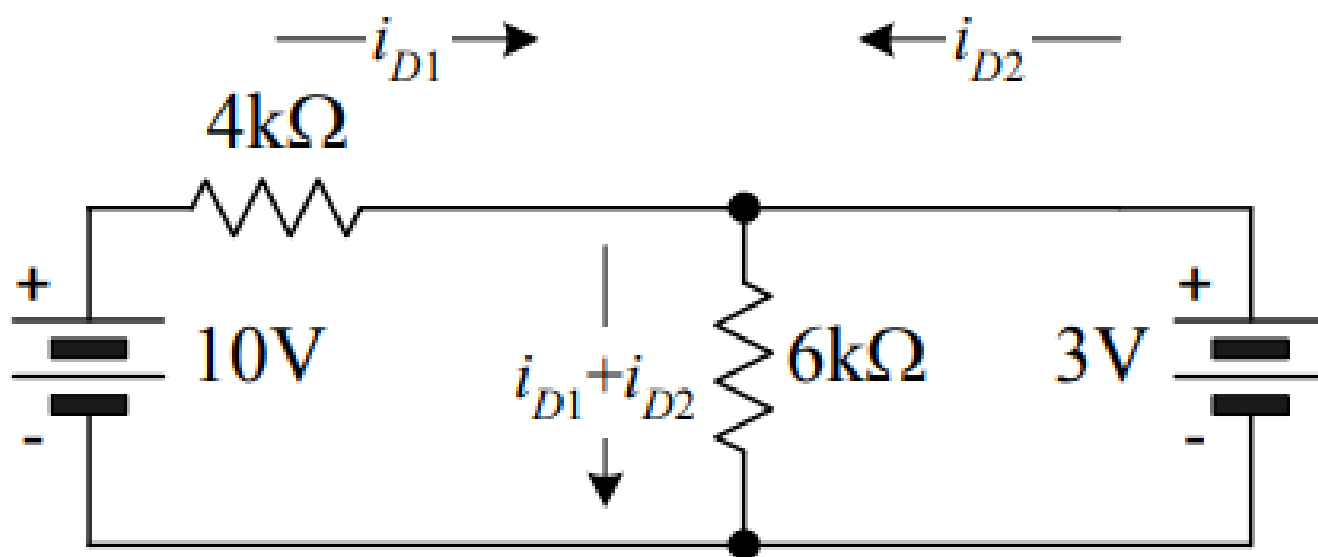
Exercise

- Explain that D1 and D2 are off is not a valid assumption



Exercise

- Show that D1 and D2 are on is not valid also



Answer

$$i_{D1} + i_{D2} = \frac{3V}{6k\Omega} = 0.5mA$$

$$i_{D1} = \frac{10V - 3V}{4k\Omega} = 1.75mA$$

$$i_{D2} = (i_{D1} + i_{D2}) - i_{D1} = 0.5 - 1.75 = -1.25mA$$

Midterm (60 mins – Closed Book)

- 29/03/2019 – From 8g30 – 303B4
- Multichoice + Written

- Chapter 1: Basic Electronic Components
 - Determine the resistor values (4-band colors, 5-band colors)
 - LEDs connectors (Serial + Parallel)

- Chapter 2: Diode
 - Diode Principles and Models
 - Applications using Diodes

- Chapter 3: BJT (npn)
 - Amplifier Coefficient, Applications
 - Cutoff, Saturation and Amplifier modes
 - $V_{be} = 0.7$ (for default)