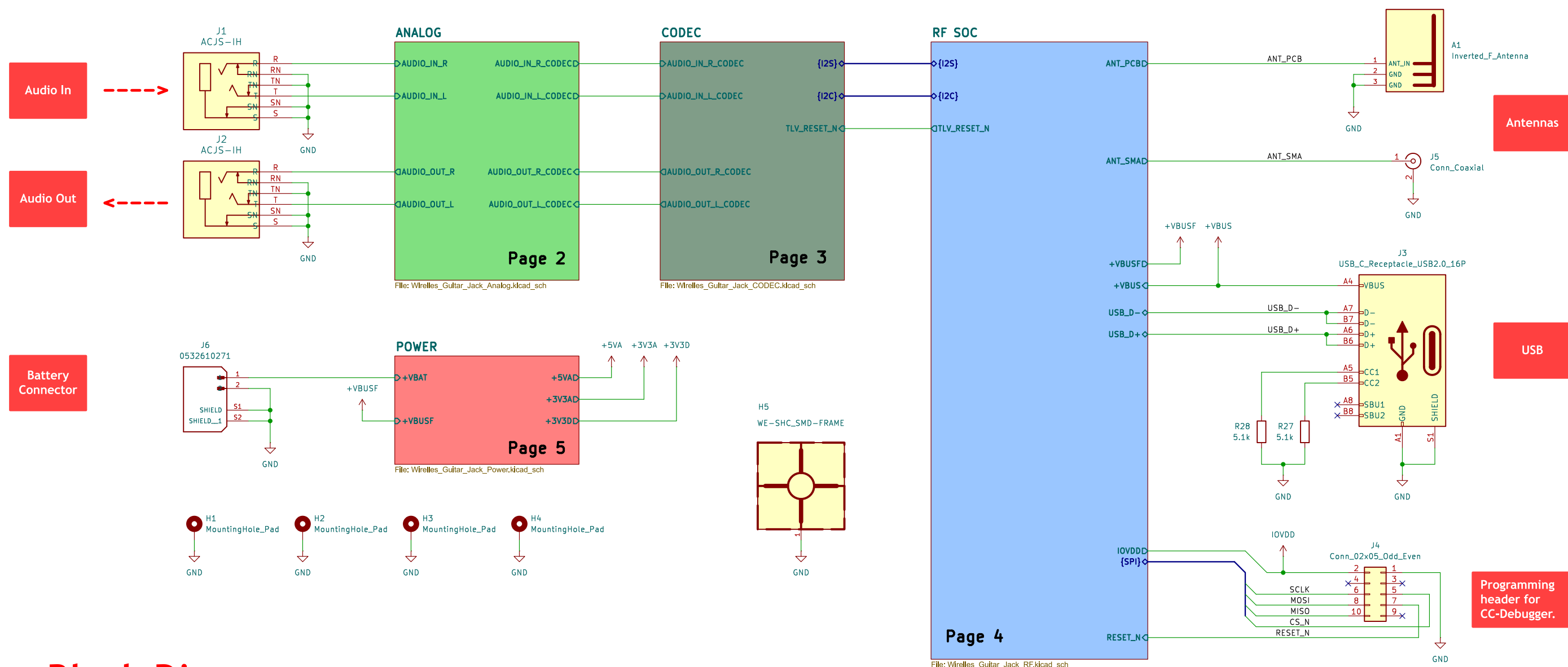
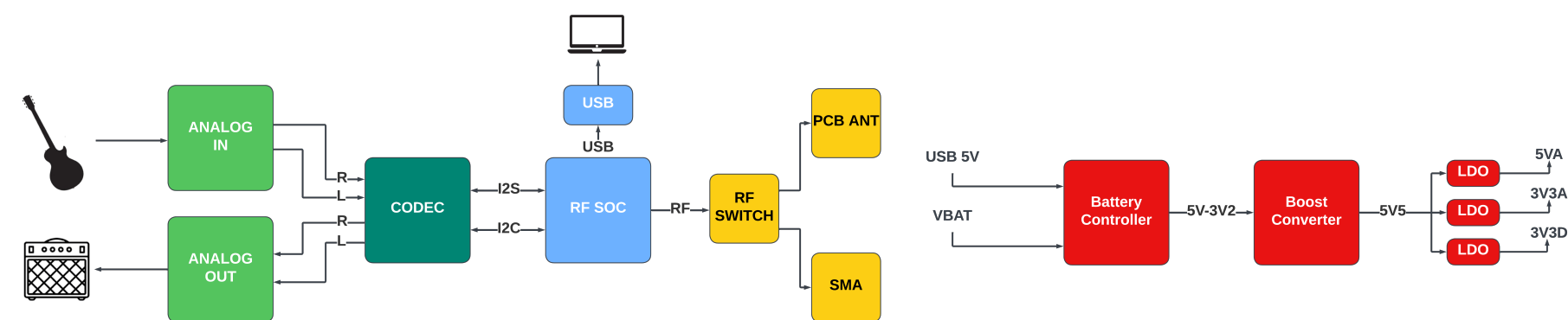


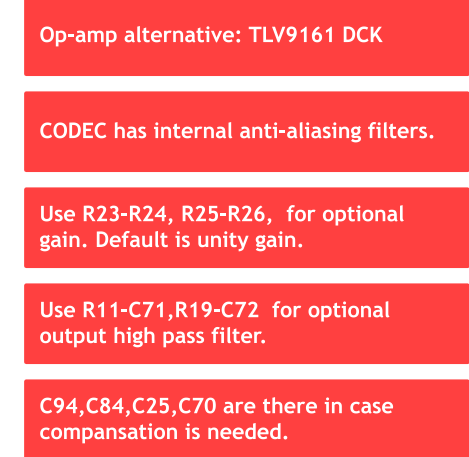
# System Architecture



# Block Diagram

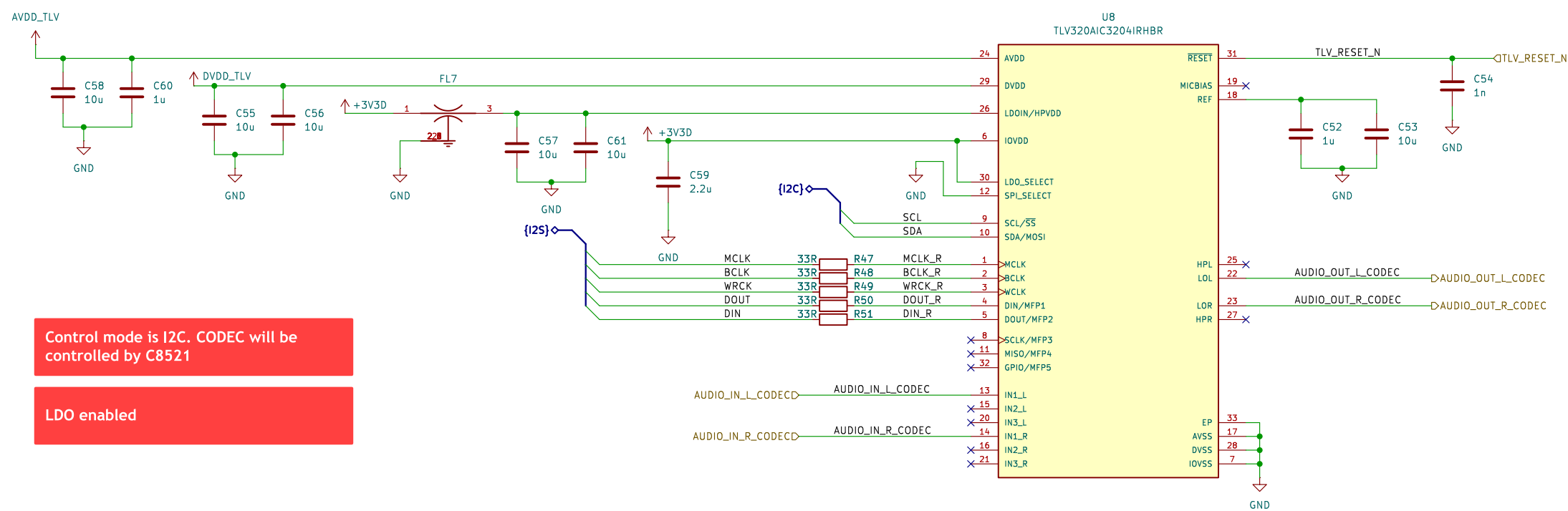


	Board Name: <b>Wirelles Guitar Jack Transceiver</b>	Project Name: <b>Wirelles Guitar Jack</b>	
	Designer: Orhun Dabak	Date: 2025-03-29	Revision: 0.1
Sheet Title: System Architecture	File Name: Wirelles_Guitar_Jack_TX.kicad_sch	Size: <b>A3</b>	Sheet: <b>1</b> of <b>5</b>



	Board Name: <b>Wireless Guitar Jack Transceiver</b>		Project Name: <b>Wireless Guitar Jack</b>	
	Designer: Orhun Dabak		Date: 2025-03-29	Revision: 0.1
Sheet Title: <b>ANALOG</b>	File Name: Wireless_Guitar_Jack_Analog.kicad_sch		Size: <b>A2</b>	Sheet: <b>2</b> of <b>5</b>

# CODEC



Control mode is I2C. CODEC will be controlled by C8521

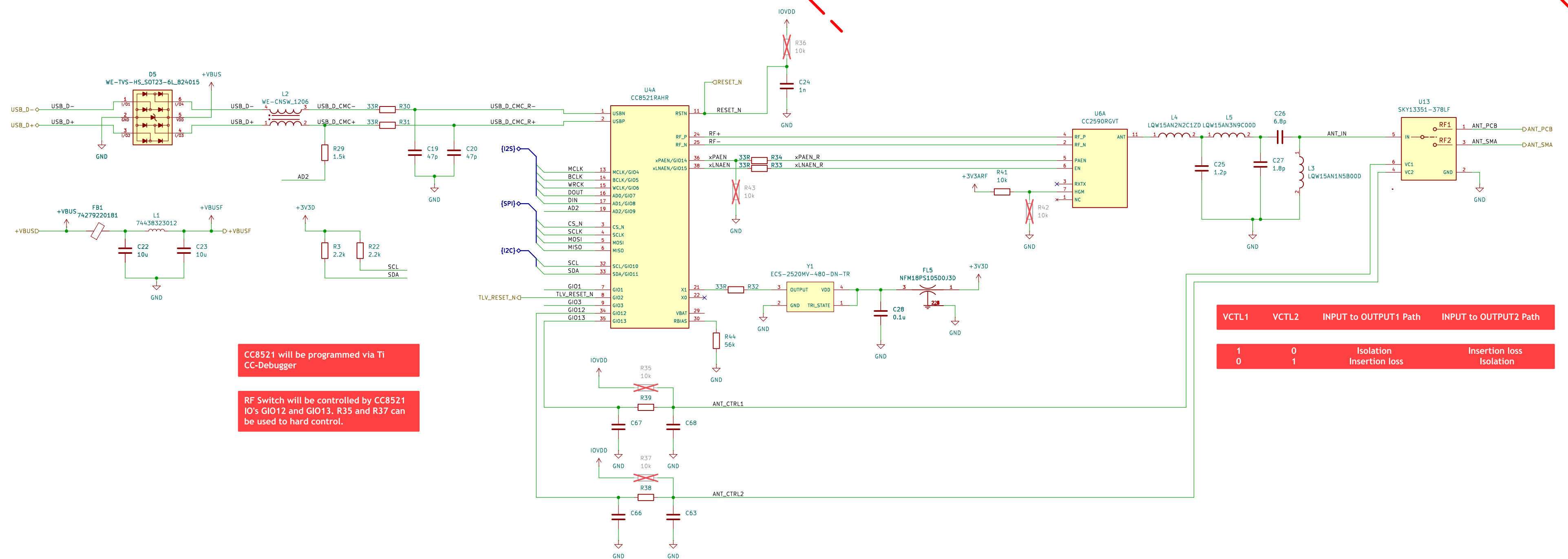
LDO enabled



Board Name: <b>Wirelles Guitar Jack Transceiver</b>	Project Name: <b>Wirelles Guitar Jack</b>	
	Designer: Orhun Dabak	Date: 2025-03-29
Sheet Title: CODEC	File Name: Wirelles_Guitar_Jack_CODEC.kicad_sch	Size: <b>A3</b>
		Sheet: <b>3</b> of <b>5</b>

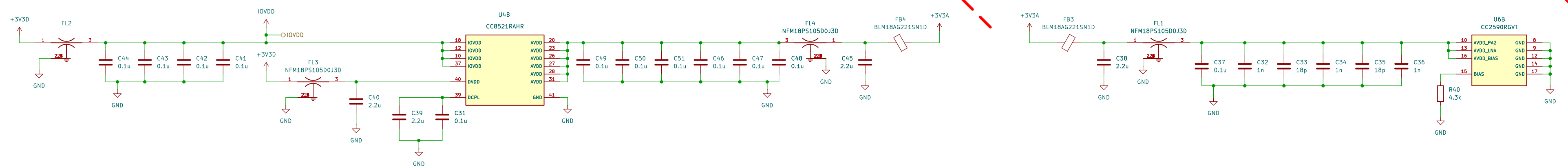
## RF SOC

## RF Output

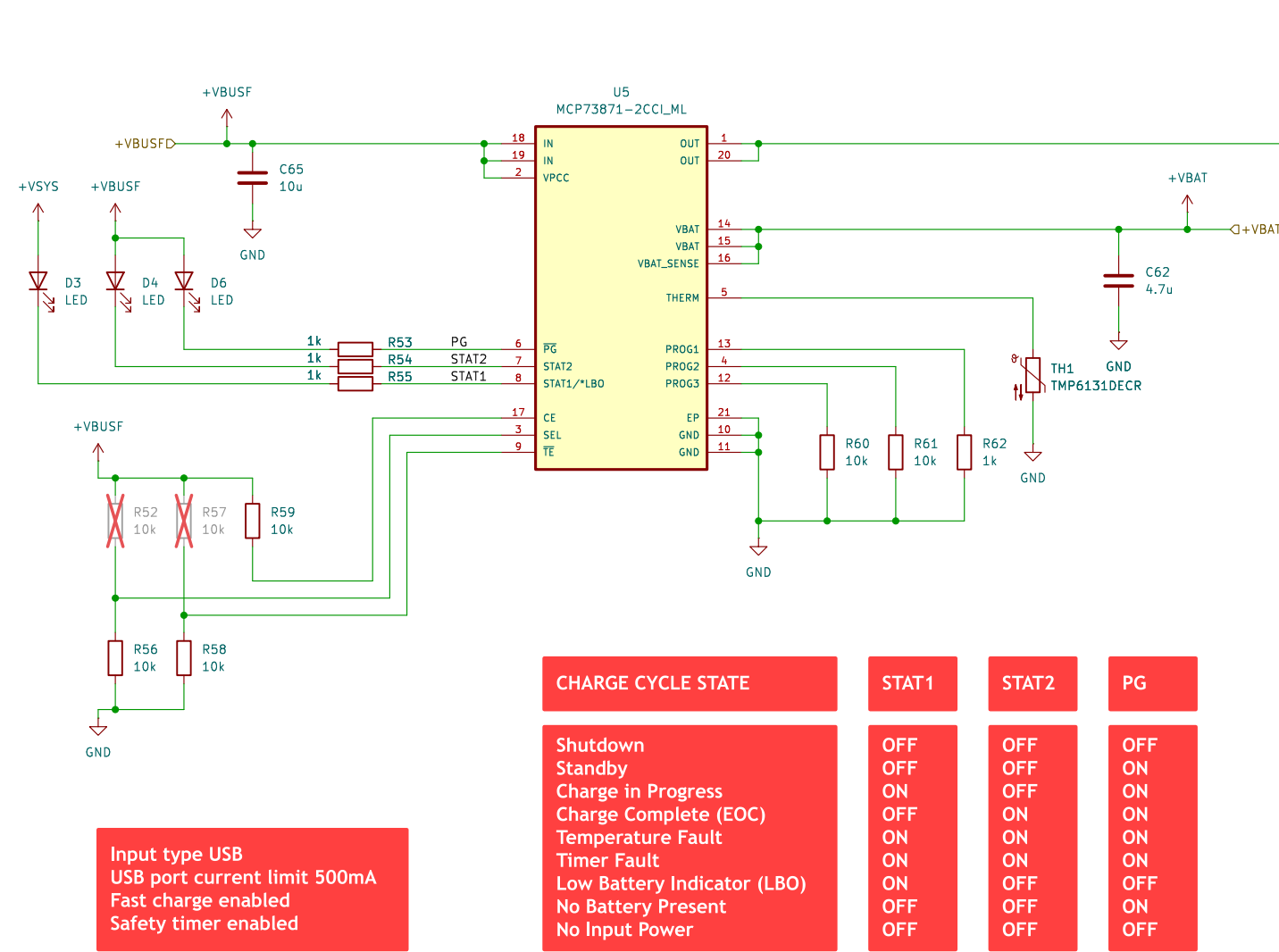


## CC8521 Power

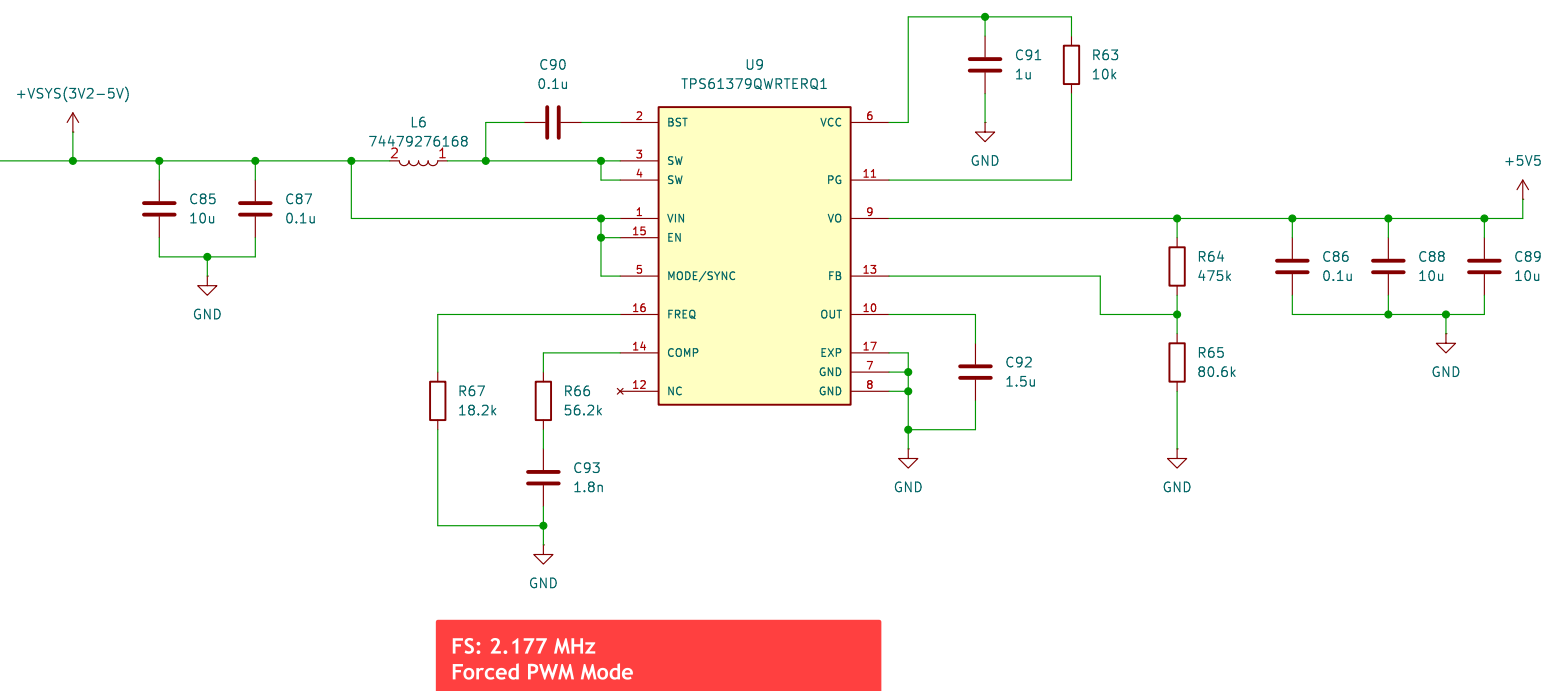
## CC2590 Power



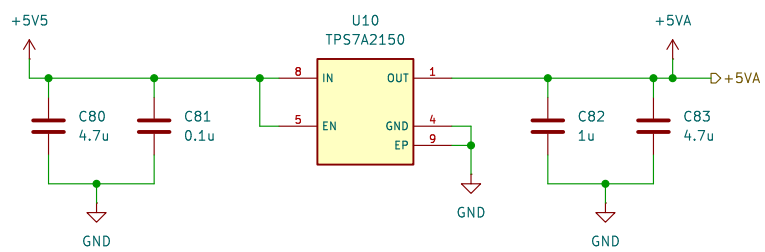
## Battery Controller



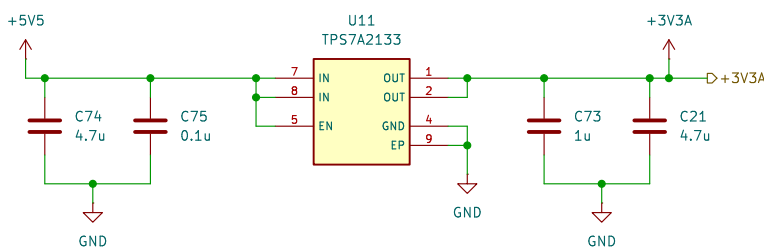
## Main Power Boost Converter



## Op-amp Supply



## RF SOC Supply



## CODEC Supply

