



# VISHNU PRASATH S P

## ELECTRONICS AND COMMUNICATION ENGINEERING STUDENT

### CONTACT

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624618

### TECHNICAL SKILLS

- C - Intermediate
- C++ - Intermediate
- Verilog - Basic

### AREA OF INTEREST

- Problem Solving
- Digital Electronics

### PERSONAL SKILLS

- Time Management
- Team player
- Adaptability
- Communication
- Quick learner

### CAREER OBJECTIVE

To work in a challenging environment where my technical and management skills are used for the growth of the organization as well as to enhance my knowledge.

### EDUCATIONAL QUALIFICATION

- BANNARI AMMAN INSTITUTE OF TECHNOLOGY,  
BE - ECE - CGPA : 8.71\* (Till 4th Semester)  
2021-2025
- SAINIK SCHOOL AMARAVATHI NAGAR,  
HSC - Percentage : 83  
2019-2021
- SAINIK SCHOOL AMARAVATHI NAGAR,  
SSLC - Percentage : 79  
2018-2019

### PROJECTS

- Precise Crop Suggestion**  
**Description:** A Recommendation System that predicts suitable crop based on NPK values of the soil.  
**Technolody used:**LoRaWAN,Web develepment,GateWay  
**Role:** HardWare Interface  
**Duration:** 40 days
- Hydroponics System**  
**Description :** A Project to Automize the needs of the plants without Water .  
**Technology Used:** ESP Wlith cloud  
**Duration:**15 days

## TOOLS

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- Arduino Ide
- Esim

## LANGUAGES KNOWN

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- English (R/W/S)
- Tamil (R/W/S)
- Hindi (R/S)

## EXTRA CURRICULAR ACTIVITIES

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- SPORTS-BASKETBALL

Runner up in Inter house Basketball conducted by Sainik school.

- CO-CURRICULAR

Runner up in Mono-Act Tamil conducted by Sainik School Amaravathi Nagar.

## PERSONAL PROFILE

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- Gender : Male
- Date of Birth :22/08/2003
- Father's Name: Periyasamy K
- Mother's Name: SudhaRani P
- Nationality : Indian

## INTERNSHIP

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### 1. TESSOLVE SEMICONDUCTOR PVT.LTD.

**Mode :** Offline

**Concepts learnt :** Basic Verilog

**Duration :** 15 days

## TECHNICAL ACTIVITIES

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### HACKATHONS

- Finalist in BITHACK'24 conducted by Bannari amman Institute of Technology
- Participated in **4th Technovation Hackathon** organized by Sharda University-Noida
- Participated in **5th Technovation Hackathon** organized by Sharda University-Noida

### PAPER PRESENTATIONS

- Presented a paper on 'Precision Crop Suggestions' at the National Conference on Recent Trends in Science and Technology .
- Presented a paper on “Hydroponics System” conducted by PSG Cllege Of Engineering.

### CIRCUIT DESIGN CHALLENGE

- Winner in **Mixed Signal SoC design Marathon** organized by IIT, Bombay with VLSI System Design Pvt Ltd.

### ACHEIVEMENTS:

- Qualified to stage-2 of e-Yantra Robotics Competition(eYRC 2022-23) organized by IIT,Bombay

## DECLARATION

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I hereby declare that all the information stated above is true to the best of my knowledge

**DATE :**

**PLACE : Sathyamangalam**

**SIGNATURE**