Incremental Encoding of Multiple Inheritance Hierarchies

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Abstract

Incremental updates to multiple inheritance hierarchies are becoming more prevalent with the increasing number of persistent applications supporting complex objects, making the efficient computation of lattice operations such as greatest lower bound (GLB), least upper bound (LUB), and subsumption more and more important. General techniques for the compact encoding of a hierarchy are presented. One such method is to plunge the given ordering into a boolean lattice of binary words, leading to an almost constant-time complexity of the lattice operations. The method is based on an inverted version of the encoding of Ait-Kaci et al. to allow incremental update. Simple grouping is used to reduce the code space while keeping the lattice operations efficient. Comparisons are made to an incremental version of the range compression scheme of Agrawal et al., where each class is assigned an interval, and relationships are based on containment in the interval. The result is two encoding methods which have their relative merits. The former being better for smaller, more structured hierarchies, and the latter for larger, less organized hierarchies.

1 Introduction

Multiple inheritance hierarchies arise in many areas, including knowledge representation and reasoning, database management and query processing, and programming. Neural network models, semantic networks and state spaces all require such a structure. Complex-object databases, where objects are grouped into classes organized into an inheritance hierarchy, are becoming more prevalent. Database researchers have recognized the utility of hierarchies in querying databases. Scheduling packages, computer-aided design software, and modeling software such as UML (Unified Modeling Language) and OMT (Object Modeling Technique) employ hierarchal structures. Object-oriented programming languages such as C++ and Java are based on organizing classes into a hierarchy. All of these areas have one common requirement – a compact representation of the hierarchy with the ability to compute relationships efficiently.

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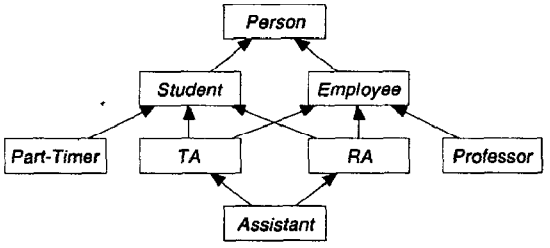

Figure 1: Partial university hierarchy.

In general, objects that are instances of classes organized in an inheritance hierarchy are manipulated via expressions involving the conjunction and disjunction of classes, and relationship testing. These operations represent the greatest lower bounds (GLB), least upper bounds (LUB), and subsumption. Embedding the hierarchy in a boolean lattice enables the expressions to be evaluated as binary AND, OR, and containment. Permitting multiple inheritance complicates the expressions involving conjunction and disjunction, leading to a possibility of the result representing a disjunction or conjunction of classes, respectively.

Consider for example the inheritance hierarchy of Figure 1. The class *Person* is an immediate superclass of *Student*, while *Student* is an immediate subclass of *Person*. Class *TA* is an immediate subclass of both *Student* and *Employee*, and thus inherits all properties of both classes. Class *TA* is also a subclass of class *Person*.

With the assumption that an object is created in its most specialized class, the computation of the join of two classes is required. For example, the claim that an object is both a *TA* and an *RA* in the simple university hierarchy implies that the object is in the class representing the conjunction of the two classes, or in lattice terms, the GLB of the two classes, which is class *Assistant*. Similarly, the LUB operation represents the lowest superclass of an object if it is known that it is in one of several classes. For example, an object in either the *Student* or *Employee* class is known to be in the *Person* class.

Multiple inheritance further complicates these operations as the result may not be a single class. In the example, the GLB of classes *Student* and *Employee* is the two classes *TA* and *RA*, representing the notion that a person who is both a student and an employee must be either a TA or RA, or both. Also, the LUB of classes *TA* and *RA* is the two classes

Classes	Transitive	Ait-Kaci	Ganguly	Caseau	Agrawal	Fall
Person	11111111	11111	11111	00000	[1,8]	[]
Student	01011011	11011	10111	00001	[1,3], [4,5]	1
Employee	00111101	11101	01111	00010	[4,7], [2,2]	[1]
Part-Timer	00000010	00010	10110	00101	[1-1]	2
TA	00001001	01001	10101	01011	[2,2], [4,4]	1.[2]
RA	00010001	10001	01101	10011	[4,5]	[1-1]
Professor	00000100	00100	01110	00110	[6,6]	[1-[1]]
Assistant	00000001	00001	00001	11011	[4,4]	1.[2-1]

Table 1: Encodings of university hierarchy.

Student and *Employee*, representing the notion that a person who is either a *TA* or *RA* is both a student and employee.

Previous representations of hierarchies have involved a static, compile-time encoding which requires recalculation for any updates. A dynamic approach is more appropriate as many applications contain persistent data and hierarchies are constantly changing. Recomputation of the encoding is not only too time consuming, but also requires reassigning new codes to objects in persistent applications – an expensive operation. The ideal encoding allows for run-time additions to the hierarchy to be efficiently encoded with little storage overhead and requires few changes to existing codes.

Four earlier methods are reviewed in Section 2, along with a fourth which fails to support GLB and LUB operations. Variations of two of the methods are developed in Section 3, and experimental results presented in Section 4. Summary remarks follow in Section 5, along with directions for future research.

2 Background

One approach used to store a poset involves its transitive closure matrix. Let $x_1, x_2, \ldots, x_n$ be the elements of the hierarchy. A transitive closure matrix is a two-dimensional array of 0's and 1's whose (i, j)th element is 1 if and only if x_i is an ancestor of x_j. This method requires $O(n^2)$ bits of storage, and is illustrated in the first two columns of Table 1.

Ait-Kaci et al. [2] developed an encoding method based on a compressed version of transitive closure by only using new bit positions where necessary, as illustrated in the first three columns of Table 1. Unfortunately the encoding is done from the bottom up; that is, the lowest levels of the hierarchy are encoded first. During the encoding, a class is given the code consisting of the binary OR of the codes of its children. The code length is incremented only when the class has a single child (in order to distinguish the codes of the two classes) and when the computed codes indicate a relationship with the an element not related. The method is also static, and uses a large compile-time overhead to create the encoding. This prohibits the incremental update of the hierarchy and fails to model applications that are developed over time from the top down, as is the case with many persistent hierarchies. The encoding produced is compact relative to transitive closure in the sense that new bit positions are added only to differentiate nodes with common descendants. This produces an encoding with constant-time, very efficient calculations of GLB, LUB, and subsumption, using binary AND, binary OR, and comparison, respectively.

A method termed *modulation* is also employed to group a hierarchy into *modules*, and thus allow for more reuse of bit positions by adding a group code to a class. The elements of a group are encoded separately using the above method. This produces a much more compact encoding for hierarchies that group well, and does not add much overhead to the lattice operations. Unfortunately, according to Ganguly et al. [6], modulation requires $O(n^2)$ time and $O(nd)$ space, where d is the degree of the graph of the hierarchy and n is the number of nodes in the hierarchy. Both encoding methods achieve a best case of $O(\log n)$ and worst case of $O(n)$ length codes.

Ganguly et al. [6] improve on these results by developing an algorithm which is shown via empirical experiments to be significantly more space efficient and does not rely on the structure of the hierarchy. Again the algorithm is static, as it requires both a bottom-up and top-down traversal of the hierarchy. This would result in a complex transformation to enable dynamic additions to the hierarchy. The computation of LUB and subsumption are similar, but the computation of GLB requires the addition of a code representing the classes with multiple parents.

Caseau [4] achieves a more compact binary encoding of a hierarchy which supports subsumption but does not allow GLB and LUB computations. A sample encoding is shown in Table 1. His encoding first requires the transformation of the hierarchy into a lattice. The encoding then proceeds by locating the primary nodes (nodes with unique parents) and assigning a bit position to them. The choice of bit position for a node relies on the positions used for the nodes related to the node's ancestors. The algorithm works from the top down, and modifications to chosen bit positions are required if conflicts occur when a new node is added. Since GLB and LUB computations are not supported, bit positions are reused in nodes shown to be unrelated by other inherited positions. For example, classes *Part-Timer* and *Professor* share bit position three in the encoding in Table 1. The overall encoding produced is much more compact than that of Ait-Kaci et al. because it does not support the lattice operations. The top-down nature of the algorithm is borrowed in the implementation of an inverted version of the encoding of Ait-Kaci et al. in Section 3.

Agrawal et al. [1] propose a range compression scheme that assigns an interval to a class (or multiple intervals to support multiple inheritance), as illustrated in Table 1. A class is a subclass of another if its interval(s) fall into the interval range(s) of the superclass. Encoding is done by first performing a post-order traversal of a spanning tree for the hierarchy. A class is assigned an interval corresponding to the range between the lowest postorder number among its descendents and its own postorder number. Multiple inheritace requires the addition of secondary intervals for some nodes to reflect the additional paths down the hierarchy. Lattice operations can then be performed by comparing the

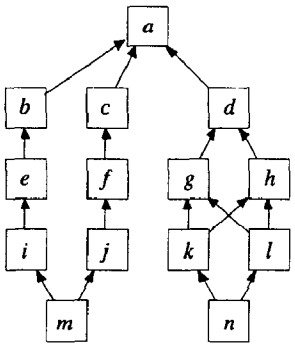

Figure 2: Example hierarchy.

intervals of the classes involved. Although creating a spanning tree for a hierarchy is a static, compile-time operation, Agrawal et al. also discuss the possibility of allowing incremental updates to the hierarchy. This aspect of the encoding is explored in Section 3.

Fall [5] employs so-called *sparse terms* for the dynamic encoding of hierarchies from the top-down. Fall claims that the storage requirements for sparse terms is significantly less than that for bit vectors and intervals, although this is not apparent from the simple encoding in Table 1. Although optimal for certain types of hierarchies, sparse terms are of the same order of storage as the other encodings on general binary trees. In fact, in the real-world example of 1,815 nodes from a chess-learning program, sparse terms required 442 bits per code, as compared to 590 for the compact encoding of Ait-Kaci et al. The measure of bits per code for sparse terms is not well explained, and seems to be an underestimate for the space required to keep track of sparse terms in a dynamic environment given their variable length and the need for pointers.

3 Methods

The adaptation of the compact encoding method of Ait-Kaci et al. [2] and the interval encoding method of Agrawal et al. [1] to a persistent, incremental application involves the handling of dynamic encoding starting from the top class in the hierarchy and adding each new leaf class as it is added as a subclass of existing classes. That is, hierarchies will be encoded from the top down, with no new classes added in the middle of an existing inheritance relationship. Since the encodings are performed at run-time, the amount of overhead being stored to assist in the encoding must be minimized. Further, the computation of the lattice operations must remain efficient. The methods are presented below, with the hierarchy illustrated in Figure 2 used for demonstration.

The proposed methods limit the number of code changes as classes are added by only requiring changes when the class added is involved in a multiple inheritance. Since the multiple inheritance factor is typically small (close to one) [4], this is an infrequent occurrence. Also, the number of classes affected usually remains small as it is only those in the same region of the hierarchy. The only exception is during group combining in modulated encoding.

3.1 Incremental Top-down Encoding

Incremental top-down encoding arose from the application of the top-down approach of Caseau [4] to the bottom-up encoding methods of Ait-Kaci et al. [2]. It differs from the bottom-up approach in that it enables codes to be generated for each new class as it is added to the hierarchy, ridding the system of the overhead of code recomputation and assignment. In addition, the code lengths are made variable, so that those classes that only require a short code do not store a long string of zeros, thus saving additional storage. As well, the increment of a code length beyond that of the currently stored codes does not require the modification of all codes to conform to the new length.

The encoding proceeds as follows. The root node is assigned a code of 0, and each new class is assigned a code calculated using the *Encode* function defined below. The notation $\gamma(x)$ in the functions refer to the encoding of node x, and $\vee$ refers to the Boolean OR operation on binary codes. The functions rely on a global variable p, initialized to zero, that represents the current length of the longest code, and is used to determine the next available bit position.

The function *Encode* determines the new code for class n by computing the binary OR of the codes of n's parents if there are multiple parents, or by adding a new bit to the front of the parent's code.

$Encode(x : class)$::
  let $\{x_1, ..., x_n\} = Parents(x)$ in
    if $n > 1$ then
      $\gamma(x) \leftarrow \bigvee_{i=1}^{n} \gamma(x_i)$
    else $\gamma(x) \leftarrow Increment(x_n)$
  $ResolveConflicts(x)$

Each new code must be unique and not conflict with any other code already present in the hierarchy. *ResolveConflicts* ensures this uniqueness by comparing the code assigned to a new class to the codes attributed to the members in the *incomparable* set of the class. The incomparable set is made up of the classes that are neither subclasses nor superclasses of the given class. It is sufficient to examine the classes descended from the parents of the given node, because those are the only ones with the same bits as those contained in the codes of the parents.

$ResolveConflicts(x : class)$::
  for each $y \in IncSet(x)$ do
    if $\gamma(x) = \gamma(y)$ then
      $\gamma(x) \leftarrow Increment(x)$
      $Propagate(y)$
    else if $\gamma(x) < \gamma(y)$ then
      $\gamma(x) \leftarrow Increment(x)$
    else if $\gamma(x) > \gamma(y)$ then
      $Propagate(y)$

If a conflict arises, *Increment* ensures that the codes involved are made unique by placing a new bit at the beginning of the codes causing the conflict. This bit position is calculated by taking the binary value obtained by raising two to the power of the length of the longest code.

$Increment(x : class) : binary$::
  $p \leftarrow p + 1$
  return $2^{p-1} \vee \gamma(x)$

When a class with children is incremented, its descendants must also receive the new bit to maintain the containment of the code of the parent in the code of the descendants. *Propagate* handles this procedure.

Propagate(x : *class*)::
  $\gamma(x) \leftarrow$ *Increment*(x)
  for each $y \in$ *Children*(x) **do**
    $\gamma(y) \leftarrow \gamma(y) \vee \gamma(x)$

The final encoding for the example hierarchy is given on the left side of Table 2. After assigning code '0' to node a, *Increment* of the parent code is used to assign the codes to classes b through j. The code for classes k and l result from the binary OR of the codes for their parents but, since this would lead to the same code for the two classes, the call to *ResolveConflicts* increments the code for class l as a member of the incomparable set, and calls *Propagate* to increment the code for class k. The codes for classes m and n are simply the binary OR of the codes for their respective parents.

With this method, the lattice operations of GLB and LUB become binary OR and AND operations, respectively. For example, for the GLB of i and j, take the OR of codes '10001001' and '100010010', giving '110011011', which is precisely the code of m, the GLB of i and j. It should be noted that the operations do not necessarily return the code of a class. Taking the GLB of g and h gives '1100100', which is the code of none of the classes. The decoding function

$$\gamma^{-1}(c) = \lceil \{x \mid c < \gamma(x)\} \rceil$$

where $\lceil X \rceil$ is the set of maximal elements of a set of classes X, extracts the top classes subsumed by the code gives the set $\{k, l\}$ as desired, which is the set of maximal common lower bounds of g and h. Similarly, the GLB of e and f gives '11011', which results in the class m after application of the decoding function.

Storage overhead during the encoding can be minimized by not requiring the explicit storage of parent-child relationships. Instead, the existing encoding may be employed in determining these relationships, with the small cost of scanning through the codes. For example, during *Resolve-Conflicts*, the *IncSet* of a class can be found by scanning all codes and excluding those whose relationships to the parents of the node indicate a relationship with x.

The overall space required by an encoding can also be minimized by allowing variable length codes. One approach is to employ the size of the machine word. As the encoding proceeds and the code length exceeds the word size, a second word can be added to the codes of those classes that require it. Also, if the first bit of the word is used to indicated the presence of a second word, the complexity of the operations is only increased slightly. This scheme is easily extended for longer codes. In a dynamic environment, where the codes are stored in dynamic memory using pointers, each pointer will point to the first word of a code. The overall storage required will be much less than having all codes the same size.

3.2 Modulated Top-down Encoding

To further compress the encoding for each class, the notion of grouping (a simplification of modulation [2]) is used with incremental top-down encoding to allow classes to be divided into groups based on inheritance from classes at the top levels of the hierarchy. Each group is assigned a distinct code, separating it from other groups, and classes in the group are assigned codes as in top-down encoding. This enables the sharing of bit positions in class codes in different groups, while maintaining the distinction between codes through the use of the group code.

Class Name	Top-down Encoding	Partial Group	Partial Code	Modulated Group	Modulated Code
a	0	0	0	0	0
b	1	1	0	11	1000
c	10	10	0	11	100
d	100	100	0	100	0
e	1001	1	1	11	1001
f	10010	10	1	11	101
g	100100	100	1	100	1
h	1000100	100	10	100	10
i	10001001	1	10	11	1011
j	100010010	10	10	11	111
k	10001100100	100	1011	100	1011
l	1001100100	100	111	100	111
m	110011011			11	1111
n	11001100100			100	1111

Table 2: Top-down and modulated encoding results.

Consider the example in Figure 2 and the partial encoding in Table 2. Class a is placed in a separate group with code '0'. Top-level classes b, c, and d are placed in separate groups with codes '1', '10', and '100', respectively. Classes b, e, and i in group '1' are encoded as per top-down encoding with codes '0', '1', and '10', respectively, as are classes c, f, and j, which end up with the same codes in group '10'. Classes d, g, and h are assigned codes '0', '1', and '10', respectively in group '100'. Classes k and l cause a conflict within group '100', resulting in code '1011' and '111', respectively.

Multiple inheritance creates difficulties with this scheme as an added class may have parents in two or more distinct groups. This requires a group combining operation. First, the code for the new group is created by taking the binary OR of the codes of the groups involved, and all members of the groups become members of the new group. Since the group code no longer differentiates classes that share bit positions, the codes of the classes within the groups are adjusted. For each group, a new bit is added in a distinct position to the codes of all classes within the group. This results in the distinguishing of two classes which shared bit positions by the presence of the distinct new bits assigned to them.

Consider the continuation of the encoding of the example hierarchy in Figure 2. The addition of class m with parents i and j in groups '1' and '10', respectively results in the combination of the groups '1' and '10', leading to group '11', and the codes in group '10' incremented with a bit in position three, and those in group '1' with a bit in position four. The resulting codes for the entire hierarchy are presented on the right side of Table 2.

Adding further levels of grouping (that is, grouping within a group) can further compact the overall code size. Further reuse of bit positions could result, with the added expense of further considerations given to group combining with multiple inheritance. In large hierarchies, the benefits would be great. Unfortunately, the depth of grouping has its limitations. The number of classes in a group determines the

overall efficiency; too many classes, too little code reuse; too few classes, too many groups and a large group code. Also, with too much grouping multiple inheritance would lead to many group combinations, and thus too many increases in the code lengths of the classes in the groups. Experimental results indicate that the grouping should only be performed at the second or third level.

The efficiency of the lattice operations suffers slightly with modulated encoding, as an additional comparison must be performed on the group codes to determine if the classes are in the same group. If they are, then the lattice operations are the same as with non-modulated encoding. Two classes in different groups where the group codes are in a subsumption relationship must also be in a subsumption relationship. Finally, the GLB and LUB of modulated codes is the binary OR and AND respectively of both the group and class codes. The decoding function does become a bit more complex as well, and involves determining the maximal elements whose codes are subsumed by the code, using the subsumption test of modulated codes.

As an example, consider the LUB of classes i and k. The binary AND operation gives the group code 0 and class code 1011. The only element in the hierarchy subsumed by group code 0 and class code 1011 is class a, with group code 0 and class code 0. Examination of the hierarchy reveals that class a is the LUB of i and k.

3.3 Top-down Range Compression

Agrawal et al. [1] describe a modification to their range compression scheme which permits incremental updates to the hierarchy after an encoding has been performed. The idea is to leave gaps between the numbers used for nodes so as to permit the assignment of numbers in the gap to new nodes. A variation of this approach is described in this section which permits the hierarchy to be encoded in a completely incremental fashion. Index numbers are assigned to each node beginning with an arbitrarily high number for the root node. Subsequent numbers for each descendant are assigned based on gaps remaining in the numbers currently used. As long as the root index number is sufficiently high, the numbers available will not be exhausted, even with relatively large hierarchies.

The root node is assigned the value $2^{16}-1$ in our experiments. This value turned out to be practical with the use of 16-bit integers and with the sizes of the hierarchies involved. For each subsequent node, the index number is assigned so as to maximize the remaining available index numbers for descendants of each existing node. The following functions perform the encoding.

FindPlace calculates the correct index number by comparing the differences between numbers among the primary parent of the node and all of the node's siblings. A lower limit for the new index number is also calculated so that the ranges do not overlap. The two existing nodes with the largest difference in index numbers become the bounds for the new index number. If the largest difference occurs between the lowest value and the lower limit, these become the bounds. The value half way between the bounds maximizes the remaining available index numbers, and thus is used as the new index number. The range assigned to the node uses this number as both its lower and upper bounds.

Class	First 10	Adding 2 More	Complete
a	[8,64]	[4,64]	[4,64]
b	[20,32]	[20,32]	[20,32],[34,34]
c	[36,48]	[36,48]	[34,48]
d	[8,16]	[4,16]	[4,16]
e	[20,24]	[20,24]	[20,24],[34,34]
f	[36,40]	[36,40]	[34,40]
g	[8, 8]	[4, 8],[10,10]	[4, 8],[9,10]
h	[12,12]	[10,12],[4, 4]	[9,12],[4, 4]
i	[20,20]	[20,20]	[20,20],[34,34]
j	[36,36]	[36,36]	[34,36]
k		[4, 4]	[4, 4],[9, 9]
l		[10,10]	[9,10]
m			[34,34]
n			[9, 9]

Table 3: Incremental range compression results.

FindPlace(x : *class*) : *integer* ::
  $p \leftarrow$index of the 1st parent of x
  $b \leftarrow$ *FindLowerBound*(x)
  $S \leftarrow$set of indices of siblings of x
  $C \leftarrow$Sort($\{p\} \cup S \cup \{b\}$)
  return$(a+b)/2$ where (a,b) = adjacent members of C with greatest difference

FindLowerBound determines the index number of the next sibling of the first ancestor with siblings.

FindLowerBound(x : *class*) : *integer* ::
  $p \leftarrow$1st parent of x
  if $p = \emptyset$ **return** 0
  if $p.siblings = \{\}$ **return** *FindLowerBound*(p)
  else return $p.nextsibling.upperbound$

Procedure *AdjustRanges* changes the lower bounds of the new nodes' ancestors to the new index value if they do not already contain it in their range.

AdjustRanges(x : *class*, i : *integer*, c : *integer*) ::
  $R \leftarrow$interval of x which contains c
  if $R.lowerbound > i$ **then**
    $R.lowerbound \leftarrow i$
    for each $y \in Parents(x)$ **do**
      AdjustRanges($y, i, R.upperbound$)

In a multiple inheritance situation the index number of the new node is determined solely based on one of its parents (its primary parent). The other parent(s) are ignored initially. After the node has had a range assigned to it, the remaining parent(s) and their ancestors must have the range of the new node added to their set of ranges. Ancestors who already encompass this range do not require the additional range.

Consider the example hierarchy and let 64 be the largest index number used. Class a is assigned the index number 64. Classes b, c, and d are then assigned numbers 32, 48, and 16, respectively, by maximizing the gaps between values. Classes e, f, g, h, i, and j, each of which requires an index number less than that of its parent and greater than that of its parent's siblings, are assigned 24, 40, 8, 12, 20, and 36, respectively. This leads to the encoding on the left side of Table 3, where the range assigned to each class consists of the lowest index assigned to its descendants and its own

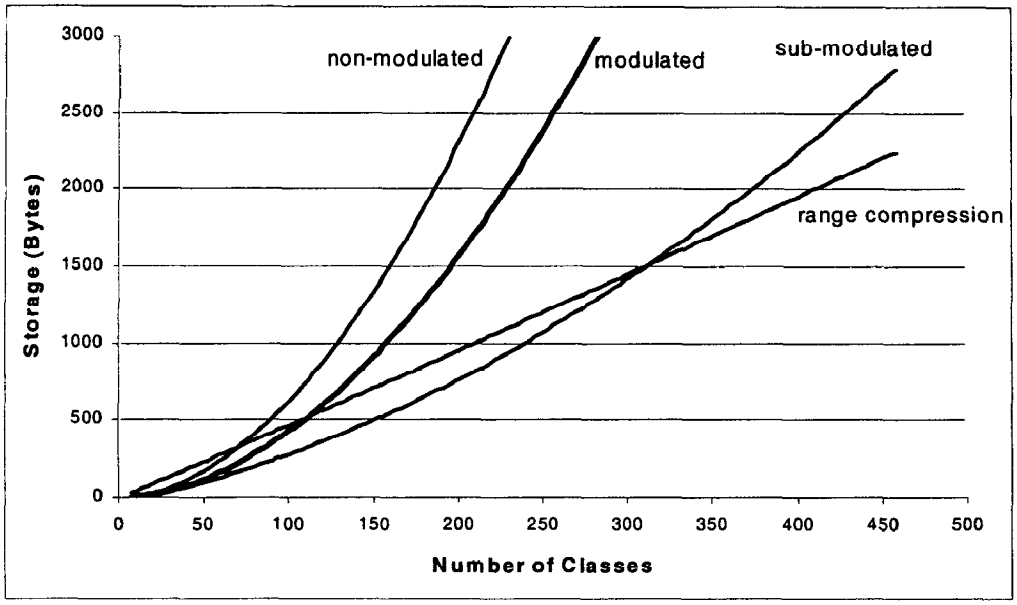

Figure 3: Results on randomly generated hierarchies.

index. Adding class k with index number 4 requires adding the second range to h due to the multiple inheritance, as well as the extension of the ranges of the ancestors a, d, and g to include 4. The case is similar for the addition of class l, except that some of the ancestors already include the new index number 10 in their ranges. This leads to the second encoding in Table 3. The addition of class m with index number 34 adds second ranges to i and its ancestors b and e and adds 34 to the ranges of c, f, and j. The final change is the addition of node n with index number 9. This adds the second range to class k and extends the ranges of g, h, and l. This is illustrated in the final encoding of Table 3.

The lattice operations for range compression are not as simple as those for top-down encoding, but they remain efficient. Subsumption can be calculated simply by comparing the ranges of each class involved. For example, classes n and h are in a subsumption relationship, as illustrated by the fact that the index number of class n, 9, is included in one of the ranges of class h, [9,12]. The GLB of two classes is calculated as the intersection of their ranges. For example, the GLB of classes i and j is calculated by intersecting the ranges of i, namely [20,20] and [34,34] with the range of j, namely [34,36], giving the range [34,34], which is precisely the range of class m, the GLB of i and j. As with top-down encoding, the resulting range may not represent a single class. The GLB of g and h gives the resulting ranges [4,4] and [9,10], which represents the classes k and l as the set of top classes subsumed by the ranges. LUB can be calculated similarly using the union of the ranges.

4 Experimental Results

To compare the encoding methods in the previous section, several hundred hierarchies were generated. For each hierarchy, there was a single top-level node, and the number of second level nodes (between four and ten) was supplied. The resulting hierarchies were pseudo-randomly generated based on a multiple inheritance probability factor of 10% and with an average depth of seven levels. This produced 380 hierarchies ranging in size from seven classes to 458 classes. Figure 3 illustrates the results of these experiments by comparing the number of classes in a hierarchy with the total number of bytes required for storing the encoding.

As expected, non-modulated top-down encoding performs poorly for most hierarchies, due to the rapid increase in code size as the hierarchies become wider. Modulated encoding reduces the code size drastically, but storage is still significantly larger than with range compression. Range compression produces a roughly linear relationship between the number of classes and storage size, since each class receives a discrete number of bytes per range and the number of ranges per class remains small.

A significant result is how well sub-modulated encoding performs. Sub-modulated encoding involves a second level of grouping; that is, groups are created at the third level of the hierarchy. Over 80% of the hierarchies encoded achieved the lowest storage using the sub-modulation method, which is significant considering the linear nature of range compression. On hierarchies with more than 300 classes, range compression achieved a more compact encoding, though the savings are at the added cost involved in the lattice operations.

To determine if these results were consistent with a real-world hierarchy, a 300-object LAURE class lattice [3] was obtained and encoded in a top-down manner using the various methods, as illustrated in Table 4. Non-modulated top-down encoding produced a code of 5,279 bytes, with the largest code being 281 bits. A top level of modulation barely reduced the size due to the fact that the top level has only four classes and multiple inheritance ends up combining all four of the resulting groups. A second level of modulation slightly reduces this. The most benefit is obtained with a third level of modulation where the resulting size is 1,452 bytes, with a largest code size of only 120 bits, and 30 groups. This result is comparable to that of range compression, where the encoding of LAURE requires 1,336 bytes.

Method	Non-modulated	Modulated	Mod-2	Mod-3	Ranges
Storage (bytes)	5279	5247	4403	1452	1336
Largest Code (bits)	281	279	252	120	-
Groups	1	2	3	31	-

Table 4: Encoding the LAURE hierarchy.

One further note on the two encoding methods is necessary. For continued growth of a large hierarchy, the choice of index number for the root node in range compression may become insufficient; that is, there may be a point in time where there is no remaining numbers to choose for a new class. In this situation, the current values will have to be adjusted - an expensive operation. With top-down incremental encoding, the number of bits being used for each class is variable, and thus the codes for new classes can be expanded without affecting the existing codes.

5 Summary

Persistent applications involving multiple inheritance hierarchies require the ability to efficiently compute lattice operations via some encoding scheme. A second requirement is the ability to allow incremental update to the hierarchy without the entire recomputation of the encoding and the reassignment of codes to classes. Previous works on encoding have focused on compile-time encodings, requiring the recalculation of codes once new classes are added. As well, the static nature of these methods requires the maintenance of significant storage overhead consisting of relationships during the encoding process.

This paper has examined several top-down, incremental encoding methodologies. Comparisons made illustrate that there is a trade-off in the final choice for a particular application. For small hierarchies (less than 300 nodes), a top-down incremental encoding with second level grouping (modulation) appears to perform best. For larger hierarchies, top-down range compression achieves a more compact encoding, with the added expense of slightly more complex lattice operations. Unfortunately range compression, as implemented, is unable to cope with very large growth of a hierarchy.

One benefit of the encodings is the ability to represent the union or disjunction of classes as a single code or range. For example, for top-down incremental encoding, the GLB of classes g and h of the example hierarchy produced the code '1100100', which represents the code of no single class. Using the decoding function this code was calculated to represent the disjunction of k and l. In some applications, the storage of the code without decoding is sufficient. For example, in the database constraint reasoning system developed in [7], the code is sufficient to determine which constraints apply to a particular object; thus decoding is only performed when required in the output.

Work is on-going to determine if relaxing the need for the LUB operation can shorten the codes required in incremental encoding. With the close relationship to the work of Caseau [4], perhaps the resulting code size will be significantly smaller. Further, work is continuing on implementing the encodings into the constraint reasoning system mentioned above.

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