

Solver setups

Name	Solver	Flags	Input
ACL2[-g]	ACL2	--time-limit 5 g: --generalize	SMT-LIB
CVC4[-Gen]	CVC4 (1.7)	--lang=smt2 --quant-ind --tlimit=5000	SMT-LIB
CVC4-Gen-1.8	CVC4 (1.8)	Gen: --conjecture-gen --lang=smt2 --quant-ind --tlimit=5000 --conjecture-gen	
Imandra	Imandra	default mode with 5 second server timeout	functional program encoding
Vampire[-gcx]	Vampire	-ind struct -t 5 --input.syntax smtlib g: -indgen on c: -indoct on x: -to lpo -drc off	SMT-LIB
Zeno	Zeno	-t 5 --no-isa	functional program encoding
ZipperPosition	ZipperPosition	-t 5	.zf (native input format)
ZipRewrite		-t 5	.zf with definitions as rewrite rules

All benchmarks were run on an Intel Core i7-4702MQ CPU @ 2.20GHz with 8GB of RAM.

Benchmarks

benchmark set	example	count	ACL2	ACL2[-g]	CVC4	CVC4-Gen	CVC4-Gen-1.8	Imandra	Vampire	Vampire.c	Vampire.cx	Vampire.g	Vampire.gc	Vampire.gcx	Vampire.gx	Zeno	ZipRewrite	ZipperPosition
combined/combined_nat_list	$\forall n, x. (cons(n + s(n), x) \text{ ++ } (x \text{ ++ } x) = (cons(s(n) + n, x) \text{ ++ } x) \text{ ++ } x)$	1	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0
list/concat_assoc_1var_10occ	$\forall v_0. ((v_0 \text{ ++ } v_0) \text{ ++ } (((v_0 \text{ ++ } v_0) \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0)))) = ((v_0 \text{ ++ } (v_0 \text{ ++ } v_0)) \text{ ++ } (v_0 \text{ ++ } v_0)) \text{ ++ } (v_0 \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0))))$	50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	36	0
list/concat_assoc_1var_3occ	$\forall v_0. (v_0 \text{ ++ } (v_0 \text{ ++ } v_0) = (v_0 \text{ ++ } v_0) \text{ ++ } v_0)$	1	0	1	0	0	0	0	0	0	0	1	1	1	1	0	0	1
list/concat_assoc_1var_4occ	$\forall v_0. (v_0 \text{ ++ } (v_0 \text{ ++ } (v_0 \text{ ++ } v_0)) = (v_0 \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0))$	10	0	0	0	0	0	0	0	1	8	9	10	10	0	0	10	0
list/concat_assoc_1var_5occ	$\forall v_0. (v_0 \text{ ++ } (((v_0 \text{ ++ } v_0) \text{ ++ } v_0) \text{ ++ } v_0) = v_0 \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0)))$	50	1	0	0	0	0	0	0	3	22	27	38	12	0	0	50	0
list/concat_assoc_1var_6occ	$\forall v_0. ((v_0 \text{ ++ } v_0) \text{ ++ } ((v_0 \text{ ++ } (v_0 \text{ ++ } v_0)) \text{ ++ } v_0) = ((v_0 \text{ ++ } v_0) \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } (v_0 \text{ ++ } v_0)))$	50	0	0	0	0	0	0	0	1	3	13	17	6	0	1	50	0
list/concat_assoc_1var_7occ	$\forall v_0. (v_0 \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0))) = (v_0 \text{ ++ } v_0) \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } (v_0 \text{ ++ } v_0))))$	50	0	0	0	0	0	0	0	2	0	5	12	1	0	0	50	0
list/concat_assoc_1var_8occ	$\forall v_0. (((v_0 \text{ ++ } v_0) \text{ ++ } v_0) \text{ ++ } (((v_0 \text{ ++ } v_0) \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0)) = (v_0 \text{ ++ } (v_0 \text{ ++ } v_0)) \text{ ++ } ((v_0 \text{ ++ } (v_0 \text{ ++ } v_0)) \text{ ++ } (v_0 \text{ ++ } v_0)))$	50	0	0	0	0	0	0	0	0	1	1	2	1	0	0	47	0
list/concat_assoc_1var_9occ	$\forall v_0. ((v_0 \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } (((v_0 \text{ ++ } v_0) \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0)))) \text{ ++ } v_0 = v_0 \text{ ++ } ((v_0 \text{ ++ } v_0) \text{ ++ } (((v_0 \text{ ++ } v_0) \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } (v_0 \text{ ++ } v_0))))$	50	0	0	0	0	0	0	0	1	1	1	2	1	0	0	47	0
list/concat_assoc_2var_4occ	$\forall x, y. (x \text{ ++ } (y \text{ ++ } (x \text{ ++ } x)) = (x \text{ ++ } y) \text{ ++ } (x \text{ ++ } x))$	1	0	1	0	0	0	0	0	0	1	1	1	1	1	0	1	1
list/concat_assoc_3var_3occ	$\forall x, y, z. (x \text{ ++ } (y \text{ ++ } z) = (x \text{ ++ } y) \text{ ++ } z)$	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
list/pref_1var_1.2occ	$\forall v_0. pref(v_0, v_0 \text{ ++ } v_0)$	1	0	1	0	0	0	0	0	0	1	1	1	1	1	0	0	0
list/pref_1var_1.3occ	$\forall v_0. pref(v_0, v_0 \text{ ++ } (v_0 \text{ ++ } v_0))$	2	0	2	0	0	0	0	0	0	1	2	2	2	2	0	1	2
list/pref_1var_1.4occ	$\forall v_0. pref(v_0, (v_0 \text{ ++ } v_0) \text{ ++ } (v_0 \text{ ++ } v_0))$	5	0	5	0	0	0	0	0	0	2	5	5	5	5	0	2	5

benchmark set	example	count	ACL2	ACL2-g	CVC4	CVC4-Gen	CVC4-Gen-1.8	Imandra	Vampire	Vampire.c	Vampire.cx	Vampire.g	Vampire.gc	Vampire.gcx	Vampire.gx	Vampire.x	Zeno	ZipRewrite	ZipperPosition
list/pref_1var_1.5occ	$\forall v_0.pref(v_0, v_0 ++ (v_0 ++ ((v_0 ++ v_0) ++ v_0)))$	14	0	14	0	0	0	0	0	0	6	14	14	14	14	0	6	14	0
list/pref_1var_2.3occ	$\forall v_0.pref(v_0 ++ v_0, (v_0 ++ v_0) ++ v_0)$	2	0	2	0	0	0	0	0	0	1	0	1	1	0	0	1	0	0
list/pref_1var_2.4occ	$\forall v_0.pref(v_0 ++ v_0, (v_0 ++ v_0) ++ (v_0 ++ v_0))$	5	0	5	0	0	0	0	0	0	1	0	2	2	0	0	1	5	0
list/pref_1var_2.5occ	$\forall v_0.pref(v_0 ++ v_0, (v_0 ++ v_0) ++ (v_0 ++ (v_0 ++ v_0)))$	14	0	14	0	0	0	0	0	0	3	0	5	5	0	0	3	14	0
list/pref_1var_2.6occ	$\forall v_0.pref(v_0 ++ v_0, ((v_0 ++ v_0) ++ (v_0 ++ v_0)) ++ (v_0 ++ v_0))$	42	0	42	0	0	0	0	0	0	9	0	15	14	0	0	9	42	0
list/pref_1var_3.3occ	$\forall v_0.pref(v_0 ++ (v_0 ++ v_0), (v_0 ++ v_0) ++ v_0)$	2	0	0	0	0	0	0	0	0	0	2	1	1	2	0	0	2	0
list/pref_1var_3.4occ	$\forall v_0.pref((v_0 ++ v_0) ++ v_0, (v_0 ++ (v_0 ++ v_0)) ++ v_0)$	10	0	0	0	0	0	0	0	0	2	0	2	2	0	0	2	0	0
list/pref_1var_3.5occ	$\forall v_0.pref(v_0 ++ (v_0 ++ v_0), v_0 ++ (((v_0 ++ v_0) ++ v_0) ++ v_0))$	28	0	0	0	0	0	0	0	0	4	0	4	4	0	0	4	28	0
list/pref_1var_3.6occ	$\forall v_0.pref(((v_0 ++ v_0) ++ v_0, (v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ (v_0 ++ v_0)))$	50	0	0	0	0	0	0	0	0	4	0	7	6	0	0	6	50	0
list/pref_1var_3.7occ	$\forall v_0.pref(v_0 ++ (v_0 ++ v_0), (v_0 ++ (v_0 ++ (v_0 ++ v_0))) ++ (v_0 ++ (v_0 ++ v_0)))$	50	0	0	0	0	0	0	0	0	6	0	8	8	0	0	8	50	0
list/pref_1var_4.4occ	$\forall v_0.pref(((v_0 ++ v_0) ++ v_0) ++ v_0, (v_0 ++ (v_0 ++ v_0)) ++ v_0)$	20	0	0	0	0	0	0	0	0	0	0	5	6	0	0	0	20	0
list/pref_1var_4.5occ	$\forall v_0.pref(v_0 ++ (v_0 ++ (v_0 ++ v_0)), v_0 ++ ((v_0 ++ v_0 ++ v_0) ++ v_0))$	50	0	0	0	0	0	0	0	0	5	0	7	6	0	0	5	0	0
list/pref_1var_4.6occ	$\forall v_0.pref((v_0 ++ (v_0 ++ v_0)) ++ v_0, v_0 ++ (v_0 ++ (v_0 ++ (v_0 ++ (v_0 ++ v_0)))))$	50	0	0	0	0	0	0	0	0	3	0	3	3	0	0	4	50	0
list/pref_1var_4.7occ	$\forall v_0.pref((v_0 ++ v_0) ++ (v_0 ++ v_0), ((v_0 ++ v_0) ++ (v_0 ++ v_0)) ++ ((v_0 ++ v_0) ++ v_0))$	50	0	0	0	0	0	0	0	0	2	0	2	2	0	0	2	50	0
list/pref_1var_4.8occ	$\forall v_0.pref((v_0 ++ v_0) ++ (v_0 ++ v_0), ((v_0 ++ (v_0 ++ v_0)) ++ (v_0 ++ v_0)) ++ ((v_0 ++ v_0) ++ v_0))$	50	0	0	0	0	0	0	0	0	2	0	2	2	0	0	2	50	0
list/pref_1var_5.5occ	$\forall v_0.pref((v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ v_0), ((v_0 ++ v_0) ++ v_0) ++ (v_0 ++ v_0))$	50	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	50	0
list/pref_1var_5.6occ	$\forall v_0.pref(v_0 ++ (((v_0 ++ v_0) ++ v_0) ++ v_0), (v_0 ++ v_0) ++ (v_0 ++ ((v_0 ++ v_0) ++ v_0)))$	50	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
list/pref_1var_5.7occ	$\forall v_0.pref(v_0 ++ (v_0 ++ (v_0 ++ (v_0 ++ v_0))), v_0 ++ (v_0 ++ ((v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ v_0))))$	50	0	0	0	0	0	0	0	0	1	0	1	1	0	0	2	50	0
list/pref_1var_5.8occ	$\forall v_0.pref((v_0 ++ (v_0 ++ v_0)) ++ (v_0 ++ v_0), (v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ (v_0 ++ v_0))))$	50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	50	0
list/pref_1var_5.9occ	$\forall v_0.pref(v_0 ++ ((v_0 ++ v_0) ++ (v_0 ++ v_0)), (v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ ((v_0 ++ v_0) ++ v_0))))$	50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	3	50	0
list/pref_2var_1.2occ	$\forall x.y.pref(x, x ++ y)$	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
list/reverse_expressions	$\forall x.(revAcc(x) = rev(x))$	4	0	0	0	0	0	1	0	0	1	2	2	2	2	0	0	2	0
nat/add_assoc_1var_10occ	$\forall v_0.(((v_0 + v_0) + ((v_0 + v_0) + (v_0 + (v_0 + v_0)))) + ((v_0 + v_0) + v_0) = (((((v_0 + v_0) + v_0) + v_0) + (v_0 + v_0)) + v_0) + (v_0 + (v_0 + v_0))))$	50	0	0	0	50	0	0	0	0	3	0	0	1	0	0	2	43	0
nat/add_assoc_1var_3occ	$\forall v_0.(v_0 + (v_0 + v_0) = (v_0 + v_0) + v_0)$	1	0	1	0	1	0	0	0	0	1	1	1	1	1	0	1	1	0
nat/add_assoc_1var_4occ	$\forall v_0.((v_0 + (v_0 + v_0)) + v_0 = ((v_0 + v_0) + v_0) + v_0)$	10	0	0	0	10	0	0	0	0	4	8	10	10	10	0	2	10	0
nat/add_assoc_1var_5occ	$\forall v_0.(((v_0 + v_0) + (v_0 + v_0)) + v_0 = (v_0 + (v_0 + v_0)) + (v_0 + v_0))$	50	1	0	0	50	0	0	0	0	12	13	22	41	17	0	6	50	0
nat/add_assoc_1var_6occ	$\forall v_0.((v_0 + ((v_0 + v_0) + v_0)) + (v_0 + v_0) = ((v_0 + v_0) + ((v_0 + v_0) + v_0)) + v_0)$	50	3	0	0	50	0	0	0	0	12	2	14	31	9	0	11	50	0
nat/add_assoc_1var_7occ	$\forall v_0.(((v_0 + v_0) + v_0) + (v_0 + (v_0 + (v_0 + v_0)))) = ((v_0 + v_0) + v_0) + ((v_0 + (v_0 + v_0)) + v_0)$	50	0	0	0	50	0	0	0	0	6	1	5	12	1	0	2	50	0

benchmark set	example	count	ACL2	ACL2-g	CVC4	CVC4-Gen	CVC4-Gen-1.8	Inandra	Vampire	Vampire.c	Vampire.cx	Vampire.g	Vampire.gc	Vampire.gcx	Vampire.gx	Vampire.x	Zeno	ZipRewrite	ZipperPosition
nat/add_s_zero_mix_18	$\forall v_0, v_1, v_2, v_3, v_4, v_5, v_6. (s(s(s(s(s(v_5) + s(v_4)) + v_3)))) + (v_0 + s(s(s(v_6) + v_6) + (v_2 + s(v_1)))) = s(s(s(v_2 + (s(s(s(v_4)) + (((s(v_6) + v_6) + v_0) + (s(s(v_3)) + s(v_1)))))) + v_5)))$	50	0	0	0	0	0	1	0	0	1	0	0	1	2	5	1	0	0
nat/add_s_zero_mix_21	$\forall v_0, v_1, v_2, v_3, v_4, v_5, v_6, v_7. s(((s(v_7) + v_4) + s(s(s(s(v_5)))))) + s(s(s(s(v_2)) + (s(v_1) + (v_0 + s(v_3)))) + s(v_6))) = s(s((s(s(v_2)) + s(s(v_7))) + ((s(v_3 + (v_6 + s(s(s(v_1 + v_4)))))) + s(s(s(v_5)))) + v_0))))$	50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
nat/add_s_zero_mix_24	$\forall v_0, v_1, v_2, v_3, v_4, v_5, v_6, v_7, v_8. (((s(zero) + (s(s(v_6) + v_7) + (v_3 + s(v_4)))) + s(s(s(v_2)))) + s(s(s(v_1) + v_8) + s(s(v_5) + (s(v_4) + v_0))) = s(s((s(s(s(s(s(v_4) + v_4) + ((v_8 + v_5) + s(s(s(v_3 + s(s(s(v_2 + (v_6 + s(v_0)))))))))) + zero))) + v_7) + v_1)))$	50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
nat/add_s_zero_mix_27	$\forall v_0, v_1, v_2, v_3, v_4, v_5, v_6, v_7, v_8. (s(s(s(s(v_0) + v_6)) + s(s(s(v_5) + (s(s(s(v_3) + v_7)) + s(s(s(s(s(s(v_4)) + s(v_2)) + (s(v_1) + v_8)))))))) = s(v_2 + s(s(s(v_3)))) + (s(s(s(s(s(s(s(s(s(v_5) + s(v_4 + s(v_0))) + s(v_7))) + s(v_1))) + v_6)))) + v_8))$	50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
nat/add_s_zero_mix_3	$\forall v_0, v_1. (v_1 + s(v_0) = s(v_0 + v_1))$	34	14	18	32	34	31	29	32	32	34	34	34	34	34	32	30	34	32
nat/add_s_zero_mix_30	$\forall v_0, v_1, v_2, v_3, v_4, v_5, v_6, v_7, v_8, v_9. (s(s(s(v_7))) + ((v_6 + s(s(s(v_4) + s(v_5)))) + (s(v_8) + s(s(((v_3 + (s(v_7) + v_1)) + s(s(zero))) + s(s(v_9 + s(s(v_2) + s(v_0))))))) = ((s(s((v_4 + ((s(v_7) + v_1) + v_7)) + s(s(s(s(s(s(v_8) + s(s(s(v_9)))))))))) + v_0) + v_2) + (s(v_5) + (s(s(zero + s(s(s(v_3)))) + v_6))))$	50	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
nat/add_s_zero_mix_6	$\forall v_0. (s(s(s(s(v_0)) + zero)) = s(zero + s(s(s(v_0))))$	50	7	8	29	33	26	37	32	33	44	33	32	50	49	37	25	47	34
nat/add_s_zero_mix_9	$\forall v_0, v_1, v_2. ((s(s(s(s(s(v_2)))) + s(v_0)) + v_1 = s(s(s(s(v_2)))) + s(v_0 + s(v_1)))$	50	7	4	17	18	10	30	12	14	31	10	8	37	40	31	13	30	12
nat/equal	$\forall x. equal(x, x)$	4	1	0	2	4	2	2	1	1	1	2	2	2	2	1	0	4	2
nat/leq_lvar_1.2occ	$\forall v_0. (v_0 \leq v_0 + v_0)$	1	0	1	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0
nat/leq_lvar_1.3occ	$\forall v_0. (v_0 \leq v_0 + (v_0 + v_0))$	2	0	2	0	0	0	0	0	0	1	2	2	2	2	0	2	2	0
nat/leq_lvar_1.4occ	$\forall v_0. (v_0 \leq (v_0 + (v_0 + v_0)) + v_0)$	5	0	5	0	0	0	0	0	0	3	5	5	5	5	0	4	5	0
nat/leq_lvar_1.5occ	$\forall v_0. (v_0 \leq (v_0 + (v_0 + v_0)) + (v_0 + v_0))$	14	0	14	0	0	0	0	0	0	7	14	14	14	14	0	12	14	0
nat/leq_lvar_2.3occ	$\forall v_0. (v_0 + v_0 \leq (v_0 + v_0) + v_0)$	2	0	2	0	0	0	0	0	0	1	2	2	1	1	0	2	0	0
nat/leq_lvar_2.4occ	$\forall v_0. (v_0 + v_0 \leq (v_0 + (v_0 + v_0)) + v_0)$	5	0	5	0	0	0	0	0	0	2	1	2	2	0	0	2	5	0
nat/leq_lvar_2.5occ	$\forall v_0. (v_0 + v_0 \leq (v_0 + (v_0 + v_0)) + (v_0 + v_0))$	14	0	14	0	0	0	0	0	0	5	0	5	5	0	0	6	14	0
nat/leq_lvar_2.6occ	$\forall v_0. (v_0 + v_0 \leq (v_0 + (((v_0 + v_0) + v_0) + v_0)) + v_0)$	42	0	42	0	0	0	0	0	0	13	0	16	14	0	0	11	42	0
nat/leq_lvar_3.3occ	$\forall v_0. (v_0 + (v_0 + v_0) \leq (v_0 + v_0) + v_0)$	2	0	2	0	2	0	0	0	0	0	2	2	2	2	0	2	2	0
nat/leq_lvar_3.4occ	$\forall v_0. ((v_0 + v_0) + v_0 \leq (v_0 + (v_0 + v_0)) + v_0)$	10	0	0	0	0	0	0	0	0	2	0	2	2	0	0	4	0	0
nat/leq_lvar_3.5occ	$\forall v_0. ((v_0 + v_0) + v_0 \leq v_0 + ((v_0 + v_0) + (v_0 + v_0)))$	28	0	0	0	0	0	0	0	0	4	0	5	4	0	0	8	28	0
nat/leq_lvar_3.6occ	$\forall v_0. ((v_0 + v_0) + v_0 \leq ((v_0 + v_0) + (v_0 + v_0)) + (v_0 + v_0))$	50	0	0	0	0	0	0	0	0	8	0	11	10	0	0	17	50	0
nat/leq_lvar_3.7occ	$\forall v_0. (v_0 + (v_0 + v_0) \leq ((v_0 + v_0) + (v_0 + v_0)) + ((v_0 + v_0) + v_0))$	50	0	0	0	0	0	0	0	0	9	0	9	9	0	0	28	50	0

[illegible]