£ *	- DE Assignment:
	Nome: Vyom Raval Jaimin Balvi
	Taimin Salvi
	Class CSE E 22 BCE301 & Roll HD : 22 BCE310
16191	Ele vato & Simulation: - Roll No 22 BCE310
Jo 100	IN IT OF THE PROPERTY OF THE P
	Sequential Design
× 0	Thorse and con o
×	
× 1	be called to any Floor, but must Proceed through Intermediate Floors before reading
*	through antesmediate thouse
X	the destination.
0 <i>y</i>	States input
	Succession of the first of the
× 1	6 1 2 3 1 6 1 2 3 1 1
× 1	00 01 10 11 00 01 10 11 0 1
X.O	$\bigcirc \times \bigcirc \longrightarrow \bigcirc \longrightarrow \bigcirc \bot$
x• 1	State Diagram
1 ×	0 1,2,3
X	(21) 1
1 X	G(100) X (01)
. U Y	G d Q
	2,1, 6
	(1) (1) (10) (10) (10) (10) (10) (10) (1
	2
	3 3 4 5 0 0
	· · · · · · · · · · · · · · · · · · ·
	0 X 1 1

- DE Asymment :-

ivlos mont

(Present) States	Stee all	(Earliest Hext State) States and Marie	Tolk-Alle Valor	Inputs	-
0, 00	F ₁ F ₀	0, 0,	J, k,	Jo ko	
	Access to the second	100	of bidings	*	
00	0 0	0 0	0 ×	0 ×	
00 (10)	0 100	20017 1 End	Anx elostos	el x	
0 00000	.1120n b	to any 1 FIDIO. U	bex coch	1 ×	
O Qaiba	1 100000	Ocourt 1 striberne	dato dixordi.	l x	
01	0 0	0.0.0	the x domati	X I	U
01	0 1	0 1	0 X	XO	THE PERSON NAMED IN COLUMN TWO IS NOT THE PERSON NAMED IN COLUMN TWO IS NAMED IN COLUMN TW
01	1 10900	1.1.0	Le testex	×I	DOSEDINA
01	1 1	1 0	L X	× 1	ACCOUNT.
10 0	001	0 0 1	G 11 x 3	1 ×	1
10 11	01 1 10	0001	LL CX LO OO	1 ×	
10	10	1 0	X O	OX	
10	1 1	1 1	State X Diagon	1 ×	
11	00	1 0	C X4,2,3		
11	01	1 0	X O	× I	
11	10	T_ (00),	x (00)	× I	in the second
11	11	1 1	> ×10	X O	
4 11		91). 6.3			1

As Per Excitation

Table of J-k flip flop:

Q	Q [†]	k J
0	0	Ox
0	1	i ×
1	0	× 1.
1	1	ΧO

	_
/	
1	m
1.	۰

	Q100 F1 F000 01 10 11 10 11 10 10 10 10 10 10 10
	$\begin{array}{cccccccccccccccccccccccccccccccccccc$
	2,00 1600 01 110 1117
	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
0	0,00 ff 00 01 10 11 Jo = Fo + O, Fi + O, F,
	01 X X X X X X 10 X X X X X X X X X X X
c	$k_0 = F_0 + O(F_1 + O(F_1))$ $= F_0 + O(F_1 + O(F_1))$
11	
	· (2080,000)
	0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0



- * Components Included:
 - 1) Priority Encoder
- 2) Basic Logic Gates i.e. AND, OR, XDR, etc
- 3) J-k Flip Flop , D Flip Flop
- 4) 7- segment Display
- 5) Multiplexe &
- 6) De-HWHIPlexex

Random Sequence Counter (0-4-1-2-3-5-0);

present state			esent state Next state			fup flop		
A(t)	B(+)	C(t)			((4.41)	DA	DR	De
0	0	0	2	0	001	. 1,	0.	00
103	2 -07	O 0 7 =	O ke	0	1 1	0	0	1
. 0	0	10/+ Jt =	0	1.	0	0,	1.	0
0	1	0	0	1	110	0	11	1
0	1	1		0	× 1 -	1	O	11
1	0		0	0	0	0	0	0

Equations?