



**BOARD OF SECONDARY EDUCATION
ANDHRA PRADESH**



Head Master

Siddhartha Gurukula Vidyalayam
CHENNARAOPET-106 312

SECONDARY SCHOOL CERTIFICATE

PC/32/0202373/1

CERTIFIED THAT **GADDAMEDI SHIVARAJU**

S/O **ILAIAH**

Bearing Roll No. **0581975**

belongs to

SIDDARTHA G VIDYALAYAM CHENNARAOPET

has appeared and PASSED at the SSC EXAMINATION held in

MAR, 1998

with

FIRST

division and

TELUGU

as the medium of instruction

DATE OF BIRTH OF
THE CANDIDATE

DAY

MONTH

YEAR

08/06/1983

ZERO

EIGHT

JUN

ONE

NINE

EIGHT

THREE

THE CANDIDATE SECURED THE FOLLOWING PERCENTAGE OF MARKS

SUBJECT	Marks secured (in Figures)	Marks secured (in Words)
FIRST LANGUAGE (TELUGU)	70	SEVEN ZERO
THIRD LANGUAGE - ENGLISH	50	FIVE ZERO
MATHEMATICS	75	SEVEN SIX
GENERAL SCIENCE	69	SIX NINE
SOCIAL STUDIES	78	SEVEN EIGHT
TOTAL	343	(THREE FOUR THREE)
SECOND LANGUAGE (HINDI)	68	SIX EIGHT
GRAND TOTAL	411	(FOUR ONE ONE)

Marks of Identification

1. Amole on the right hand wrist.
- 2.

Head of Institution
with School Stamp

Head Master
**Siddhartha Gurukula Vidyalayam
CHENNARAOPET-106 312**

Date of Issue by the Board

25/05/1998.

SECRETARY
BOARD OF SECONDARY EDUCATION

HYDERABAD

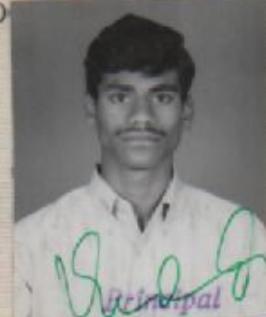
Any corrections in the certificate will not be entertained after 3 years from the date of issue.

Any unauthorised correction in the certificate will result in cancellation of certificate.



Board of Intermediate Education A. P.

Vidya Bhavan, Nampally, Hyderabad- 500 003



Sri Ravanti Junior College
DHARMARAM - 506 330

Dist. Warangal-(A. P.)
son XXXX001ex

PASS CERTIFICATE CUM MEMORANDUM OF MARKS

This is to certify that **GADDAMEDDI SHIVARAJU**

of **ILIAH**

passed the

Intermediate Public Examination held in **MARCH/2000**

with Registered No. : **5267656** in **TELUGU** medium

and is placed in **FIRST DIVISION.**

The subjects in which he/she is examined and the marks awarded are as follows :

Subject	Paper-I		Paper-II				
	Maximum Marks	Marks Obtained	Maximum Marks	Marks Obtained			
Part - 1 : ENGLISH	100	038	100	038			
Part - 2 : SANSKRIT	100	090	100	095			
Part - 3 : Optional Subjects							
MATHEMATICS-A	075	051	075	031			
MATHEMATICS-B	075	060	075	031			
PHYSICS	060	048	060	031			
PHYSICS PRACTICAL			030	027			
CHEMISTRY	060	038	060	021			
CHEMISTRY PRACTICAL			030	029			
Total Marks In Figures	628	 					
In Words	SIX "TWO***EIGHT"						
Date	26-05-2000						

Principal
Signature of the Principal and College Seal

Sri Ravanti Junior College

DHARMARAM - 506 330

NOTE: LEGIBLE MARKS ARE OVERLEAF. * INSET MARKS OBTAINED AT EARLIER EXAMINATION.

Dist. Warangal - (A. P.)



Controller of Examinations

02

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

College: 65 - ADAMEC, PALWANCHA

HT No: 01651A0241



30269000035

SI. No. PC 00100056



PROVISIONAL CERTIFICATE

20269000048

This is to certify that Mr. GADDAMEDI SHIVARAJU
son of Mr. ILAIAH
passed B. Tech. (ELECTRICAL & ELECTRONICS ENGINEERING) degree
examination of this University, held in November, 2009 and that he was
placed in SECOND CLASS.

He has satisfied all the requirements for the award of the degree.

Verified by

Hyderabad - A.P.

K. Ram Ling
Controller of Examinations

E Sri Baba Rao

REGISTRAR



Date: February 24, 2010



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA**



CONSOLIDATED MARKS MEMO / CREDIT SHEET

BACHELOR OF TECHNOLOGY- ELECTRICAL & ELECTRONICS ENGINEERING

CMM. No. : **C 0109644**

Serial No. : 20269000048

Name : GADDAMEDI SHIVARAJU

Hall Ticket No. : 01651A0241

Year of Admission : 2001-2002

Name of the College : A D A M E C, PALWANCHI

Month & Year of Final Exam : November, 2009

Class Awarded : SECOND CLASS

S.No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
I YEAR											
1	ENGLISH	12	47	59	6	2	MATHEMATICS - I	12	48	60	6
3	APPLIED PHYSICS	12	37	49	4	4	COMPUTERS & INFORMATION TECHNOLOGY	12	28	40	6
5	NETWORK THEORY	12	36	48	6	6	SEMICONDUCTOR DEVICES & CKTS.	12	60	72	6
7	C & DATA STRUCTURES	08	40	48	6	8	COMPUTER PROGRAMMING (LAB)	07	21	28	8
9	SEMICONDUCTOR DEVICES & CKTS. (LAB)	08	20	28	4	10	ENGINEERING DRAWING PRACTICE	08	21	29	4
I SEMESTER			II YEAR			II SEMESTER					
1	MATHEMATICS - II	12	38	50	4	1	CONTROL SYSTEMS	17	28	45	4
2	PULSE AND DIGITAL CIRCUITS	08	61	69	4	2	ELECTRO MECHANICS - II	12	33	45	4
3	SWITCHING THEORY & LOGIC DESIGN	08	32	40	4	3	POWER SYSTEMS - I	12	28	40	4
4	PRIME MOVERS & PUMPS	08	32	40	4	4	MANAGERIAL ECON. & PRIN. OF ACC.	14	36	50	4
5	ELECTRO MAGNETIC FIELDS	12	42	54	4	5	LINEAR & DIGITAL I.C. APPLICATIONS	12	40	52	4
6	ELECTRO MECHANICS - I	12	28	40	4	6	MATHEMATICS - III	08	50	58	4
7	PRIME MOVERS & PUMPS (LAB)	08	21	29	2	7	IC & PULSE & DIGITAL CIRCUITS (LAB)	07	33	40	2
8	NETWORKS (LAB)	08	30	38	2	8	ELECTRO MECHANICS (LAB)	06	31	37	2
I SEMESTER			III YEAR			II SEMESTER					
1	COMPUTER ORGANIZATION	10	37	47	4	1	ANALYSIS OF LINEAR SYSTEMS	10	30	40	4
2	ELECTRICAL MEASUREMENTS	09	31	40	4	2	MICROPROCESSORS AND INTERFACING	06	34	40	4
3	POWER SYSTEMS-II	13	28	41	4	3	MODELLING OF POWER SYSTEM COMPONENTS	18	48	66	4
4	POWER ELECTRONICS	13	38	51	4	4	INSTRUMENTATION	13	28	41	4
5	ELECTROMECHANICS-III	09	32	41	4	5	POWER SYSTEMS - III	10	42	52	4
6	MANAGEMENT SCIENCE	13	28	41	4	6	UTILIZATION OF ELECTRICAL ENERGY	09	40	49	4
7	ELECTROMECHANICS (LAB) - II	20	32	52	2	7	ELECTRICAL MEASUREMENTS LAB	22	31	53	2
8	CONTROL SYSTEMS (LAB)	15	25	40	2	8	POWER ELECTRONICS LAB	18	30	48	2
I SEMESTER			IV YEAR			II SEMESTER					
1	DIGITAL SIGNAL PROCESSING	13	28	41	4	1	H V D C TRANSMISSION	17	33	50	4
2	POWER SEMICONDUCTOR DRIVES	16	31	47	4	2	PRINCIPLES OF SOFTWARE ENGG.	18	44	62	4
3	COMPUTER METHODS IN POWER SYS.	15	39	54	4	3	PROJECT & SEMINAR #	20	150	170	8
4	POWER SYSTEM OPERATION & CONTROL	18	33	51	4						
5	NON-CONVENTIONAL ENERGY SOURCES	20	28	48	4						
6	NEURAL NETWORKS&APPLICATIONS	17	34	51	4						
7	MICROPROCESSORS LAB	12	28	40	2						
8	SIMULATION OF ELEC. SYS. LAB	20	39	59	2						

(# Project Internal=40, External=160)

Number of Credits registered for : 212 Aggregate Marks Secured for best : 212

Aggregate Marks Secured : 2603 OUT OF 4900 (53.12)

Date of Issue : February 24, 2010

(see overleaf for Rules concerned to award of class)

(*Courses registered but not counted for calculation of aggregate)

K. Ram Ling
CONTROLLER OF EXAMINATIONS