

Introduction to Mediatek QoS

2013/7/16











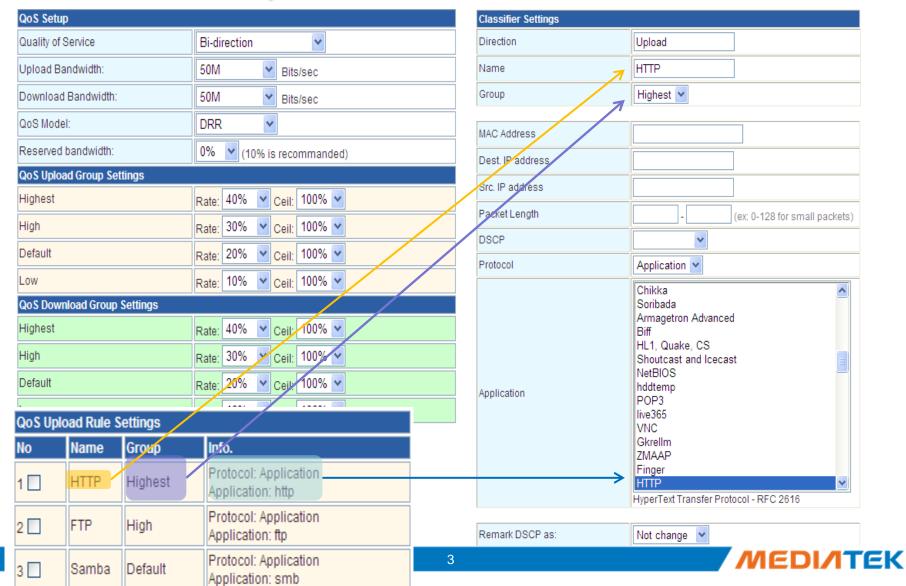
- Overview of QoS Web UI
- SW QoS (TC, traffic control)
 - DRR
 - SPQ
 - Mixed
 - Fairness QoS
- HW QoS (QDMA, MT7621)
 - Replace DRR
 - Replace SPQ
 - Replace Mixed
- Test Result



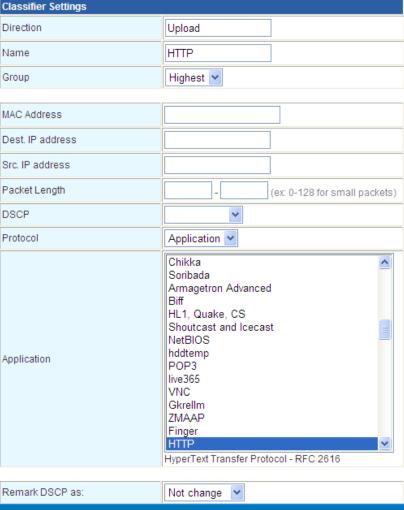
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Mediatek QoS Web UI



Mediatek QoS Web UI



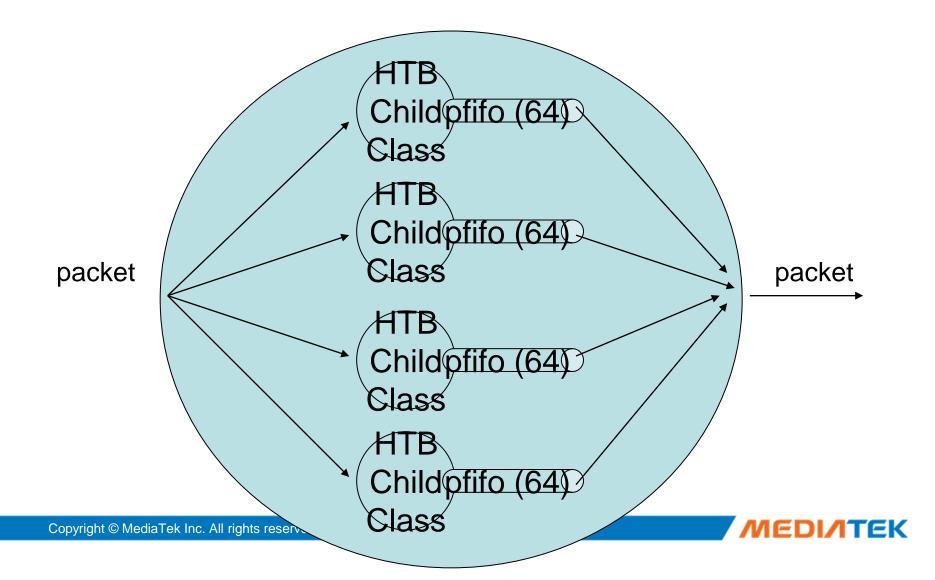
Mediatek SW QoS supports these classifiers currently:

- a) SRC/DSP IP address (with netmask)
- b) Packet length
- c) DSCP field
- d) ICMP, TCP/UDP port range
- e) Layer 7 (content inspection)

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QoS Model: DRR(HTB, Rate limited)



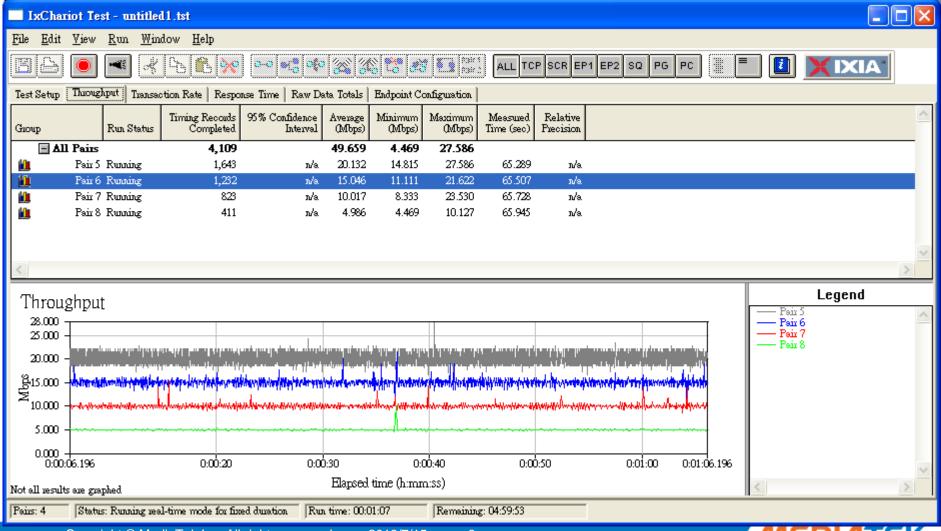
QoS Model: DRR(HTB, Rate limited)

Four QoS groups are shown after specifying Global settings in Ralink SW QoS. Now all packets through this gateway are classified into these four QoS groups according to the user's QoS rules settings. The four QoS groups are subsequently shown.

Quality of Service Settings				
You may setup rules to provide Quality of Service guarantees for specific applications.				
QoS Setup				
Quality of Service	Download from Internet			
Upload Bandwidth:	32M Bits/sec			
Download Bandwidth:	32M Bits/sec			
QoS Model:	DRR			
Reserved bandwidth:	0% (10% is recommanded)			
QoS Download Settings				
Highest	Rate: 10% V Ceil: 100% V			
High 4 groups	Rate: 10% V Ceil: 100% V			
Default	Rate: 10% V Ceil: 100% V			
Low	Rate: 10% V Ceil: 100% V			
Submit				



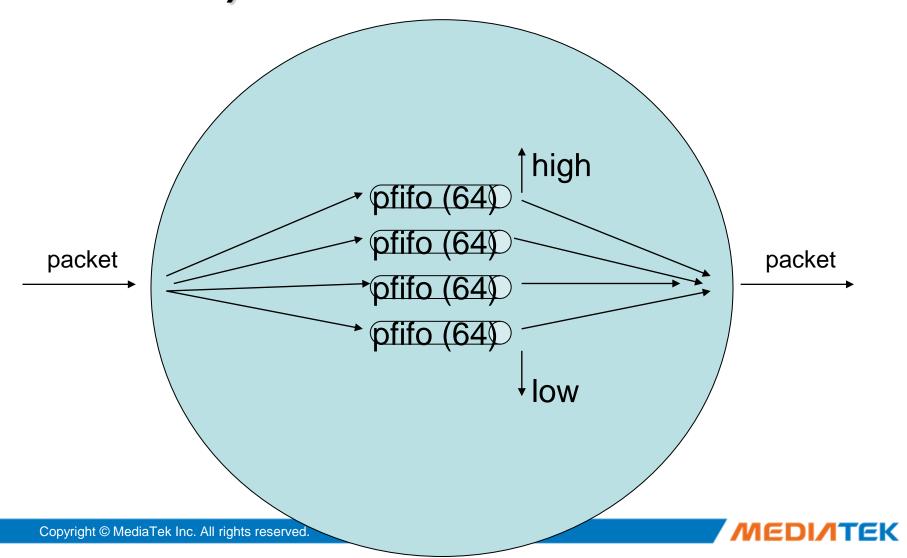
QoS Model: DRR(HTB, Rate limited)



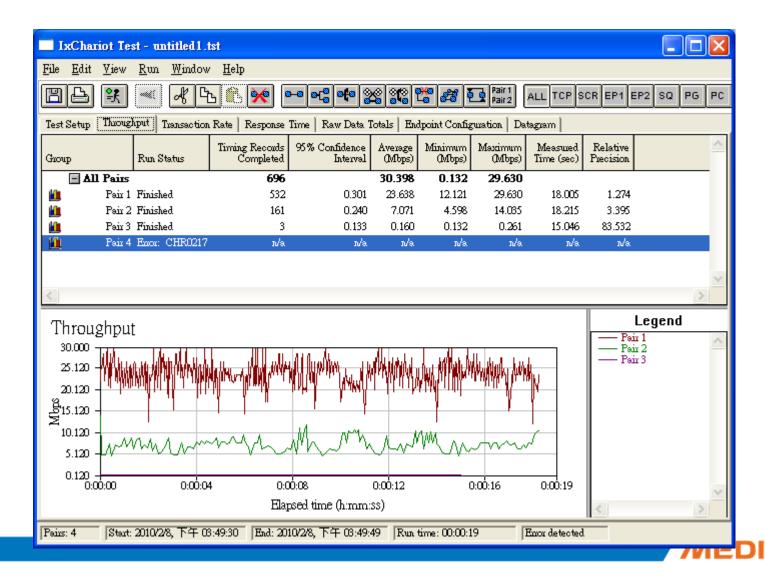
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QoS Model: SPQ(PRIO, Priority schedule)



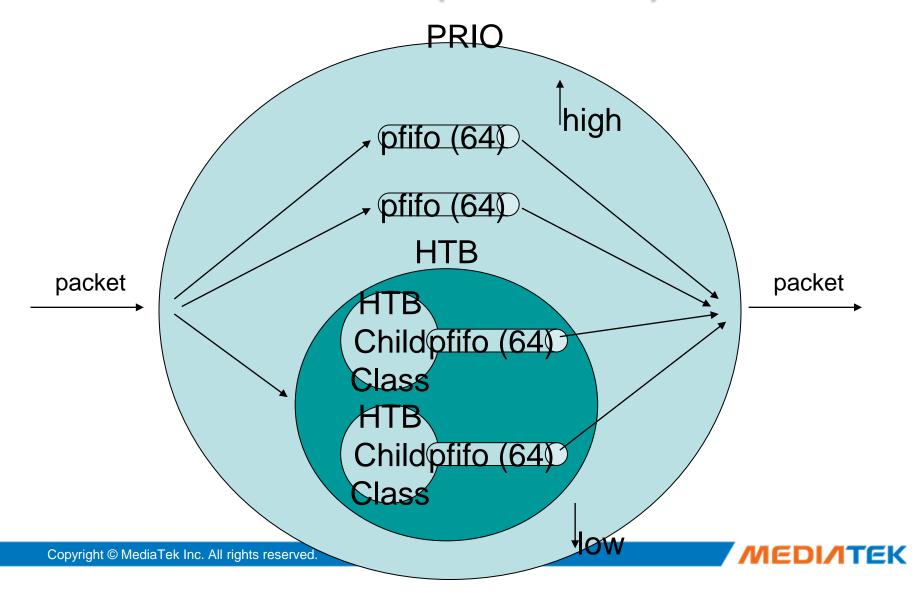
SPQ (Priority, upload, UDP)



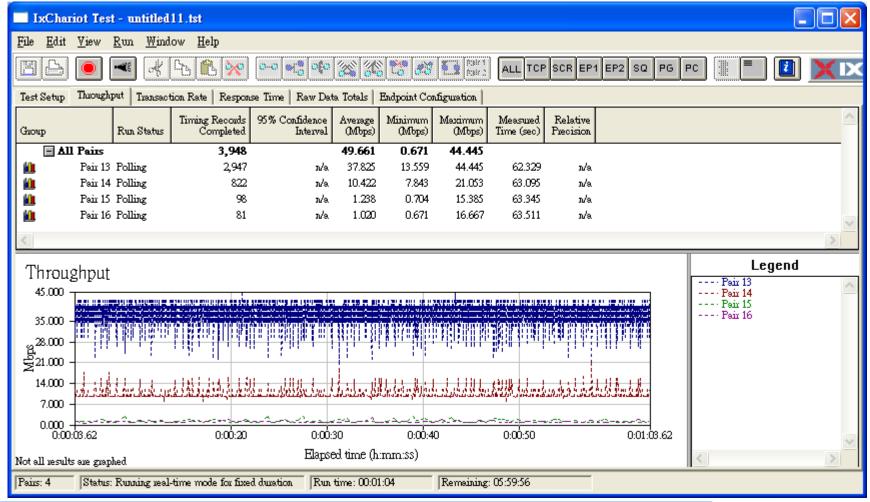
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QoS Model: Mixed(SPQ+HTB)



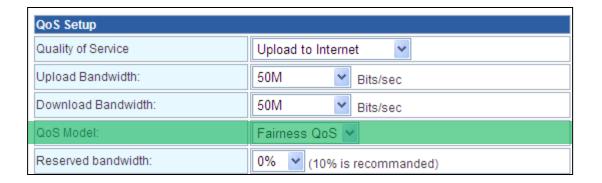
QoS Model: Mixed(SPQ+HTB)



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QoS Model: fairness QoS



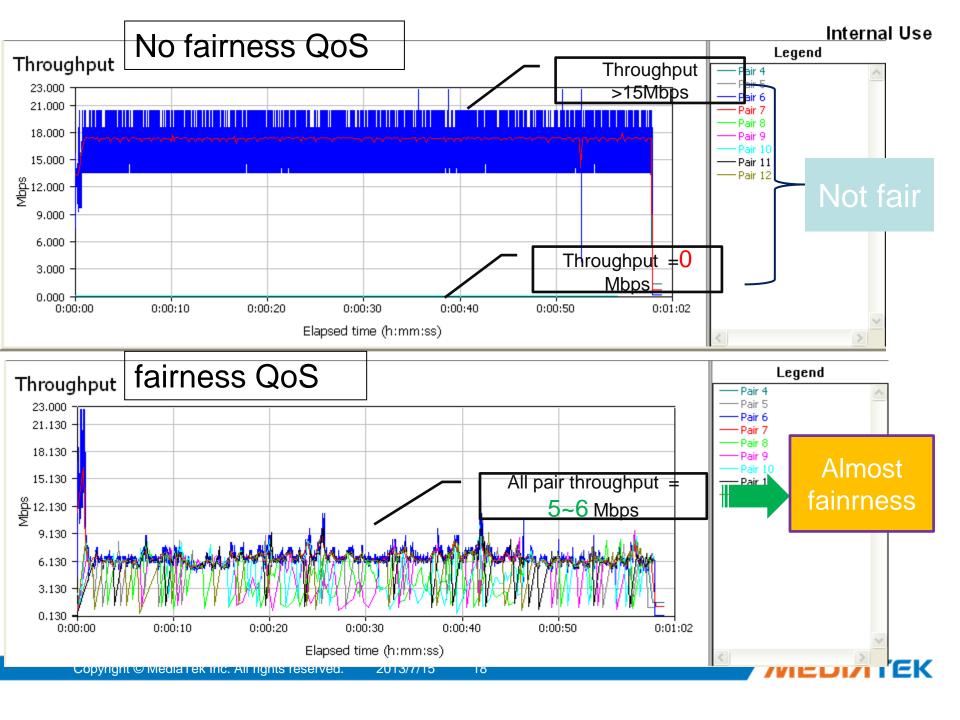
Fairness QoS does not shape traffic but only schedules the transmission of packets, based on 'flows'. The goal is to ensure fairness so that each flow is able to send data in turn, thus preventing any single flow from drowning out

QoS Model: fairness QoS

Test condition: 9 pair

Group		Run Status	Timing Records Completed		Endpoint 2	Network Protocol	Service Quality	Script/Stream Filename
=	All Pairs		4,426					
	Pair 4	Finished: Warning(s)	135	10.10.10.3	10.10.20.13	RTP		IPTVv.scr
	Pair 5	Finished	414	10.10.10.3	10.10.20.13	TCP		Filesndl.scr
	Pair 6	Finished: Warning(s)	1,917	10.10.10.3	10.10.20.13	RTP		IPTVa.scr
	Pair 7	Finished	136	10.10.10.3	10.10.20.13	UDP		IPTVv.scr
	Pair 8	Finished	314	10.10.10.3	10.10.20.13	TCP		Filesndl.scr
	Pair 9	Finished	351	10.10.10.3	10.10.20.13	TCP		Filesndl.scr
	Pair 10	Finished	359	10.10.10.3	10.10.20.13	TCP		Filesndl.scr
	Pair 11	Finished	407	10.10.10.3	10.10.20.13	TCP		Filesndl.scr
	Pair 12	Finished	393	10.10.10.3	10.10.20.13	TCP		Filesndl.scr





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How to Classify Packets

We want to set these four priority.

```
VI = min 50Mbps, max = unlimited (Highest)
```

VO = min 20Mbps, max = unlimited(High)

BK = min 5Mbps, max = unlimited(Default)

BG = min 1Mbps, max = unlimited (Low)

We can use DSCP value as classify rule.

DS5 DS4	DS3	Dea	D01	Deu	ECN	ECN
DS5 DS4 (P2) (P1)	(P0)	D32	ופט	D30	ECIV	ECIV

P2 P1 P0	802.1D	AC	
000	0	BE	
001	1	BK	CS1
010	2	BK	CS2
011	3	BE	CS3
100	4	VI	CS4
101	5	VI	CS5
110	6	VO	CS6
111	7	VO	CS7

iptables -t mangle -A qos_prerouting_rule_chain -m dscp --dscp-class CS4 -j MARK --set-mark 52 (VI) iptables -t mangle -A qos_prerouting_rule_chain -m dscp --dscp-class CS6 -j MARK --set-mark 22 (VO) iptables -t mangle -A qos_prerouting_rule_chain -m dscp --dscp-class BE -j MARK --set-mark 62 (BE) iptables -t mangle -A qos_prerouting_rule_chain -m dscp --dscp-

QDMA do dequeue

Highest Priority → set mark 52

class CS2 -j MARK --set-mark 12 (BK)

High Priority → set mark 22

Default Priority → set mark 62

Low Priority → set mark 12

Internal Use

HW QoS (QDMA) replace DF

- QDMA replace HTB (TC function)
 - Minimum Rate
 - Maximum Rate



HTB Example: Minimum Rate

Maximum Rate

tc class add/dev imq0 parent 3:0 classid 3:1 htb rate 1024kbit ceil 2048kbit

QDMA Example:

Minimum Rate Maximum Rate

qdma rate 10 1 1024 1 2048

Usage: | qdma rate [queue] [min_en] [min_rate] [max_en] [max_rate]

HW QoS (QDMA) replace DRR

classify packet + QDMA → classify packet + dequeue

```
gdma sch_rate 0 1 51200 (total BW = 51200kbyte)
qdma resv 2 50 50
qdma sch 20
qdma rate 2 1 5120 1 51200 (min rate=5120kbyte, max rate=5120kbyte)
gdma m2g 11 2 (set queue mapping: skb with mark 11 to queue 2.)
qdma resv 3 50 50
qdma sch 30
qdma rate 3 1 10240 1 51200
qdma m2q 61 3
qdma resv 4 50 50
qdma sch 40
qdma rate 4 1 15360 1 51200
qdma m2q 21 4
qdma resv 5 50 50
qdma sch 50
```

Four different minimum rate=> four different priority

HW QoS (QDMA) replace SPQ

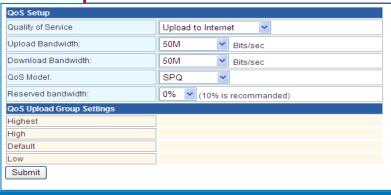
QDMA replace SPQ (TC function)

QDMA strict priority Example

qdma rate 2 0 0 0 0 qdma rate 3 0 0 0 0



Queue 3 priority > Queue 2 priority



HW QoS (QDMA) replace SPQ

qdma sch_rate 0 1 51200 qdma sch 20 qdma rate 2 0 0 0 0 qdma m2q 11 2 qdma sch 30 Highest: Queue 5 qdma rate 3 0 0 0 0 High: Queue 4 qdma m2q 61 3 Default: Queue 3 qdma sch 40 Low: Queue 2 qdma rate 4 0 0 0 0 qdma m2q 21 4 qdma sch 50 qdma rate 5 0 0 0 0 qdma m2q 61 5

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HW QoS (QDMA) replace Mixed mode

qdma sch_rate 1 1 51200 (total BW=50M) qdma resv 10 50 50 qdma sch 10 1 qdma rate 10 1 5120 1 51200 (Queue 10=total BW*10%=5M) qdma m2q 12 10

Highest: Queue 13 High: Queue 12

Default: Queue 11

Low: Queue 10

qdma resv 11 50 50 qdma sch 11 1

qdma rate 11 1 10240 1 51200 (Queue 11=total BW*20%=10M)

qdma m2q 62 11

qdma resv 12 40 40 qdma sch 12 1 qdma rate 12 0 0 0 0 (Strict Priority) qdma m2q 22 12

qdma resv 13 50 50 qdma sch 13 1 qdma rate 13 0 0 0 0 (Strict Priority)

QoS Setup				
Quality of Service	Download from Internet 💌			
Upload Bandwidth:	50M Bits/sec			
Download Bandwidth:	50M Bits/sec			
QoS Model:	SPQ+DRR 💌			
Reserved bandwidth:	0% (10% is recommanded)			
QoS Download Group Settings				
Highest				
High				
Default	Rate: 20% Ceil: 100% V			
Low	Rate: 10% Y Ceil: 100% Y			

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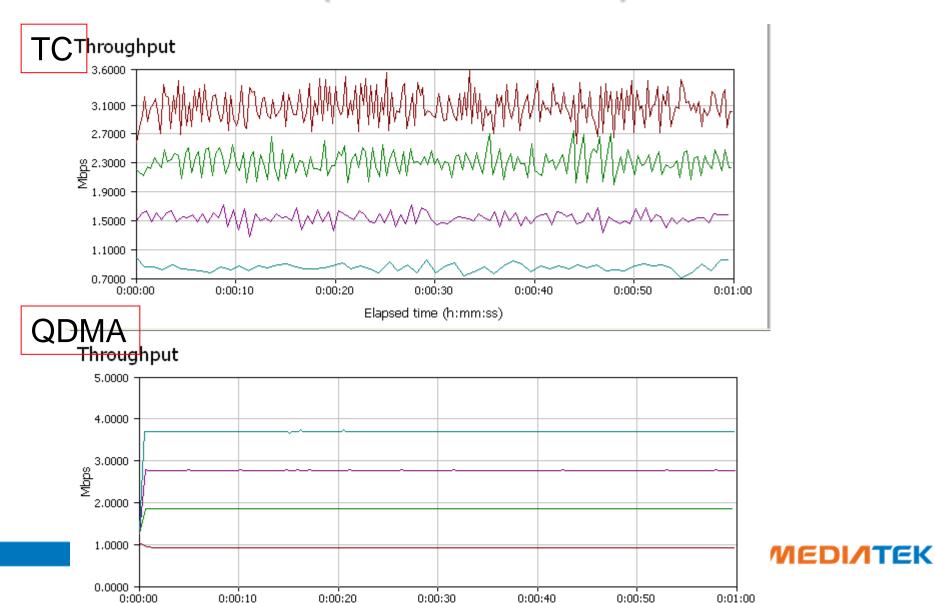
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Test Result (1)

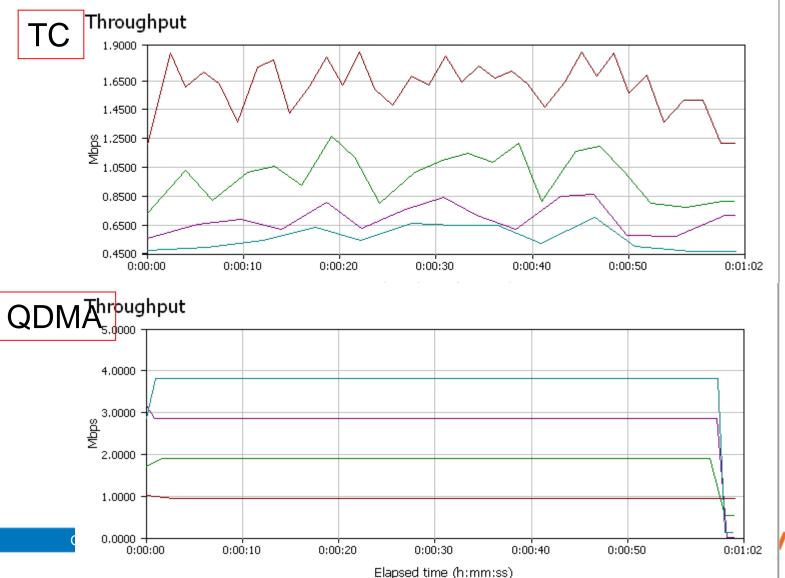
- Test Condition
 - QoS Mode:DRR
 - Total BW = 10M
 - DUT: MT7621 FPGA (QDMA V.S TC)



Test Result (MT7621 FPGA): TCP

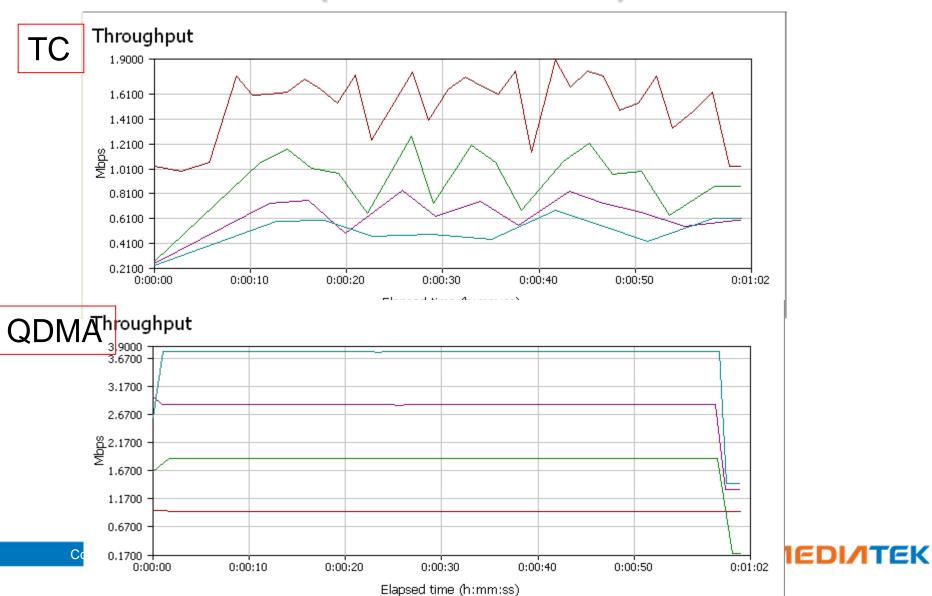


Test Result (MT7621 FPGA): UDP

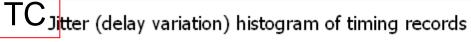


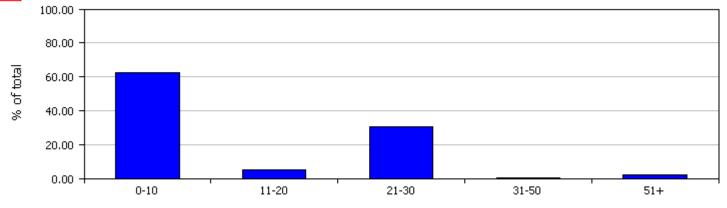


Test Result (MT7621 FPGA): RTP



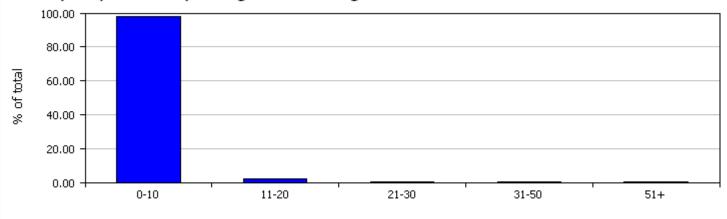
Test Result (MT7621 FPGA): RTP





QDMA litter (delay variation) histogram of ti

Jitter (delay variation) histogram of timing records





Milliseconds

QDMA V.S TC (MT7621 FPGA)

- QDMA can lower CPU loading.
- QDMA can control total BW precisely especially when CPU is busy or computing power is low.
- QDMA can short the response time and jitter.
- QDMA can make transmission rate become more smooth and stable.

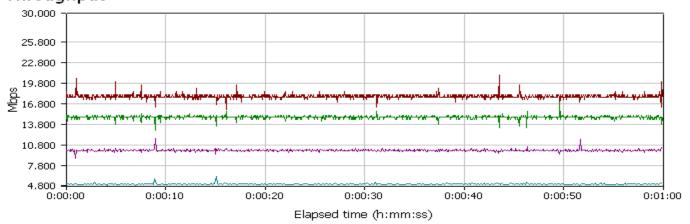


Test Result (2)

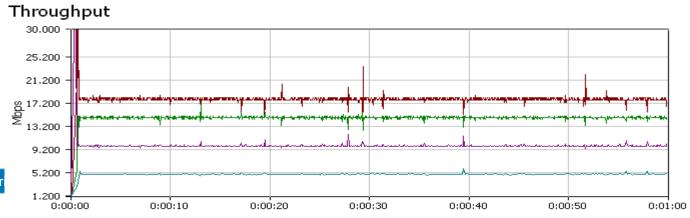
- Test Condition
 - QoS Mode:DRR, SPQ
 - Total BW = 50M
 - DUT : MT7621 ASIC(QDMA HW NAT V.S QDMA no HW NAT)

MT7621 ASIC (CPU800M): DRR

UL(No HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)



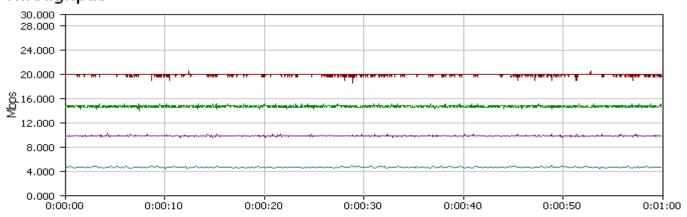
UL(HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)



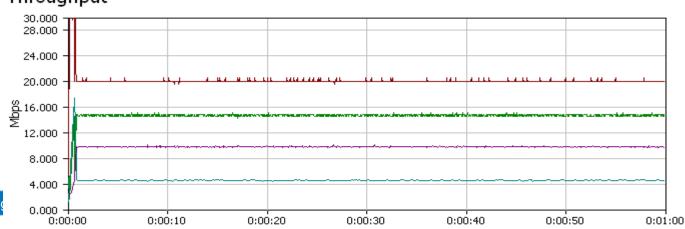


MT7621 ASIC (CPU800M): DRR

DL(No HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)
 Throughput



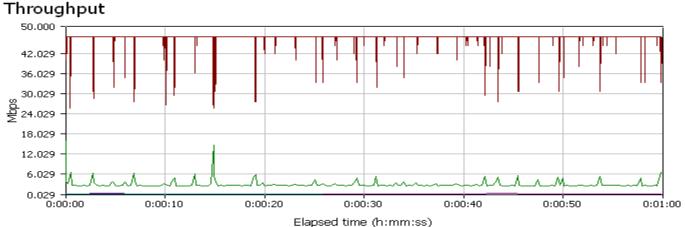
DL(HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)
 Throughput



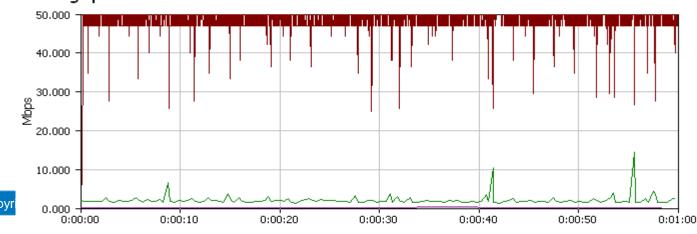


MT7621 ASIC (CPU800M): SPQ

UL(No HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)



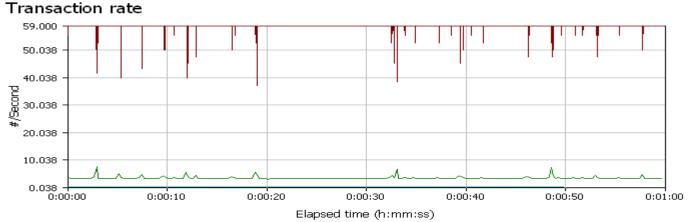
UL(HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)
 Throughput



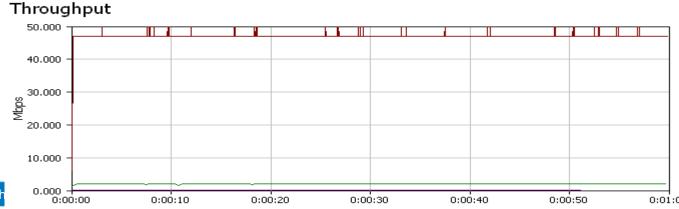


MT7621 ASIC (CPU800M): SPQ

DL(No HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)



DL(HWNAT)(TCP port 2000, 3000, 4000, 5000)(Priority High-> Low)



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Elapsed time (h:mm:ss)

QDMA Benefit

- QDMA can lower CPU loading.
- In the past, when HW NAT enable, we can not use SW QoS.
- In MT7621, QDMA can do bandwidth control and schedular when HW NAT enable.

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