Description

The VSM3404 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge.This device is suitable for use as a load switch and PWM applications.

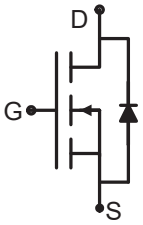
Genera Features

- V_{DS} = 30V,I_D = 5.8A
  $R_{DS(ON)}$ < 31mΩ @ V_{GS}=10V
  $R_{DS(ON)}$ < 43mΩ @ V_{GS}=4.5V
- High Power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Load switch
- PWM application

SOT-23-3



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VSM3404-S2	VSM3404	SOT-23-3	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	5.8	A
Drain Current-Pulsed (Note 1)	I_{DM}	20	A
Maximum Power Dissipation	P_D	1.4	W
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	89	℃/W

Electrical Characteristics (T_A=25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	30	33	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V,V_{GS}=0V	-	-	1	μA

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	1.2	1.6	2.4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=5A	-	25.5	31	mΩ
		V_{GS}=4.5V, I_D=4A	-	36	43	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V,I_D=5A	-	15	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{lss}	V_{DS}=15V,V_{GS}=0V, F=1.0MHz	-	485.8	-	PF
Output Capacitance	C_{oss}		-	65.2	-	PF
Reverse Transfer Capacitance	C_{rss}		-	54	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=15V, R_L=3Ω V_{GS}=10V,R_{GEN}=3Ω	-	5	-	nS
Turn-on Rise Time	t_r		-	3	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	15	-	nS
Turn-Off Fall Time	t_f		-	3.5	-	nS
Total Gate Charge	Q_g	V_{DS}=15V,I_D=5.8A, V_{GS}=10V	-	12.6	-	nC
Gate-Source Charge	Q_{gs}		-	1.9	-	nC
Gate-Drain Charge	Q_{gd}		-	2.6	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_S=5.8A	-	-	1.2	V
Diode Forward Current (Note 2)	I_S		-	-	5.8	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

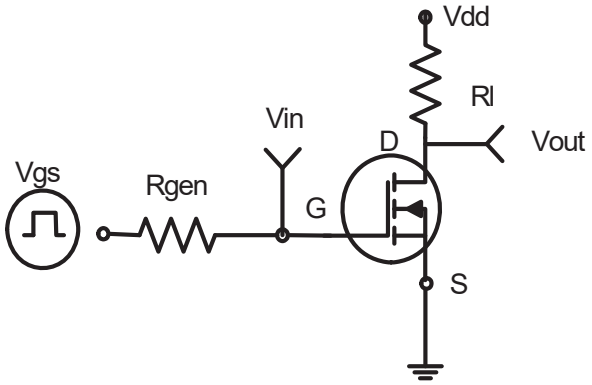


Figure 1:Switching Test Circuit

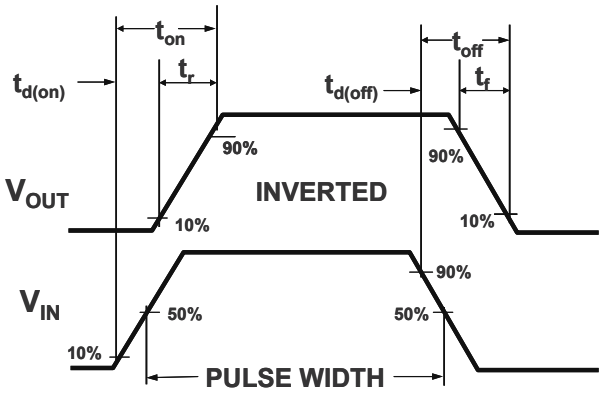


Figure 2:Switching Waveforms

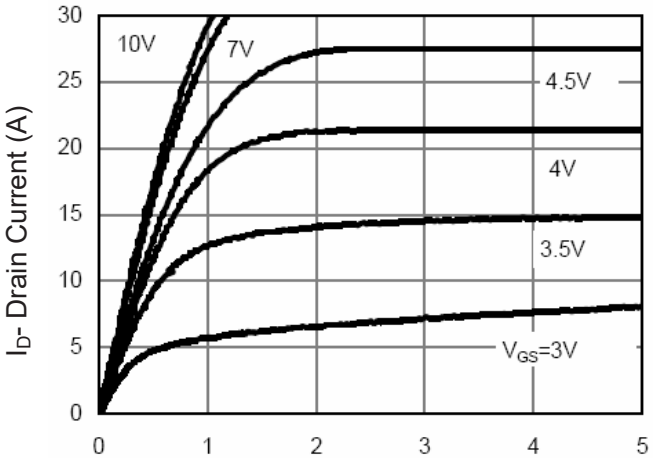


Figure 3 Output Characteristics

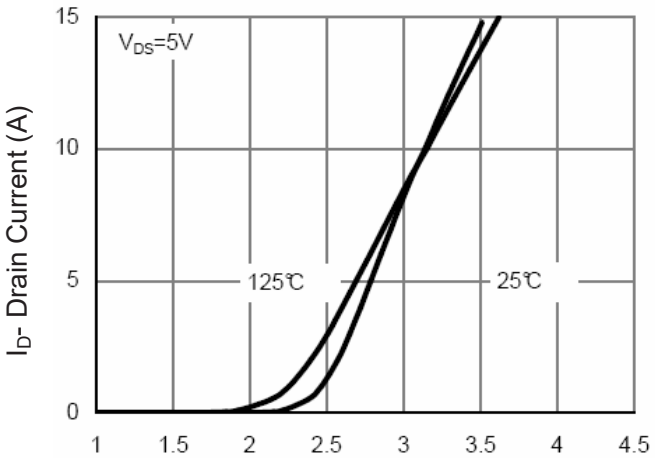


Figure 4 Transfer Characteristics

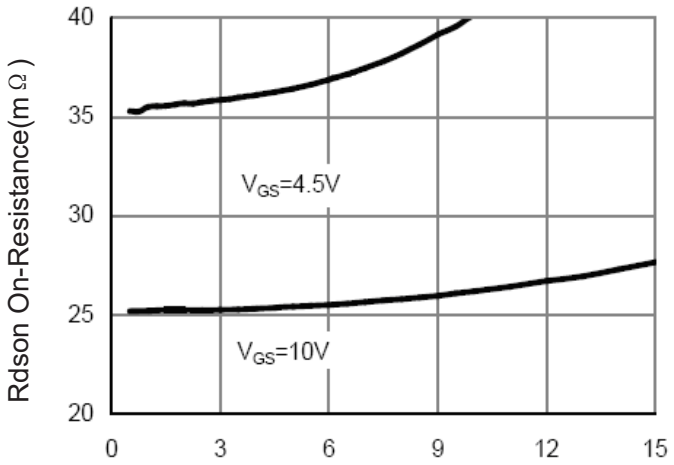


Figure 5 Drain-Source On-Resistance

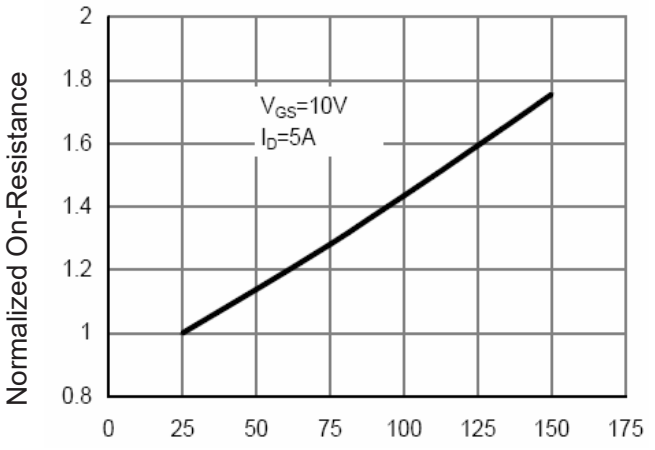


Figure 6 Drain-Source On-Resistance

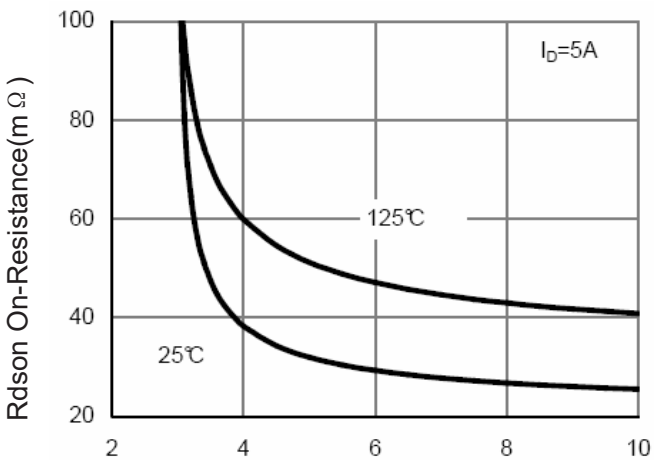


Figure7 Rdson vs Vgs

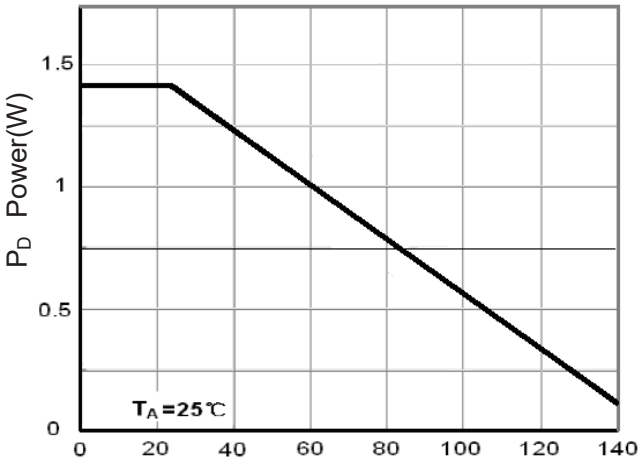


Figure 8 Power Dissipation

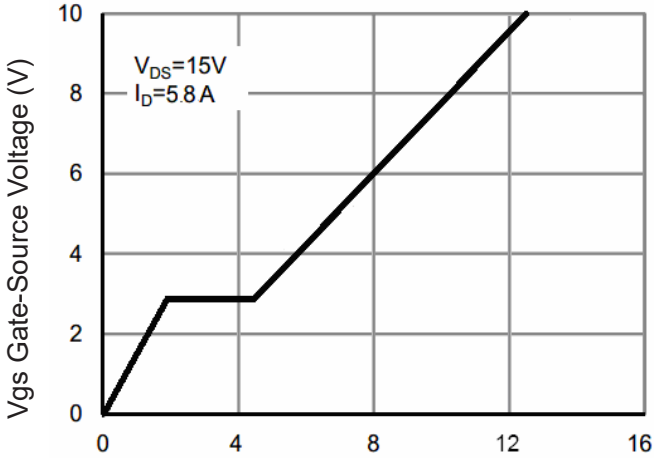


Figure 9 Gate Charge

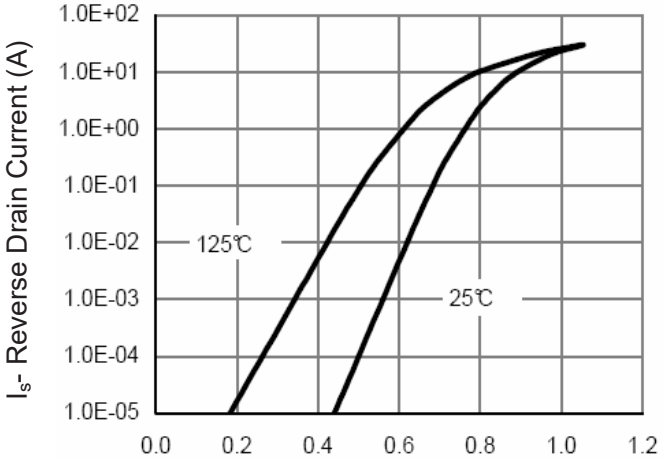


Figure 10 Source- Drain Diode Forward

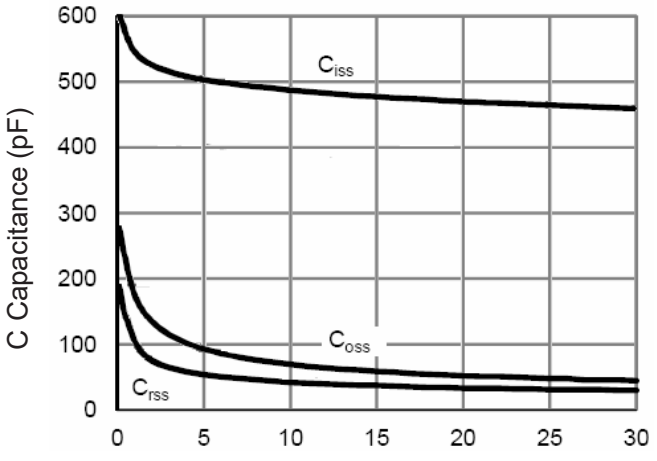


Figure 11 Capacitance vs Vds

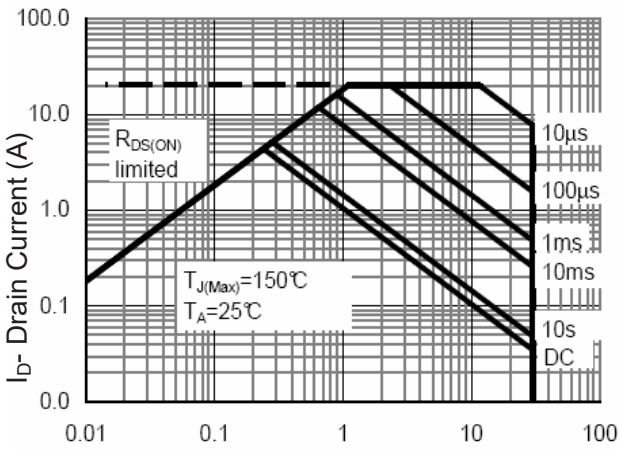


Figure 12 Safe Operation Area

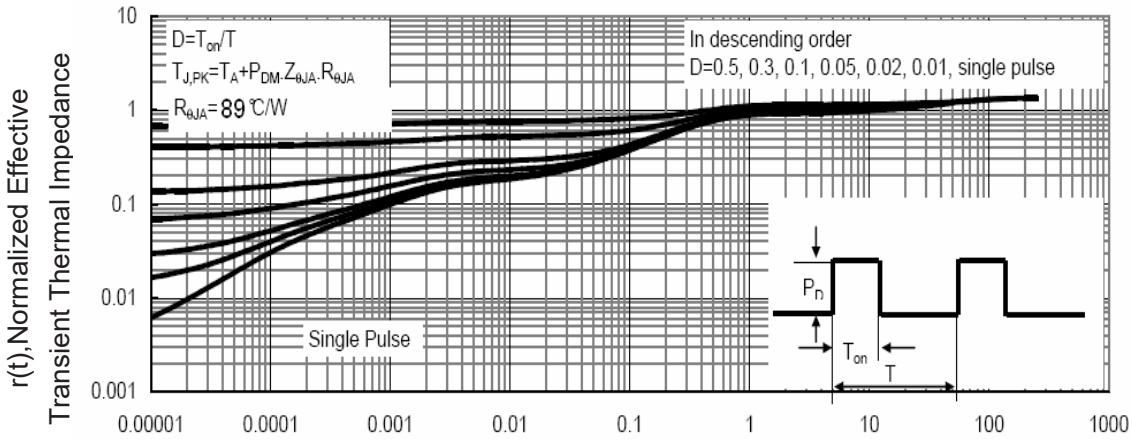


Figure 13 Normalized Maximum Transient Thermal Impedance