

Description

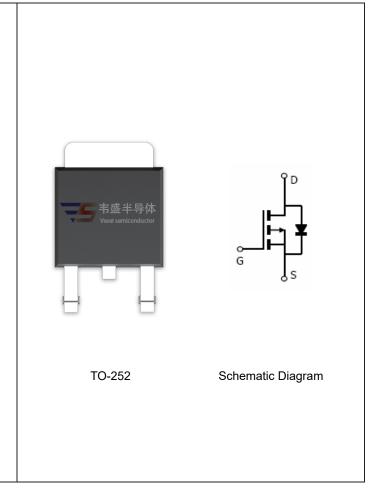
The VSM40P06 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- V_{DS} =-60V, I_{D} =-40A $R_{DS(ON)}$ <23m Ω @ V_{GS} =-10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VSM40P06-T2	VSM40P06	TO-252	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	VDS	-60	V	
Gate-Source Voltage	Vgs	±20	V	
Drain Current-Continuous	I _D	-40	А	
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	-28.3	А	
Pulsed Drain Current	I _{DM}	160	Α	
Maximum Power Dissipation	P _D	100	W	
Derating factor		0.67	W/℃	
Single pulse avalanche energy (Note 5)	E _{AS}	420	mJ	
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$ C	

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{0JC}	1.5	°C/W



Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	BV _{DSS} V _{GS} =0V I _D =-250μA		-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-2	-2.6	-4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-20A	-	19	23	mΩ
Forward Transconductance	G FS	V _{DS} =-5V,I _D =-20A	-	20	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ - 20\/\/ -0\/	-	5410	-	PF
Output Capacitance	Coss	V_{DS} =-30V, V_{GS} =0V, F=1.0MHz	-	450	-	PF
Reverse Transfer Capacitance	C _{rss}	r-1.0lvinz	-	234	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	16	-	nS
Turn-on Rise Time	t _r	V_{DD} =-30 V , I_D =-20 A	-	18	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10 V , R_{GEN} =3 Ω	-	65	-	nS
Turn-Off Fall Time	t _f		-	22	-	nS
Total Gate Charge	Qg	\/ - 20\/ - 20 \	-	89.5	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-30V, I_{D} =-20A, V_{GS} =-10V	-	19	-	nC
Gate-Drain Charge	Q _{gd}	VGS10V	-	22	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-12A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	-40	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = -20A	-	-	71	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	-	170	nC

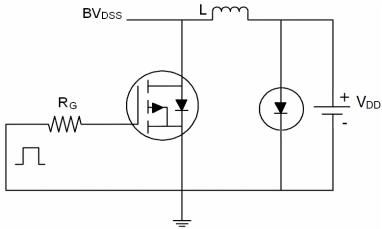
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25 $^{\circ}\text{C}$,V_{DD}=-30V,V_G=-10V,L=0.5mH,Rg=25 Ω

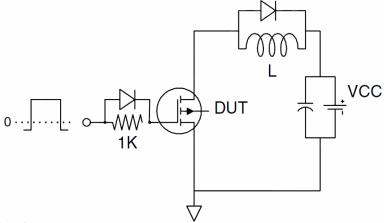


Test Circuit

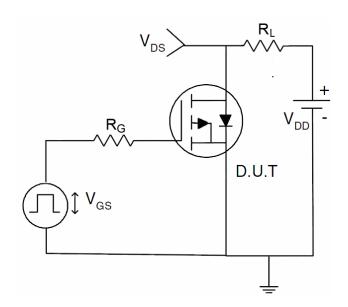
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit

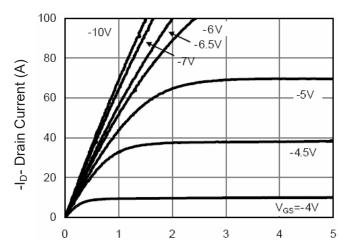


3) Switch Time Test Circuit

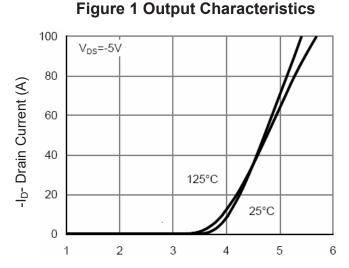




Typical Electrical and Thermal Characteristics (Curves)



-Vds Drain-Source Voltage (V)



-Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics

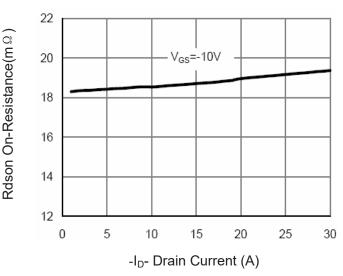


Figure 3 Rdson- Drain Current

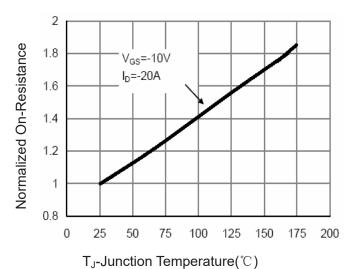


Figure 4 Rdson-JunctionTemperature

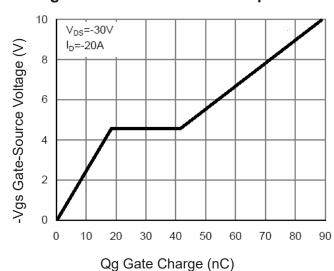


Figure 5 Gate Charge

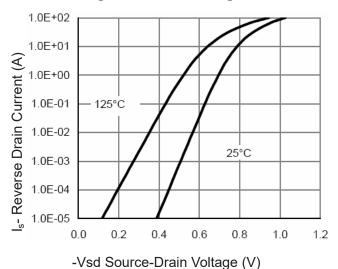


Figure 6 Source- Drain Diode Forward



-I_D- Drain Current (A)

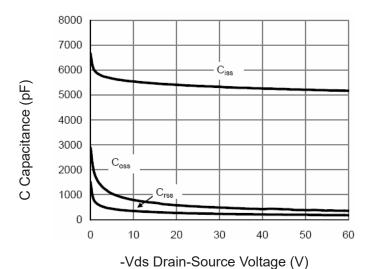
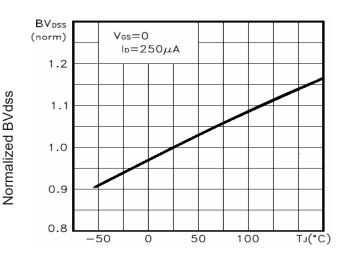


Figure 7 Capacitance vs Vds



T_J-Junction Temperature (°C)

Figure 9 BV_{DSS} vs Junction Temperature

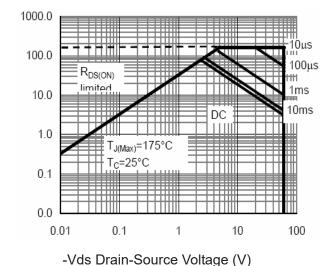
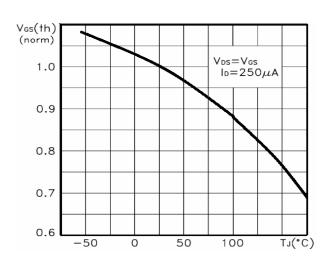
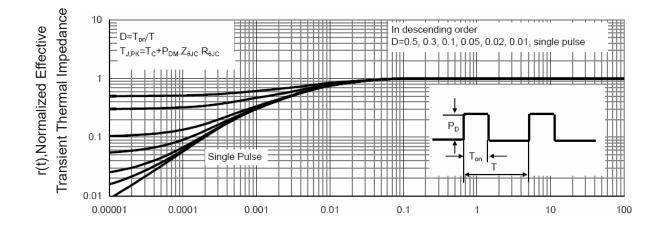


Figure 8 Safe Operation Area



T_J-Junction Temperature(℃)

Figure 10 V_{GS(th)} vs Junction Temperature



Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance