

### Description

The VSM28P06 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge .This device is well suited for high current load applications.

#### **General Features**

● V<sub>DS</sub> =-60V,I<sub>D</sub> =-28A

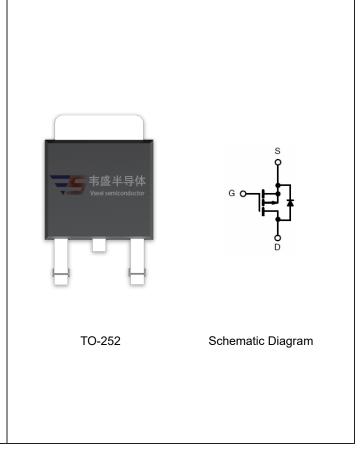
 $R_{DS(ON)}$  <48m $\Omega$  @  $V_{GS}$ =-10V

 $R_{DS(ON)}$  <55m $\Omega$  @  $V_{GS}$ =-4.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation

#### **Application**

- High side switch for full bridge converter
- DC/DC converter for LCD display



**Package Marking and Ordering Information** 

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VSM28P06-T2	VSM28P06	TO-252	-	-	-

Absolute Maximum Ratings (T<sub>c</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	-60	V	
Gate-Source Voltage	V <sub>G</sub> s	±20	V	
Drain Current-Continuous	I <sub>D</sub>	-28	А	
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100°C)	-19.8	А	
Pulsed Drain Current	I <sub>DM</sub>	-112	А	
Maximum Power Dissipation	P <sub>D</sub>	80	W	
Derating factor		0.53	W/℃	
Single pulse avalanche energy (Note 5)	Eas	55	mJ	
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 175	°C	

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{ heta JC}$	1.88	°C/W

# Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	-1	-1.5	-2.0	V
Drain Course On State Registeres	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A	-	40	48	mΩ
Drain-Source On-State Resistance		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-20A	-	48	55	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-20A	-	10	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>	\/ - 20\/\/ -0\/	-	1630.7	-	PF
Output Capacitance	Coss	$V_{DS}$ =-30V, $V_{GS}$ =0V, F=1.0MHz	-	90.6	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	r-1.0ivinz	-	77.3	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{DD}$ =-30V, $R_L$ =1.5 $\Omega$ , $V_{GS}$ =-10V, $R_G$ =3 $\Omega$	-	11	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	33	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	13	-	nS
Total Gate Charge	Qg	.,	-	30		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-30,I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V	-	3.4		nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> 10V	-	6.7		nC
Drain-Source Diode Characteristics	·					
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-20A	-		-1.2	V
Diode Forward Current (Note 2)	Is		-	-	-18	А
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF =- 20A	-	34		nS
Reverse Recovery Charge	Qrr	$di/dt = -100A/\mu s^{(Note3)}$	-	37		nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

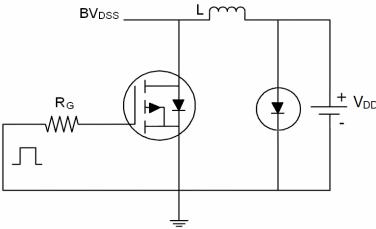
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- **4.** Guaranteed by design, not subject to production
- **5.** E<sub>AS</sub> condition: Tj=25  $^{\circ}\text{C}$  ,V<sub>DD</sub>=-30V,V<sub>G</sub>=-10V,L=0.5mH,Rg=25 $\Omega$

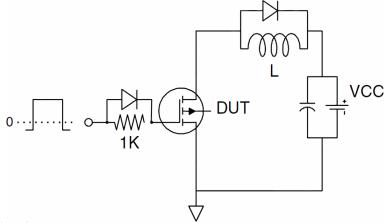


### **Test Circuit**

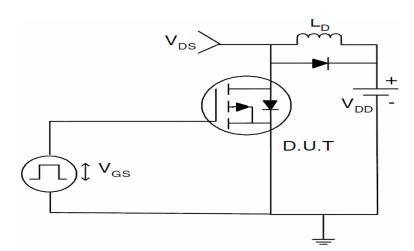
# 1) E<sub>AS</sub> Test Circuit



### 2) Gate Charge Test Circuit

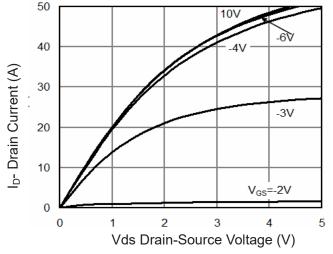


### 3) Switch Time Test Circuit

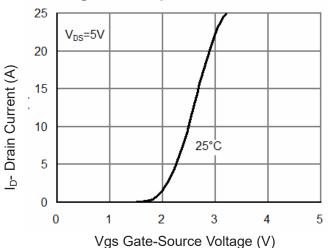




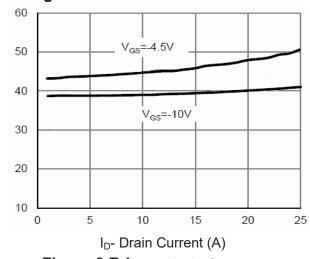




**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 



Rdson On-Resistance(m 🛭 )

Figure 3 Rdson- Drain Current

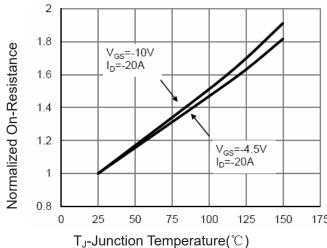


Figure 4 Rdson-Junction Temperature

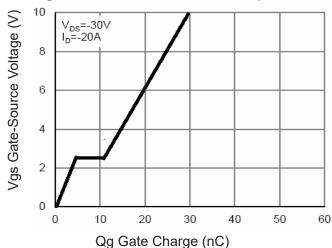


Figure 5 Gate Charge

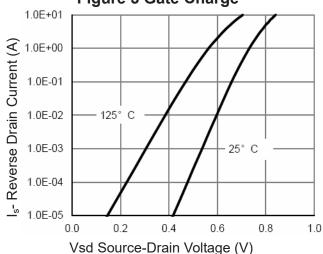


Figure 6 Source- Drain Diode Forward



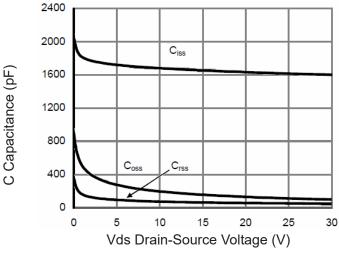


Figure 7 Capacitance vs Vds

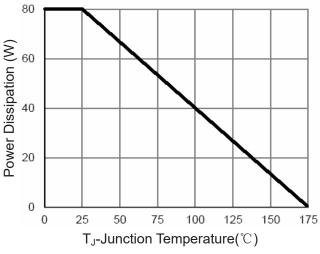


Figure 9 Power De-rating

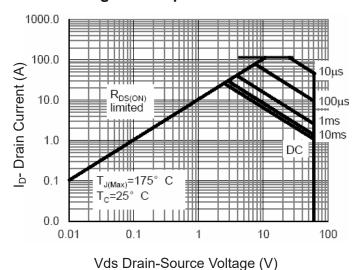


Figure 8 Safe Operation Area

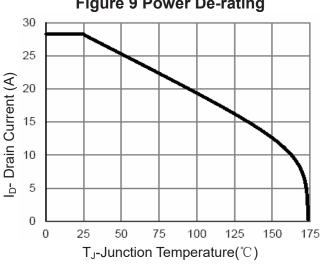


Figure 10 ID Current De-rating

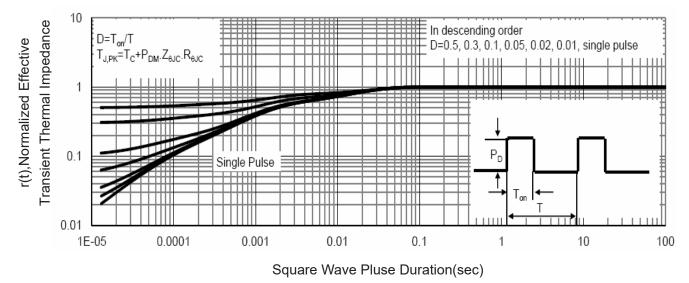


Figure 11 Normalized Maximum Transient Thermal Impedance