

# Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

#### **Features**

- $60V,45A,R_{DS(ON).max}=14m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- ♦ 100% EAS Guaranteed
- ♦ Green device available

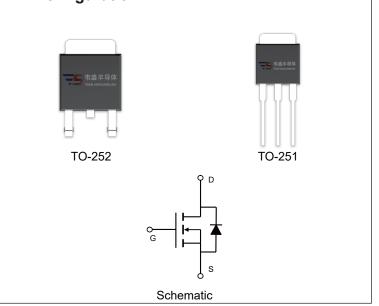
# **Applications**

- Motor Drives
- UPS
- ♦ DC-DC Converter

# **Product Summary**

 $\begin{array}{ll} V_{DSS} & 60V \\ R_{DS(on).max} \textcircled{0} \ V_{GS} \text{=} 10V & 14m\Omega \\ I_D & 45A \end{array}$ 

# **Pin Configuration**



# Absolute Maximum Ratings Tc = 25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Continuous drain current ( T <sub>C</sub> = 25°C )	1-	45	A
Continuous drain current ( T <sub>C</sub> = 100°C )	l <sub>D</sub>	31	A
Pulsed drain current <sup>1)</sup>	Ідм	180	A
Gate-Source voltage	V <sub>GSS</sub>	±20	V
Avalanche energy <sup>2)</sup>	Eas	56	mJ
Power Dissipation ( T <sub>C</sub> = 25°C )	P <sub>D</sub>	62.5	W
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C
Operating Junction Temperature Range	TJ	-55 to +150	°C

### **Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	Rejc	2	°C/W



**Package Marking and Ordering Information** 

Device	Device Package	Marking
VSM45N06-T2	TO-252	VSM45N06-T2
VSM45N06-T1	TO-251	VSM45N06-T1

# **Electrical Characteristics** T<sub>J</sub> = 25°C unless otherwise noted

Electrical Characteristics	1	T <sub>J</sub> = 25°C unless otherwise noted					
Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit	
Static characteristics				_			
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0 V, I <sub>D</sub> =250uA	60			V	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.9	1.4	1.9	V	
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V, T <sub>J</sub> = 25°C			1	μА	
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> = 125°C			30	μА	
Gate leakage current, Forward	I <sub>GSSF</sub>	V <sub>GS</sub> =20V, V <sub>DS</sub> =0 V			100	nA	
Gate leakage current, Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0 V			-100	nA	
Drain aguras en etata registance	Б	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		10	14	mΩ	
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A		11.5	15	mΩ	
Forward transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		86		S	
Dynamic characteristics							
Input capacitance	C <sub>iss</sub>	)/ 05)/ )/ 0)/		2320			
Output capacitance	Coss	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, F = 1MHz		168		pF	
Reverse transfer capacitance	Crss	- F - IIVIDZ		128			
Turn-on delay time	t <sub>d(on)</sub>			18.5		- ns	
Rise time	t <sub>r</sub>	V <sub>DD</sub> = 30V,V <sub>GS</sub> =15V, I <sub>D</sub> =20A		16.1			
Turn-off delay time	t <sub>d(off)</sub>			107.6			
Fall time	t <sub>f</sub>			55.7			
Gate resistance	Rg	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz		1.84		Ω	
Gate charge characteristics							
Gate to source charge	Q <sub>gs</sub>	.,		10			
Gate to drain charge	Q <sub>gd</sub>	V <sub>DS</sub> =30V, I <sub>D</sub> =20A, V <sub>GS</sub> = 10V		8.1		nC	
Gate charge total	Qg			50		1	
Drain-Source diode characteristic	cs and Maxii	num Ratings		'			
Continuous Source Current	Is				45	А	
Pulsed Source Current <sup>3)</sup>	I <sub>SM</sub>	]			180	А	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A, T <sub>J</sub> =25℃			1.2	V	
Reverse Recovery Time	t <sub>rr</sub>			38.2		ns	
Reverse Recovery Charge	Qrr	I <sub>S</sub> =20A,di/dt=100A/us, T <sub>J</sub> =25℃		32.5		nC	

#### Notes

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2:  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.5mH,  $I_{AS}$ =15A,  $R_G$ =25 $\Omega$ , Starting  $T_J$ =25 $^{\circ}$ C.
- 3: Pulse Test: Pulse Width  $\leq 300 \, \mu \, \text{s}$ , Duty Cycle  $\leq 2\%$ .



# **Electrical Characteristics Diagrams**

Figure 1. Typ. Output Characteristics

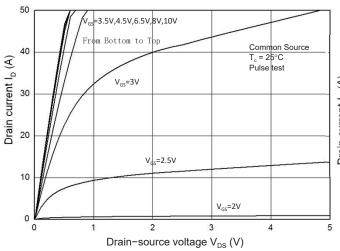


Figure 2. Transfer Characteristics

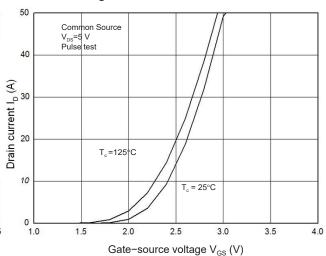


Figure 3. Capacitance Characteristics

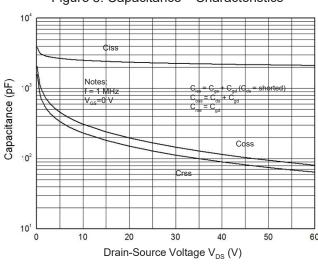


Figure 4. Gate Charge Waveform

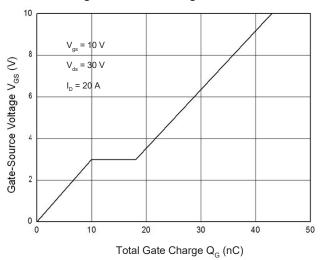


Figure 5. Body-Diode Characteristics

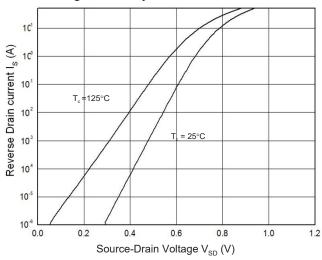
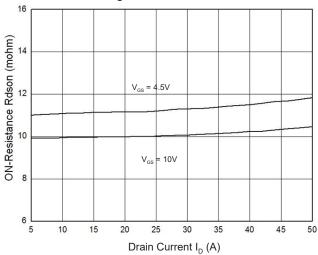


Figure 6. Rdson-Drain Current





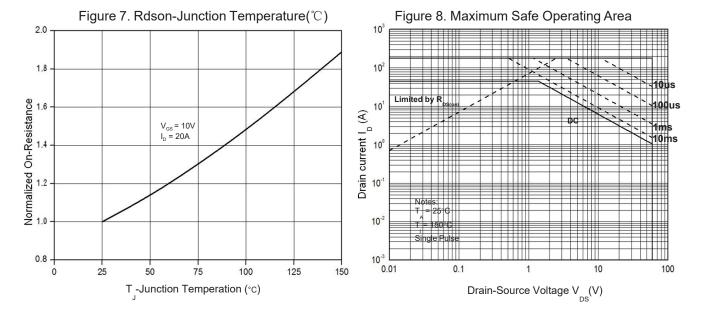
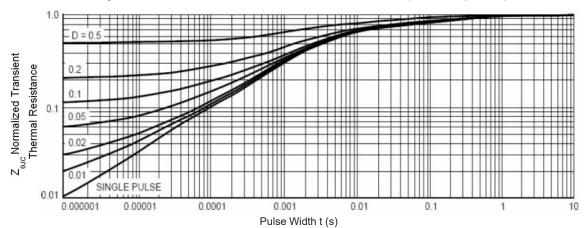


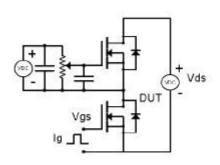
Figure 6. Normalized Maximum Transient Thermal Impedance (RthJC)





# **Test Circuit & Waveform**

Figure 8. Gate Charge Test Circuit & Waveform



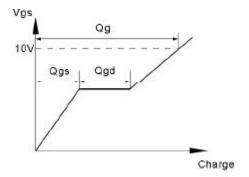
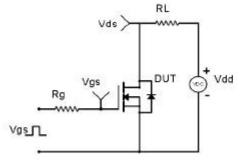


Figure 9. Resistive Switching Test Circuit & Waveforms



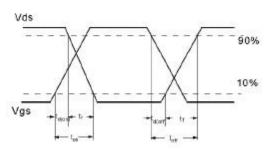
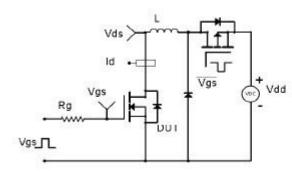


Figure 10. Unclamped Inductive Switching (UIS) Test Circuit & Waveform



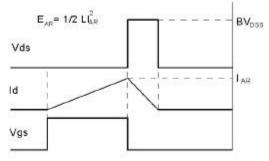


Figure 11. Diode Recovery Circuit & Waveform

