

VS79L08 Three-terminal negative voltage regulator

FEATURES

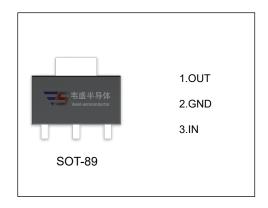
• Maximum output current I_{OM:} 0.1A

Output voltage

V_o: -8V

Continuous total dissipation

P_D:0.6 W (T_a= 25 °C)



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

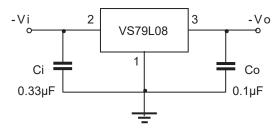
Parameter	Symbol	Value	Unit
Input Voltage	V _i	-30	V
Thermal Resistance from Junction to Ambient	R _{θJA}	208.3	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	℃
Storage Temperature Range	T _{STG}	-65~+150	℃

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (VI=-14V, Io=40mA, Ci=0.33μF, Co=0.1μF, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output Voltage	Vo	T _J =25℃	-7.76	-8.0	-8.24	V
		-10.5V≤V _I ≤-23V, Io=1mA~40mA	-7.6	-8.0	-8.4	V
		Io=1mA~70mA	-7.6	-8.0	-8.4	V
Load Regulation	ΔVο	lo=1mA~100mA ,T _J =25°C		30	100	mV
		lo=1mA~40mA ,T _J =25°C		15	50	mV
Line Regulation	ΔVο	-10.5V≤V₁≤-23V ,T _J =25°C		42	200	mV
		-11V≤V _I ≤-23V ,T _J =25°C		36	150	mV
Quiescent Current	Iq	T _J =25℃		4	6	mA
Quiescent Current Change	Δlq	-11V≤V _I ≤-23V			1.5	mA
	Δlq	1mA≤I _O ≤40mA			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz		54		μV/Vo
Ripple Rejection	RR	-11V≤V _I ≤-21V,f=120Hz	37	46		dB
Dropout Voltage	Vd	T _J =25℃		1.7		V

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



