

VS7815 Three-terminal positive voltage regulator

FEATURES

Maximum output current

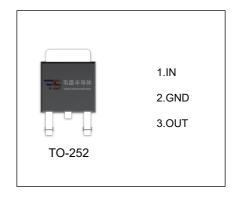
I_{OM}: 1.5 A

Output voltage

V_o: 15 V

Continuous total dissipation

P_D: 1.25 W (T_a= 25 °C)



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

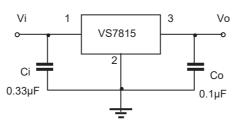
Parameter	Symbol	Value	Unit
Input Voltage	V _i	35	V
Thermal Resistance from Junction to Ambient	R _{θJA}	80	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	°C
Storage Temperature Range	T _{STG}	-65~+150	℃

ELECTRICAL CHARACTERISTICS(Vi=23V,Io=500mA,-25°C<T_J<125°C,Ci=0.33μF,Co=0.1μF, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output voltage	Vo	T _J =25℃	14.55	15	15.45	V
	V0	17.5V≤V _i ≤30V, lo=5mA-1A,P≤15W	14.25	15	15.75	V
Load Regulation	ΔVο	T _J =25℃, lo=5mA-1.5A		12	300	mV
	Δνο	T _J =25℃, lo=250mA-750mA		3	150	mV
Line regulation	ΔVο	17.5V≤V _i ≤30V, T _J =25°C		12	300	mV
	Δνο	20V≤V _i ≤26V, T _J =25°C		4	150	mV
Quiescent Current	Iq	T _J =25℃		4.3	8	mA
Quiescent Current Change	Δlq	17.5V≤V _i ≤30V			1	mA
	Δlq	5mA≤I _O ≤1A			0.5	mA
Output voltage drift	△Vo/△T	I _O =5mA		-1		mV/℃
Output Noise Voltage	V _N	10Hz≤f≤100KHz		90		μV/Vo
Ripple Rejection	RR	18.5V≤V _i ≤28.5V,f=120Hz, T _J =25°C	54	70		dB
Dropout Voltage	Vd	T _J =25°C, lo=1A		2		V
Output resistance	Ro	f=1KH _Z		19		mΩ
Short Circuit Current	Isc	T _J =25℃		230		mA
Peak Current	lpk	T _J =25℃		2.1		Α

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



