

VS78L12 Three-terminal positive voltage regulator

FEATURES

Maximum output current

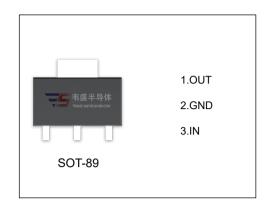
I_{OM}: 0.1A

Output voltage

V₀: 12V

Continuous total dissipation

 $P_D: 0.6 \text{ W } (T_a = 25 ^{\circ}\text{C})$



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

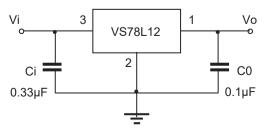
Parameter	Symbol	Value	Unit
Input Voltage	Vi	35	V
Thermal Resistance from Junction to Ambient	R _{θJA}	166.7	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	°C
Storage Temperature Range	T _{STG}	-65~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (VI=19V, Io=40mA, Ci=0.33µF,Co=0.1µF, unless otherwise specified)

Pa rameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Output voltage	Vo	T _J =25°C	11.64	12	12.36	V
		14V≤V _I ≤27V, Io=1mA-40mA	11.4	12	12.6	V
		Io=1mA-70mA	11.4	12	12.6	V
Load Regulation	A 1 /	Io=1mA-100mA,T _J =25°C		22	100	mV
	△Vo	Io=1mA-40mA,T _J =25°C		13	50	mV
Line regulation	△Vo	14.5V≤V _I ≤27V,T _J =25°C		55	250	mV
		16V≤V _I ≤27V,T _J =25°C		49	200	mV
Quiescent Current	Iq	T _J =25°C		4.3	6.5	mA
Quiescent Current Change -	△lq	16V≤V₁≤27V			1.5	mA
	△lq	1mA≤I _O ≤40mA			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz,T _J =25°C		70		μV/Vo
Ripple Rejection	RR	15V≤V _I ≤25V,f=120Hz	37	42		dB
Dropout Voltage	Vd	T _J =25°C		1.7		V

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



