

VS79L12 Three-terminal negative voltage regulator

FEATURES

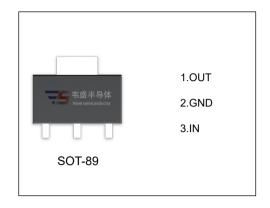
• Maximum output current

I_{OM}: 0.1A

Output voltage

V_o: -12V • Continuous total dissipation

P_D:0.6 W (T_a= 25 °C)



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

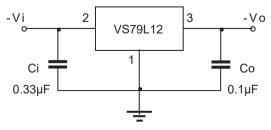
Parameter	Symbol	Value	Unit
Input Voltage	Vi	-35	V
Thermal Resistance from Junction to Ambient	R _{θJA}	208.3	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	℃
Storage Temperature Range	T _{STG}	-65~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE(Vi=-19V,Io=40mA,Ci=0.33μF,Co=0.1μF, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output Voltage	Vo	T _J =25°C	-11.64	-12	-12.36	V
		-14.5V≤V _I ≤-27V, lo=1mA~40mA	-11.4	-12	-12.6	V
		Io=1mA~70mA	-11.4	-12	-12.6	V
Load Regulation	ΔVο	lo=1mA~100mA ,T _J =25°C		24	100	mV
	Δνο	lo=1mA~40mA ,T _J =25℃		15	50	mV
Line Regulation	ΔVo	-14.5V≤V _I ≤-27V ,T _J =25°C		50	250	mV
		-16V≤V _I ≤-27V ,T _J =25°C		40	200	mV
Quiescent Current	lq	T _J =25°C			6.5	mA
Quiescent Current Change	Δlq	-16V≤V _I ≤-27V			1.5	mA
	Δlq	1mA≤I _O ≤40mA			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz ,T _J =25°C		80		μV/Vo
Ripple Rejection	RR	-15V≤V _I ≤-25V,f=120Hz	37	42		dB
Dropout Voltage	Vd	T _J =25°C		1.7		V

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



