

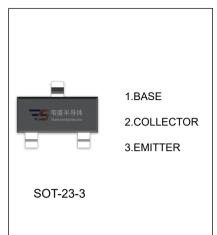
FMMT619 TRANSISTOR (NPN)

FEATURE

Low Saturation Voltage

MAXIMUM RATINGS (T_a =25 $^{\circ}$ C unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-Base Voltage	50	V
V _{CEO}	Collector-Emitter Voltage	50	V
V _{EBO}	Emitter-Base Voltage	5	V
Ic	Collector Current -Continuous	2	Α
Pc	Power Dissipation	0.35	W
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	357	°C/W
P _{CM}	Maximum Power Dissipation (note 1)	0.625	W
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient (note 1)	200	°C/W
T _J ,T _{stg}	Operation Junction and Storage Temperature Range	-55~+150	°C



ELECTRICAL CHARACTERISTICS (T_a=25℃ unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =100μA,I _E =0	50			V
Collector-emitter breakdown voltage (note 2)	V _{(BR)CEO}	I _C =10mA,I _B =0	50			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =100μA ,I _C =0	5			V
Collector cut-off current	I _{CBO}	V _{CB} =40V,I _E =0			100	nA
Emitter cut-off current	I _{EBO}	V _{EB} =4V,I _C =0			100	nA
	h _{FE(1)}	V _{CE} =2V, I _C =10mA	200			
	h _{FE(2)}	V _{CE} =2V, I _C =0.2A	300			
DC current gain (note 2)	h _{FE(3)}	V _{CE} =2V, I _C =1A	200			
	h _{FE(4)}	V _{CE} =2V, I _C =2A	100			
	h _{FE(5)}	V _{CE} =2V, I _C =6A		40		
	V _{CE(sat)1}	I _C =0.1A,I _B =10mA			20	mV
Collector-emitter saturation voltage (note 2)	V _{CE(sat)2}	I _C =1A,I _B =10mA			200	mV
	V _{CE(sat)3}	I _C =2A,I _B =100mA			220	mV
Base-emitter saturation voltage (note 2)	V _{BE(sat)}	I _C =2A,I _B =50mA			1	V
Base-emitter on voltage (note 2)	V _{BE(on)}	I _C =2A, V _{CE} =2V			1	V
Output capacitance	C _{ob}	V _{CB} =10V, f=1MHz			20	pF
Turn-on time	t _(on)	V _{CC} =10V, I _C =1A, I _{B1} =-I _{B2} =10mA		170		ns
Turn-off time	t _(off)			750		ns
Transition frequency	f _T	V _{CE} =10V,I _C =50mA, f=100MHz	100			MHz

Notes:

- 1. Maximum power dissipation is calculated assuming that the device is mounted on a ceramic substrate measuring 15x15x0.6mm.
- 2. Pulse test: Pulse width≤300µs,duty cycle≤2.0%.



