

VS78L06 Three-terminal positive voltage regulator

FEATURES

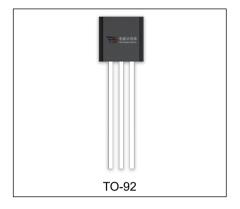
Maximum output current

I_{OM}: 0.1A

Output voltage V_O: 6V

Continuous total dissipation

P_D: 0.625 W (T_a= 25 °C)



ORDERING INFORMATION

Part Number	Package	Packing Method	Pack Quantity
VS78L06	TO-92	Bulk	1000pcs/Bag
VS78L06-TA	TO-92	Tape	2000pcs/Box

ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

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Parameter	Symbol	Value	Unit
Input Voltage	Vi	30	V
Thermal Resistance from Junction to Ambient	R _{0JA}	160	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	$^{\circ}$
Storage Temperature Range	T _{STG}	-65~+150	℃

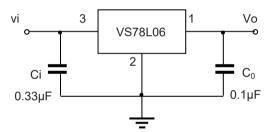


 $\textbf{T}_{a}\textbf{=25} \, \, \text{`C unless otherwise specified} \qquad \text{(Vi=11V,lo=40mA,Ci=0.33} \\ \mu\text{F,Co=0.1} \\ \mu\text{F, unless otherwise specified)}$

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output voltage	Vo	T _J =25°C	5.82	6.0	6.18	V
		8V≤V _I ≤20V, Io=1mA-40mA	5.7	6.0	6.3	V
		Io=1mA-70mA	5.7	6.0	6.3	V
Load Regulation	οV3	Io=1mA-100mA,T _J =25°C		16	80	mV
		lo=1mA-40mA,T _J =25°C		9	40	mV
Line regulation	oV3	8V≤V _I ≤20V,T _J =25°C		35	175	mV
		9V≤V _I ≤20V,T _J =25°C		29	125	mV
Quiescent Current	lq	T _J =25°C		3.9	6.0	mA
Quiescent Current Change	pl3	9V≤V _I ≤20V			1.5	mA
	pl3	1mA≤I _O ≤40mA			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz,TJ=25°C		46		μV/Vo
Ripple Rejection	RR	9V≤V ≤19V,f=120Hz	40	48		dB
Dropout Voltage	Vd	T _J =25°C		1.7		V

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



