

VS78L06 Three-terminal positive voltage regulator

FEATURES

Maximum output current

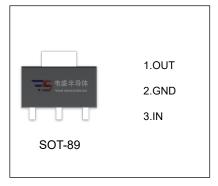
I_{OM}: 0.1A

Output voltage

V₀: 6V

Continuous total dissipation

 $P_D: 0.6 \text{ W } (T_a = 25 ^{\circ}C)$



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

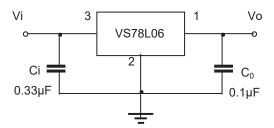
Parameter	Symbol	Value	Unit
Input Voltage	V _i	30	V
Thermal Resistance from Junction to Ambient	R _{θJA}	166.7	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	°C
Storage Temperature Range	T _{STG}	-65~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (Vi=11V,lo=40mA,Ci=0.33µF,Co=0.1µF, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output voltage Vo		T _J =25°C	5.82	6.0	6.18	V
	Vo	8V≤V _I ≤20V, Io=1mA-40mA	5.7	6.0	6.3	V
		Io=1mA-70mA	5.7	6.0	6.3	V
Load Regulation △V		lo=1mA-100mA		16	80	mV
	△Vo	lo=1mA-40mA,T _J =25°C		9	40	mV
Line regulation	△Vo	8V≤V ≤20V,TJ=25°C		35	175	mV
		9V≤V _I ≤20V,T _J =25°C		29	125	mV
Quiescent Current	Iq	T _J =25°C		3.9	6.0	mA
Quiescent Current Change ———	△lq	9V≤V ₁ ≤20V			1.5	mA
	△lq	1mA≤I _O ≤40mA			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz,T _J =25°C		46		μV/Vo
Ripple Rejection	RR	9V≤V _I ≤19V,f=120Hz	40	48		dB
Dropout Voltage	Vd	T _J =25°C		1.7		V

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



