

## VS78L08 Three-terminal positive voltage regulator

## **FEATURES**

Maximum output current

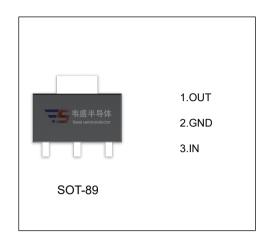
I<sub>OM</sub>: 0.1A

Output voltage

V<sub>0</sub>: 8V

Continuous total dissipation

 $P_D: 0.6 \text{ W } (T_a = 25 ^{\circ}C)$ 



## ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

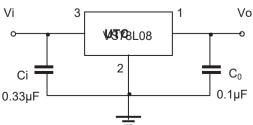
Parameter	Symbol	Value	Unit
Input Voltage	V <sub>i</sub>	30	V
Thermal Resistance from Junction to Ambient	R <sub>θJA</sub>	166.7	°C/W
Operating Junction Temperature Range	T <sub>OPR</sub>	-40~+125	℃
Storage Temperature Range	T <sub>STG</sub>	-65~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (Vi=14V, Io=40mA,Ci=0.33μF,Co=0.1μF, unless otherwise specified )

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output voltage	Vo	T <sub>J</sub> =25°C	7.76	8.0	8.24	V
		10.5V≤V <sub>I</sub> ≤23V, lo=1mA~40mA	7.6	8.0	8.4	V
		Io=1mA~70mA	7.6	8.0	8.4	V
Load Regulation Δ\	ΔVο	Io=1mA~100mA,T <sub>J</sub> =25°C		18	80	mV
	Δνο	lo=1mA~40mA,T <sub>J</sub> =25°C		10	40	mV
Line regulation	ΔVο	10.5V≤V <sub>I</sub> ≤23V,T <sub>J</sub> =25°C		42	175	mV
		11V≤V <sub>I</sub> ≤23V,T <sub>J</sub> =25°C		36	125	mV
Quiescent Current	Iq	T <sub>J</sub> =25°C		4	6	mA
Quiescent Current Change —	Δlq	11V≤V <sub>I</sub> ≤23V			1.5	mA
	Δlq	1mA≤I <sub>O</sub> ≤40mA			0.1	mA
Output Noise Voltage	V <sub>N</sub>	10Hz≤f≤100KHz,T <sub>J</sub> =25°C		54		μV/Vo
Ripple Rejection	RR	13V≤V₁≤23V,f=120Hz	37	46		dB
Dropout Voltage	Vd	T <sub>J</sub> =25°C		1.7		V

<sup>\*</sup> Pulse test.

## **TYPICAL APPLICATION**



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



