

VS78L09 Three-terminal positive voltage regulator

FEATURES

Maximum output current

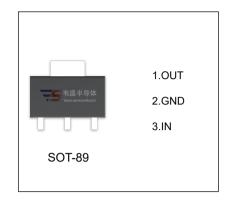
I_{OM}: 0.1A

Output voltage

V₀: 9V

Continuous total dissipation

P_D: 0.6 W (T_a= 25 °C)



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

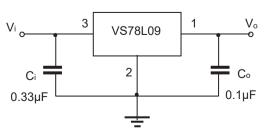
Parameter	Symbol	Value	Unit
Input Voltage	Vi	30	V
Thermal Resistance from Junction to Ambient	R _{θJA}	166.7	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	℃
Storage Temperature Range	T _{STG}	-65~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (Vi=16V,lo=40mA,Ci=0.33µF,Co=0.1µFµnless otherwise specified)

Pa rameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output voltage Vo		T _J =25°C	8.73	9.0	9.27	V
	Vo	12V≤V _I ≤24V, Io=1mA-40mA	8.55	9.0	9.45	V
		Io=1mA-70mA	8.55	9.0	9.45	V
Load Regulation △Vo		lo=1mA-100mA,T _J =25°C		19	90	mV
	△Vo	lo=1mA-40mA,T _J =25°C		11	40	mV
Line regulation $\triangle \$		12V≤V _I ≤24V,T _J =25°C		45	175	mV
	△Vo	13V≤V ≤24V,T _J =25°C		40	125	mV
Quiescent Current	Iq	T _J =25°C		4.1	6.0	mA
Quiescent Current Change	△lq	13V≤V ₁ ≤24V			1.5	mA
	△lq	1mA≤I _O ≤40mA			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz,T _J =25°C	58			μV/Vo
Ripple Rejection	RR	15V≤V _I ≤25V,f=120Hz		45		dB
Dropout Voltage	Vd	T _J =25°C		1.7		V

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



