

VS78L15 Three-terminal positive voltage regulator

FEATURES

Maximum output current

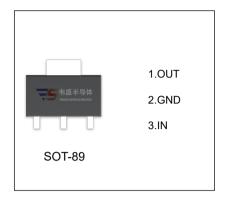
I_{OM}: 0.1A

Output voltage

V_o: 15Ŭ

Continuous total dissipation

P_D: 0.6 W (T_a= 25 °C)



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

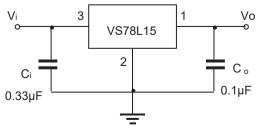
Parameter	Symbol	Value	Unit
Input Voltage	Vi	35	V
Thermal Resistance from Junction to Ambient	R _{θJA}	166.7	°C/W
Operating Junction Temperature Range	T _{OPR}	-40~+125	°C
Storage Temperature Range	T _{STG}	-65~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (Vi=23V,lo=40mA,Ci=0.33µF,Co=0.1µF,unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output voltage Vo		T _J =25°C	14.55	15	15.45	
	Vo	17.5V≤V _i ≤30V,lo=1mA-40mA	14.25	15	15.75	V
		V _i =23V, Io=1mA-70mA	14.25	15	15.75	V
Load Regulation △Vo		Io=1mA-100mA, V _i =23V,T _J =25°C		25	150	mV
	△Vo	lo=1mA-40mA, V _i =23V,T _J =25°C		15	75	mV
Line regulation		17.5V≤Vi≤30V,lo=40mA,T _J =25°C		65	300	mV
	△Vo	19V≤Vi≤30V,lo=40mA,T _J =25°C		58	250	mV
Quiescent Current	Iq	T _J =25°C		4.6	6.5	mA
Quiescent Current Change	△lq	19V≤Vi≤30V, Io=40mA			1.5	mA
	△lq	1mA≤l _O ≤40mA, Vi=23V			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz,T」=25°C		82		μV/Vo
Ripple Rejection	RR	18.5V≤Vi≤28.5V,f=120Hz	34	39		
Dropout Voltage	Vd	T _J =25°C		1.7		

^{*} Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.



