

# VS79L05 Three-terminal negative voltage regulator

### **FEATURES**

Maximum output current

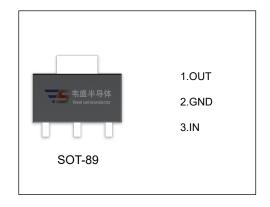
I<sub>OM</sub>: 0.1A

Output voltage

V<sub>0</sub>: -5V

Continuous total dissipation

 $P_D:0.6 \text{ W } (T_a=25 ^{\circ}\text{C})$ 



## ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

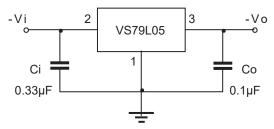
Parameter	Symbol	Value	Unit
Input Voltage	V <sub>i</sub>	-30	V
Thermal Resistance from Junction to Ambient	R <sub>θJA</sub>	208.3	°C/W
Operating Junction Temperature Range	T <sub>OPR</sub>	-40~+125	°C
Storage Temperature Range	T <sub>STG</sub>	-65~+150	°C

## ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JINCTION TEMPERATURE (VI=-10V,Io=40mA,Ci=0.33μF,Co=0.1μF, unless otherwise specified )

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Output Voltage	Vo	T <sub>J</sub> =25℃	-4.85	-5.0	-5.15	V
		-7V≤V <sub>I</sub> ≤-20V, Io=1mA~40mA	-4.75	-5.0	-5.25	V
		lo=1mA~70mA	-4.75	-5.0	-5.25	V
Load Regulation	ΔVο	lo=1mA~100mA ,T <sub>J</sub> =25℃		20	60	mV
		lo=1mA~40mA ,T <sub>J</sub> =25℃		10	30	mV
Line Regulation	ΔVο	-7V≤V <sub>I</sub> ≤-20V ,T <sub>J</sub> =25°C		15	150	mV
		-8V≤V <sub>I</sub> ≤-20V ,T <sub>J</sub> =25°C		12	100	mV
Quiescent Current	Iq	T <sub>J</sub> =25℃			6	mA
Quiescent Current Change	Δlq	-8V≤V <sub>I</sub> ≤-20V			1.5	mA
	Δlq	1mA≤V <sub>I</sub> ≤40mA			0.1	mA
Output Noise Voltage	V <sub>N</sub>	10Hz≤f≤100KHz ,T <sub>J</sub> =25°C		40		μV/Vo
Ripple Rejection	RR	-8V≤V <sub>I</sub> ≤-18V,f=120Hz ,T <sub>J</sub> =25°C	41	49		dB
Dropout Voltage	Vd	T <sub>J</sub> =25℃		1.7		V

<sup>\*</sup> Pulse test.

### **TYPICAL APPLICATION**



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as Possible to the regulators.



