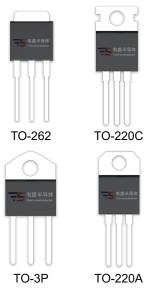


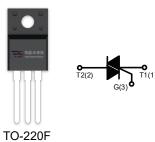
### **DESCRIPTION:**

The BTB24-800B SCR series with the parallel resistor between Gate and Cathode are especially recommended for use on straight hair, igniter, anion generator, etc.



#### **MAIN FEATURES**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	25	A
V <sub>DRM</sub> /V <sub>RRM</sub>	600/800/1200/1600	V



### **ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Value	Unit
Storage junction temperature range		T <sub>stg</sub>	-40-150	$^{\circ}\!\mathbb{C}$
Operating junction temperature range		Tj	-40-125	$^{\circ}\!\mathbb{C}$
Repetitive peak of	f-state voltage (T <sub>j</sub> =25℃)	V <sub>DRM</sub>	600/800/1200/1600	V
Repetitive peak re	verse voltage (Tj=25℃)	VRRM	600/800/1200/1600	V
RMS on-state current	TO-220A(Ins)/ TO-220F(Ins) (Tc=70°C) TO-220C/ TO-220A(Non-Ins) (Tc=85°C) TO-262 (Tc=50°C) TO-3P(Ins) (Tc=95°C)	I <sub>T(RMS)</sub>	25	Α
Non repetitive surge peak on-state current (full cycle, F=50Hz)		Ітѕм	250	А
I <sup>2</sup> t value for fusing (tp=10ms)		l <sup>2</sup> t	340	A <sup>2</sup> s



Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	dI/dt	50	A/µs
Peak gate current	Івм	4	Α
Average gate power dissipation	P <sub>G(AV)</sub>	1	W
Peak gate power	P <sub>GM</sub>	10	W

# **ELECTRICAL CHARACTERISTICS** ( $T_j$ =25 $^{\circ}$ C unless otherwise specified)

V<sub>DRM</sub> /V<sub>RRM</sub>: 600/800V

Symbol	Test Condition Quadran	Quadrant		JST24-600/800V		Unit
		Quaurani		BW	CW	Uill
Ідт	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAX	50	35	mA
V <sub>G</sub> T	VD - 12V KL - 3312	I - II -III	MAX	1	.3	V
V <sub>GD</sub>	$V_D = V_{DRM} T_j = 125$ °C $R_L = 3.3$ ΚΩ	I - II -III	MIN	0.2		V
IL	I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	MAX	80	70	mΛ
		II	IVIAA	100	80	mA
Ін	I <sub>T</sub> =100mA		MAX	75	50	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	1000	500	V/µs

V<sub>DRM</sub> /V<sub>RRM</sub>: 1200/1600V

Symbol	Test Condition Qu	Quadrant	JST24-1200V/1600V		Unit	
				BW	CW	Oilit
lgт	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAX	50	35	mA
V <sub>G</sub> T	VD = 12V KL=3312	I - II -III	MAX	1	.5	V
V <sub>GD</sub>	$V_D = V_{DRM} T_j = 125$ °C $R_L = 3.3$ ΚΩ	I - II -III	MIN	0.2		V
I.	I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	MAX	90	70	mA
l <sub>L</sub>		II	IVIAA	100	80	IIIA
Ін	I <sub>T</sub> =100mA		MAX	80	60	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	1500	1000	V/µs



V<sub>DRM</sub> /V<sub>RRM</sub>: 600/800V

Symbol	Test Condition	Quadrant		JST24-600/800V		Linit
				В	С	Unit
l		I - II -III	MAY	50	25	m A
lgт	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	IV	IV MAX	70	50	mA
V <sub>GT</sub>		ALL	MAX	1	.3	V
$V_{GD}$	$V_D = V_{DRM} T_j = 125^{\circ}C$ $R_L = 3.3 K\Omega$	ALL	MIN	0.2		V
I.	I <sub>G</sub> =1.2I <sub>GT</sub>	I -III-IV	MAX	80	70	m 1
l L		II	IVIAA	100	90	mA
I <sub>H</sub>	I <sub>T</sub> =100mA		MAX	75	60	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	500	200	V/µs

## **STATIC CHARACTERISTICS**

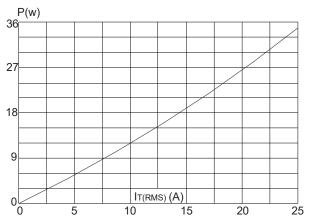
Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>TM</sub> =35A tp=380μs	T <sub>j</sub> =25℃	1.5	V
IDRM	\\ _\\ _\\ _\\	T <sub>j</sub> =25℃	5	μA
IRRM	$V_D = V_{DRM} V_R = V_{RRM}$	T <sub>j</sub> =125℃	3	mA

### **THERMAL RESISTANCES**

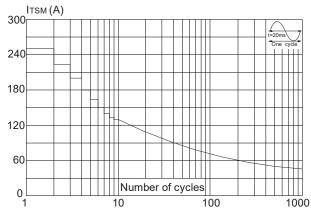
Symbol	Parame	Value	Unit	
		TO-220A(Ins)	1.5	
	junction to case(AC)	TO-220C/ TO-220A(Non-Ins)	1.1	
R <sub>th(j-c)</sub>		TO-220F(Ins)	1.7	°C/W
		TO-262	2.1	
		TO-3P(Ins)	0.67	



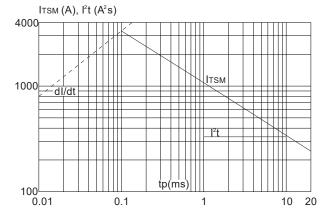
**FIG.1:** Maximum power dissipation versus RMS on-state current



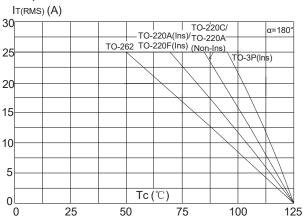
**FIG.3:** Surge peak on-state current versus number of cycles



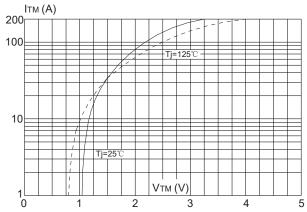
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of I<sup>2</sup>t (dI/dt < 50A/µs)



**FIG.2:** RMS on-state current versus case temperature



**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

