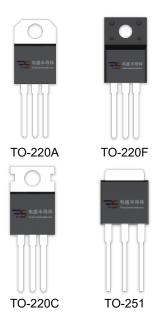
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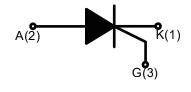
DESCRIPTION:

With high ability to withstand the shock loading of large current, BT151X-500R series of silicon controlled rectifiers provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.



MAIN FEATURES

Symbol	Value	Symbol
VDRM/ VRRM	650/800	V
I _{T(RMS)}	12	А
l _{GT}	≤15	mA



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40 - 150	${\mathbb C}$
Operating junction temperature range		Tj	-40 - 150	$^{\circ}$
Repetitive peak off-state voltage (T _j =25°ℂ)		V_{DRM}	650/800	V
Repetitive peak reverse voltage (T _j =25°C)		V _{RRM}	650/800	V
RMS on-state current	TO-251/ TO-220C/ TO-220A (Non-Ins) (T _C =130℃) TO-220A (Ins)/ TO-220F (Ins) (T _C =125℃)	I _{T(RMS)}	12	А
Non repetitive surge peak on-state current (F=50Hz tp=10ms)		Ітѕм	120	А

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Non repetitive surge peak on-state current (F=60Hz tp=8.3ms)	I _{TSM}	132	Α
I ² t value for fusing (tp=10ms)	l²t	72	A ² s
Repetitive rate of rise of on-state current $(I_G=2\times I_{GT})$	dl⊤/dt	50	A/µs
Peak gate current	Ідм	2	Α
Peak gate power	P _{GM}	5	W
Average gate power dissipation	P _{G(AV)}	0.5	W

ELECTRICAL CHARACTERISTICS (T_j=25°C unless otherwise specified)

Symbol	Test Condition	Value			llnit
	rest Condition	MIN.	TYP.	MAX.	Unit
I _{GT}	V -40V D -220	-	4	15	mA
V _{GT}	$V_D=12V R_L=33\Omega$	-	0.75	1.5	V
V _{GD}	$V_D=V_{DRM}T_j=150^{\circ}C$ RL=3.3K Ω	0.2	-	-	V
IL	I _G =1.2I _{GT}	-	12	40	mA
lн	I⊤=500mA	-	12	30	mA
dV/dt	V _D =540V Gate Open T _j =150℃	50	-	-	V/µs
dV/dt	V _D =436V Gate Open T _j =150℃	80	-	-	V/µs
ton	I _{GT} =20mA I _A =100mA I _R =10mA	-	2	-	μs
t _{off}	T _j =25℃	-	30	-	μs
R _d	Dynamic resistance T _j =125℃	-	-	35	mΩ

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V_{TM}	I _{TM} =23A tp=380μs	T _j =25℃	1.6	V
IDRM	V _D =V _{DRM} V _R =V _{RRM}	Tj=25℃	10	μA
I _{RRM}		Tj=150℃	1	mA



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THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
Rth(j-c)	Junction to case	TO-251/ TO-220C/ TO-220A (Non-Ins)	1.3	· °C/W
		TO-220A (Ins)	1.6	C/VV
		TO-220F (Ins)	1.7	



FIG.1: Maximum power dissipation versus RMS on-state current

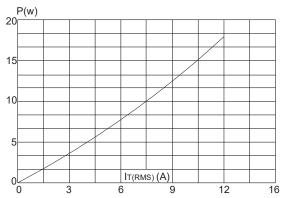


FIG.3: Surge peak on-state current versus number of cycles

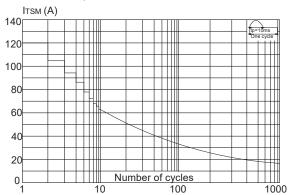


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I²t (dI/dt < 50A/μs)

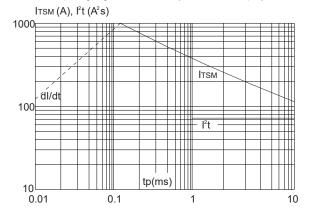


FIG.2: RMS on-state current versus case temperature

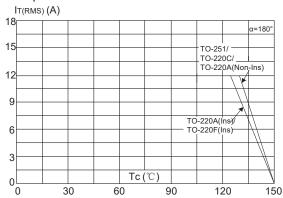


FIG.4: On-state characteristics (maximum values)

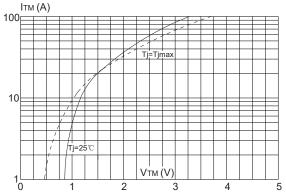


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

