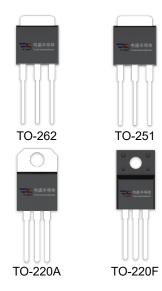


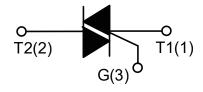
#### **DESCRIPTION:**

With high ability to withstand the shock loading of large current, T405-600T series triacs provide high dv/dt rate with strong resistance to electromagnetic interface. With high commutation performances, 3 quadrants products especially recommended for use on inductive load.



### **MAIN FEATURES**

Symbol	Value	Unit
V <sub>DRM</sub> /V <sub>RRM</sub>	600/800	V
I <sub>T(RMS)</sub>	4	А



## **ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Value	Unit
Storage junction temperature range		T <sub>stg</sub>	-40 - 150	$^{\circ}$ C
Operating junction temperature range		Tj	-40 - 125	$^{\circ}$
Repetitive peak off-state voltage (T <sub>j</sub> =25℃)		V <sub>DRM</sub>	600/800	V
Repetitive peak reverse voltage (T <sub>j</sub> =25℃)		V <sub>RRM</sub>	600/800	V
RMS on-state current	TO-251/ TO-220A(Ins)/ TO-220F(Ins) (Tc=100℃) TO-220A(Non-Ins)/ TO-262 (Tc=105℃)	It(RMS)	4	А
Non repetitive surge peak on-state current (full cycle, F=50Hz)		I <sub>TSM</sub>	40	Α
I <sup>2</sup> t value for fusing (tp=10ms)		l <sup>2</sup> t	8	A <sup>2</sup> s



Critical rate of rise of on-state current $(I_G = 2 \times I_{GT})$	dl/dt	50	A/µs
Peak gate current	I <sub>GM</sub>	4	Α
Average gate power dissipation	P <sub>G(AV)</sub>	1	W
Peak gate power	P <sub>GM</sub>	5	W

# **ELECTRICAL CHARACTERISTICS** ( $T_j$ =25 $^{\circ}$ C unless otherwise specified)

Symbol	Test Condition	Quadrant		Value			Unit	
				TW	sw	CW	BW	Uill
lgт	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAX	5	10	35	50	mA
V <sub>G</sub> T		I - II -III	MAX		1	.5		V
V <sub>GD</sub>	$V_D = V_{DRM} T_j = 125^{\circ}C$ $R_L = 3.3 K\Omega$	I - II -III	MIN	0.2			V	
IL	I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	MAX	10	20	50	70	mA
		II	IVIAA	15	35	60	80	IIIA
Ін	I <sub>T</sub> =100mA		MAX	10	15	35	60	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	50	100	400	1000	V/µs

# **STATIC CHARACTERISTICS**

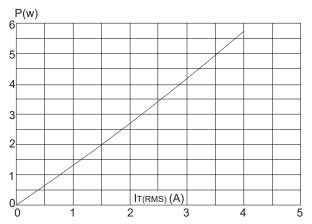
Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>тм</sub> =5.5A tp=380µs	T <sub>j</sub> =25℃	1.5	V
IDRM	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25℃	10	μA
I <sub>RRM</sub>		Tj=125℃	0.75	mA

# **THERMAL RESISTANCES**

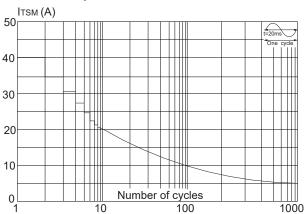
Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	junction to case(AC)	TO-251	2.8	°C/W
		TO-220A(Ins)	3.0	
		TO-262/ TO-220A(Non-Ins)	2.5	
		TO-220F(Ins)	3.3	



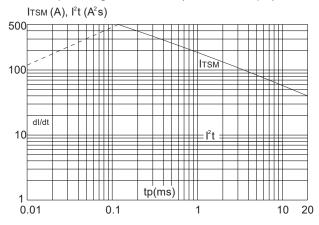
**FIG.1:** Maximum power dissipation versus RMS on-state current



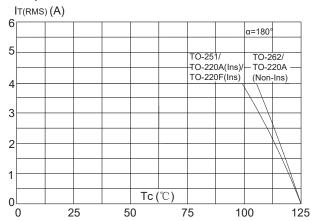
**FIG.3:** Surge peak on-state current versus number of cycles



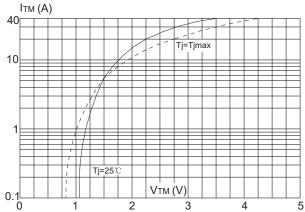
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms and corresponding value of I<sup>2</sup>t (dI/dt < 50A/µs)



**FIG.2:** RMS on-state current versus case temperature



**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

