Shenzhen VSEEI Semiconductor Co., Ltd

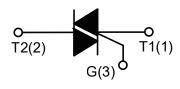
### **DESCRIPTION:**

The Z0107MN SCR series with the parallel resistor between Gate and Cathode are especially recommended for use on straight hair, igniter, anion generator, etc.



### **MAIN FEATURES**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	1	Α
Ітѕм	16	А
V <sub>TM</sub>	≤1.5	V



## **ABSOLUTE MAXIMUM RATINGS**

Param	Symbol	Value	Unit	
Storage junction temperature range		T <sub>stg</sub>	-40 - 150	$^{\circ}\mathbb{C}$
Operating junction temperature range		Tj	-40 - 125	$^{\circ}$ C
Repetitive peak off-state voltage (T <sub>j</sub> =25℃)		VDRM	600/800	V
Repetitive peak reverse voltage (T <sub>j</sub> =25℃)		V <sub>RRM</sub>	600/800	V
RMS on-state current	SOT-223/ SOT-89/ SOT-223-2L (T <sub>C</sub> =75°C)	I <sub>T(RMS)</sub>	1	А
Non repetitive surge peak on-state current (full cycle, F=50Hz)		Ітѕм	16	А
I <sup>2</sup> t value for fusing (tp=10ms)		l²t	1.28	A <sup>2</sup> s
Critical rate of rise of on-state current $(I_G=2\times I_{GT})$		dl/dt	20	A/µs
Peak gate current		l <sub>GM</sub>	2	Α
Average gate power dissipation		P <sub>G(AV)</sub>	0.5	W
Peak gate power		P <sub>GM</sub>	5	W



# **ELECTRICAL CHARACTERISTICS** (T<sub>j</sub>=25 °C unless otherwise specified)

Symbol	Test Condition	Quadrant		Va	lue	Hoit
Symbol				Т	D Unit	Unit
l <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAX	5	5	mA
		IV		5	10	
V <sub>G</sub> T		ALL	MAX	1.3		V
V <sub>GD</sub>	$V_D=V_{DRM}T_j=125$ °C RL=3.3KΩ	ALL	MIN	0.2		V
IL	I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	MAX	5	5	mA
		II -IV		10	20	
Ін	I <sub>T</sub> =200mA		MAX	5	7	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	15	20	V/µs

# **STATIC CHARACTERISTICS**

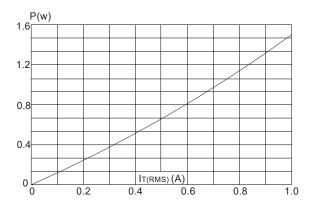
Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>тм</sub> =1.4A tp=380µs	T <sub>j</sub> =25℃	1.5	V
I <sub>DRM</sub>	VD=VDRM VR=VRRM	T <sub>j</sub> =25℃	5	μA
I <sub>RRM</sub>		Tj=125℃	500	μA

# **THERMAL RESISTANCES**

Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	junction to case(AC)	SOT-223/ SOT-89-2L/ SOT-223-2L	31	°C/W
R <sub>th(j-a)</sub>	junction to ambient	SOT-89-2L	64	
		SOT-223/ SOT-223-2L	60	°C/W



**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.3:** Surge peak on-state current versus number of cycles

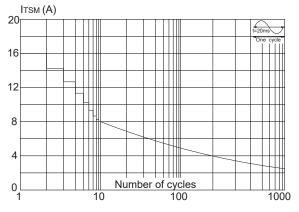
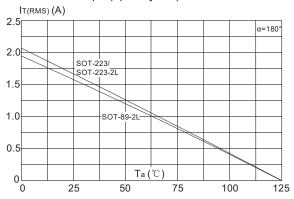
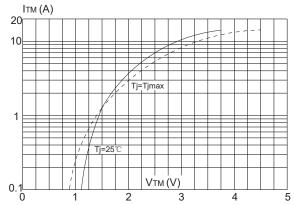


FIG.2: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness: 35µm) (full cycle)

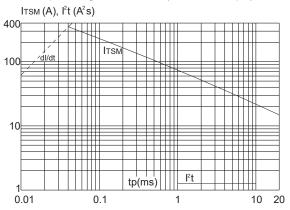


**FIG.4:** On-state characteristics (maximum values)

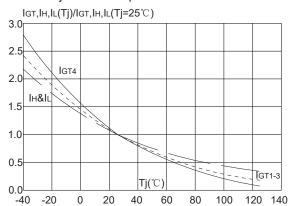




**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms and corresponding value of I<sup>2</sup>t (dI/dt < 20A/µs)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature



#### **SOLDERING PARAMETERS**

Reflow Condition		Pb-Free assembly (see figure at right)	
Pre Heat	-Temperature Min (T <sub>s(min)</sub> )	+150°C	
	-Temperature Max(T <sub>s(max)</sub> )	+200℃	
	-Time (Min to Max) (ts)	60-180 secs.	
Average ramp up rate (Liquidus Temp (T <sub>L</sub> )to peak)		3℃/sec. Max	
T <sub>s(max)</sub> to T <sub>L</sub> - Ramp-up Rate		3℃/sec. Max	
Reflow	-Temperature(T <sub>L</sub> ) (Liquidus)	+217℃	
	-Temperature(t∟)	60-150 secs.	
Peak Temp (T <sub>p</sub> )		+260(+0/-5)°C	
Time within 5°C of actual Peak Temp (t₀)		20-40secs.	
Ramp-down Rate		6℃/sec. Max	
Time 25℃ to Peak Temp (T <sub>P</sub> )		8 min. Max	
Do not exceed		+260℃	

