

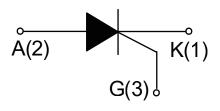
DESCRIPTION:

The P0102DA SCR provides high dv/dt rate with strong resistance to electromagnetic interface. They are especially recommended for use on residual current circuit breaker, straight hair, igniter etc.



MAIN FEATURES

| Symbol | Value | Unit |
|------------------------------------|---------|------|
| I _{T(RMS)} | 0.8 | А |
| I _{GT} | ≤120 | μA |
| V _{DRM} /V _{RRM} | 400/600 | V |



ABSOLUTE MAXIMUM RATINGS

| Parameter | | Symbol | Value | Unit |
|---|---------------------------------|---------------------|----------------------|------------------|
| Storage junction temperature range | | T _{stg} | -40-150 | $^{\circ}$ |
| Operating junction temperature range | | Tj | -40-125 ^① | $^{\circ}$ |
| Repetitive peak off-state voltage | | V _{DRM} | 400/600 | V |
| Repetitive peak reverse voltage | | V_{RRM} | 400/600 | V |
| RMS on-state current | TO-92 (T _C =60°C) | I _{T(RMS)} | 0.8 | А |
| Non repetitive surge peak on-state current (tp=10ms) | | ITSM | 8 | А |
| I ² t value for fusing (tp=10ms) | | l ² t | 0.32 | A ² s |
| Critical rate of rise of on-state current | | dl/dt | 50 | A/µs |
| Peak gate current (tp=20µs, T _j =125℃) | | I _{GM} | 0.2 | Α |
| Peak gate power (tp=20µs, T _j =125℃) | | Рдм | 0.5 | W |
| Average gate power dissipation(T _j =125°C) | | P _{G(AV)} | 0.1 | W |

NOTE 1: When we parallel connect a $\leq 1K\Omega$ resistor between Gate and Cathode, the Tj can reach 125° C; if without this resistor, the Tj only can reach 110° C.



ELECTRICAL CHARACTERISTICS (T_j =25 $^{\circ}$ C unless otherwise specified)

| Symbol | Toot Condition | Value | | | 11:4 |
|-----------------|--|-------|------|------|------|
| | Test Condition | MIN. | TYP. | MAX. | Unit |
| Ідт | V _D =12V R _L =33Ω | - | 30 | 120 | μA |
| V _{GT} | VD-12V KL-3312 | - | 0.6 | 0.8 | V |
| V _{GD} | V _D =V _{DRM} T _j =125°C | 0.2 | - | - | V |
| IL | I _G =1.2 I _{GT} | - | - | 5 | mA |
| Ін | Iτ=0.05A | - | - | 3 | mA |
| dV/dt | V _D =2/3V _{DRM} T _j =125℃ R _{GK} =1KΩ | 10 | - | - | V/µs |
| Ton | I _{TM} =2A V _D =V _{DRM(max)} I _G =10mA dI _G /dt=0.1A/µs | - | - | 3 | μs |

STATIC CHARACTERISTICS

| Symbol | Parameter | | Value(MAX) | Unit |
|------------------|-----------------------------|---------------------|------------|------|
| V _{TM} | I _T =1A tp=380μs | T _j =25℃ | 1.5 | V |
| IDRM | VD=VDRM VR=VRRM | Tj=25℃ | 5 | μΑ |
| I _{RRM} | | Tj=125℃ | 100 | μA |

THERMAL RESISTANCES

| Symbol | Parameter | | Value | Unit |
|----------------------|------------------|-------|-------|------|
| R _{th(j-c)} | junction to case | TO-92 | 75 | °C/W |



FIG.1: Maximum power dissipation versus RMS on-state current

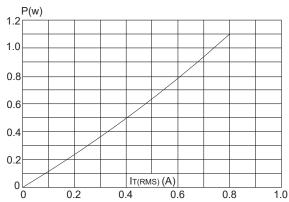


FIG.3: Surge peak on-state current versus number of cycles

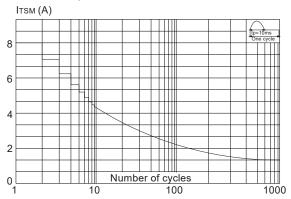


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I²t

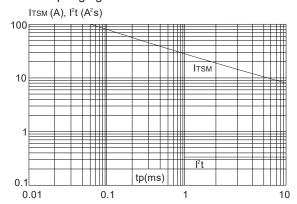


FIG.2: RMS on-state current versus case temperature

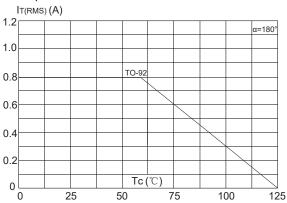


FIG.4: On-state characteristics (maximum values)

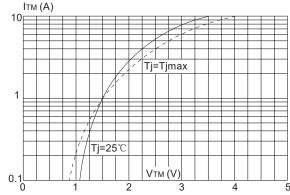


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

