

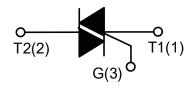
### **DESCRIPTION:**

With high ability to withstand the shock loading of large current, BTA225B-600B series triacs provide high dv/dt rate with strong resistance to electromagnetic interface. With high commutation performances, 3 quadrants products especially recommended for use on inductive load.



#### **MAIN FEATURES**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	25	А
V <sub>DRM</sub> /V <sub>RRM</sub>	600/800/1200/1600	V



### **ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Value	Unit
Storage junction temperatur	e range	T <sub>stg</sub>	-40-150	$^{\circ}\!\mathbb{C}$
Operating junction temperat	ure range	Tj	-40-125	$^{\circ}\!\mathbb{C}$
Repetitive peak off-state vol	tage (Tj=25℃)	VDRM	600/800/1200/1600	V
Repetitive peak reverse volt	age (Tj=25℃)	VRRM	600/800/1200/1600	V
RMS on-state current	TO-263 (Tc=75°C)	I <sub>T(RMS)</sub>	25	А
Non repetitive surge peak on-state current (full cycle, F=50Hz)		I <sub>TSM</sub>	250	Α
I <sup>2</sup> t value for fusing (tp=10ms	s)	l <sup>2</sup> t	340	A <sup>2</sup> s
Critical rate of rise of on-state current $(I_G = 2 \times I_{GT})$		dI/dt	50	A/µs
Peak gate current		l <sub>GM</sub>	4	Α
Average gate power dissipation		P <sub>G(AV)</sub>	1	W
Peak gate power		Рдм	10	W



# **ELECTRICAL CHARACTERISTICS** ( $T_j$ =25 $^{\circ}$ C unless otherwise specified)

V<sub>DRM</sub> /V<sub>RRM</sub>: 600/800V

Symbol	Toot Condition	Test Condition Quadrant		JST24-600/800V		Unit
Symbol	rest Condition			BW	CW	Unit
lgт	\/ <sub>0</sub> =12\/ B. =22O	I - II -III	MAX	50	35	mA
V <sub>G</sub> T	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAX	1	.3	V
V <sub>GD</sub>	$V_D = V_{DRM} T_j = 125$ °C $R_L = 3.3$ ΚΩ	I - II -III	MIN	0	.2	V
1.	I <sub>L</sub> I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	MAX	80	70	mA
IL.		II	IVIAA	100	80	ША
Ін	I <sub>T</sub> =100mA		MAX	75	50	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	1000	500	V/µs

V<sub>DRM</sub> /V<sub>RRM</sub>: 1200/1600V

Symbol	Test Condition	Quadrant		JST24-120	00V/1600V	Unit
Symbol	rest Condition	Quaurant		BW	CW	Ullit
I <sub>GT</sub>	· V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAX	50	35	mA
V <sub>G</sub> T	VD-12V KL-3312	I - II -III	MAX	1	.5	V
V <sub>GD</sub>	$V_D = V_{DRM} T_j = 125$ °C $R_L = 3.3$ ΚΩ	I - II -III	MIN	0	.2	V
1.	I <sub>L</sub> I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	NAAV	90	70	тΛ
IL.		II	MAX	100	80	mA
I <sub>H</sub>	I <sub>T</sub> =100mA		MAX	80	60	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	1500	1000	V/µs

V<sub>DRM</sub> /V<sub>RRM</sub>: 600/800V

Cymphal	maked Teet Condition Overdont			JST24-6	600/800V	I I mit
Symbol	Test Condition	Quadrant		В	С	Unit
1	I <sub>GT</sub> V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II -III	MAY	50	25	m A
IGT			IV	MAX	70	50
V <sub>G</sub> T		ALL	MAX	1	.3	V
V <sub>GD</sub>	$V_D = V_{DRM} T_j = 125$ °C $R_L = 3.3$ ΚΩ	ALL	MIN	0	.2	V



l 4.01	I -III-IV	MAX	80	70	mΛ	
IL IL	I <sub>L</sub>   I <sub>G</sub> = 1.2I <sub>GT</sub>	IG = 1.2IGT	MAX	100	90	mA
Ін	I <sub>T</sub> =100mA		MAX	75	60	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	500	200	V/µs

## **STATIC CHARACTERISTICS**

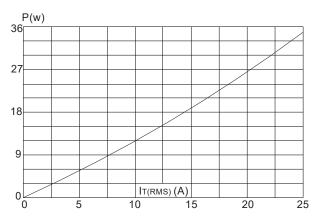
Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>ТМ</sub> =35A tp=380µs	Tj=25℃	1.5	V
IDRM		Tj=25℃	5	μA
I <sub>RRM</sub>	VD=VDRM VR=VRRM	T <sub>j</sub> =125℃	3	mA

## **THERMAL RESISTANCES**

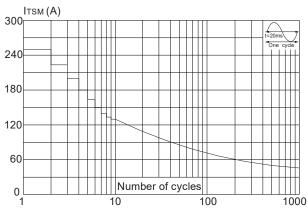
Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	junction to case(AC)	TO 262	1.3	°C/W
R <sub>th(j-a)</sub>	junction to ambient	TO-263	45	C/VV



**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.3:** Surge peak on-state current versus number of cycles



**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of I<sup>2</sup>t (dI/dt < 50A/µs)

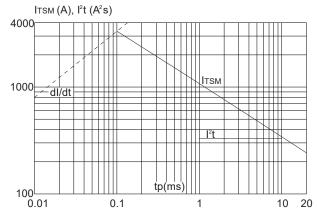
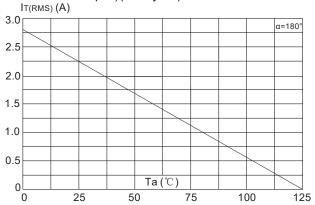
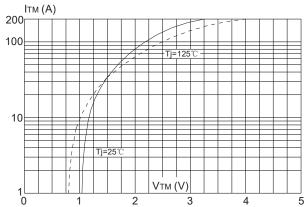


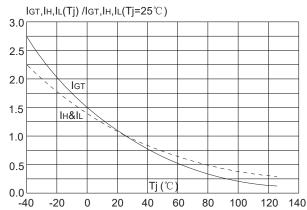
FIG.2: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness:35µm)(full cycle)



**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature





## **SOLDERING PARAMETERS**

Defloy C	anditi an	Pb-Free assembly
Reflow Condition		(see figure at right)
	-Temperature Min (T <sub>s(min)</sub> )	+150°C
Pre Heat	-Temperature Max(T <sub>s(max)</sub> )	+200℃
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquidus Temp (T <sub>L</sub> )to peak)		3°C/sec. Max
T <sub>s(max)</sub> to	T∟ - Ramp-up Rate	3℃/sec. Max
Reflow	-Temperature(T <sub>L</sub> ) (Liquidus)	+217℃
	-Temperature(t∟)	60-150 secs.
Peak Tem	ηρ (T <sub>p</sub> )	+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t₀)		20-40secs.
Ramp-down Rate		6℃/sec. Max
Time 25℃ to Peak Temp (T <sub>P</sub> )		8 min. Max
Do not ex	ceed	+260℃

