Shenzhen VSEEI Semiconductor Co., Ltd

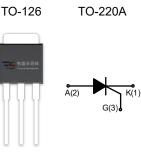
DESCRIPTION:

The BT148-400R SCR provides high dv/dt rate with strong resistance to electromagnetic interface. They are especially recommended for use on residual current circuit breaker, straight hair, igniter etc.

TO-126 TO-220A

MAIN FEATURES

| Symbol | Value | Unit |
|---------------------|-------|------|
| I _{T(RMS)} | 4 | А |
| lgт | ≤200 | μΑ |



ABSOLUTE MAXIMUM RATINGS

| Parameter | | Symbol | Value | Unit |
|--|---|---------------------|------------------------|------------------------|
| Storage junction temperature range | | T _{stg} | -40 - 150 | $^{\circ}$ C |
| Operating junction temperature range | | Tj | -40 - 125 ¹ | $^{\circ}\!\mathbb{C}$ |
| Repetitive peak off-state voltage | | V _{DRM} | 600 | V |
| Repetitive peak reverse voltage | | V _{RRM} | 600 | V |
| RMS on-state current | TO-126/ TO-202-3 (Tc=85℃) | I _{T(RMS)} | 4 | А |
| | TO-251 (T _C =90°C) | | | |
| | TO-220A(Non-Ins) (T _C =105°C) | | | |
| Non repetitive surge peak on-state current (tp=10ms) | | Ітѕм | 30 | А |
| I ² t value for fusing (tp=10ms) | | l ² t | 4.5 | A ² s |
| Critical rate of rise of on-state current | | dI/dt | 50 | A/µs |
| Peak gate current (tp=20µs, T _j =125℃) | | I _{GM} | 1.2 | Α |
| Peak gate power (tp=20µs, T _j =125℃) | | P _{GM} | 2 | W |
| Average gate power dissipation(T _j =125℃) | | P _{G(AV)} | 0.2 | W |

NOTE 1: When we parallel connect a $\leq 1K\Omega$ resistor between Gate and Cathode, the Tj can reach $125^{\circ}\mathbb{C}$; if without this resistor, the Tj only can reach $110^{\circ}\mathbb{C}$.

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ELECTRICAL CHARACTERISTICS (T_j =25 $^{\circ}$ C unless otherwise specified)

| Symbol | Test Condition | Value | | | Hoit |
|------------------|--|-------|------|------|------|
| | rest Condition | MIN. | TYP. | MAX. | Unit |
| Ідт | V _D =12V R _L =33Ω | - | 50 | 200 | μA |
| V _G T | VD-12V KL-3312 | - | 0.6 | 0.8 | V |
| V _{GD} | V _D =V _{DRM} T _j =125°C | 0.2 | - | - | V |
| IL | I _G =1.2 I _{GT} | - | 1 | 6 | mA |
| Ін | I _T =0.05A | - | 1 | 5 | mA |
| dV/dt | V _D =2/3V _{DRM} T _j =125°C R _{GK} =1KΩ | 10 | - | - | V/µs |

STATIC CHARACTERISTICS

| Symbol | Parameter | | Value(MAX) | Unit |
|------------------|-----------------------------|----------------------|------------|------|
| V _{TM} | I _T =8A tp=380μs | T _j =25℃ | 1.5 | V |
| I _{DRM} | VD=VDRM VR=VRRM | T _j =25℃ | 5 | μA |
| I _{RRM} | | T _j =125℃ | 100 | μΑ |

THERMAL RESISTANCES

| Symbol | Parameter | | Value | Unit |
|----------|------------------|----------------------|-------|--------|
| Rth(j-c) | junction to case | TO-126 | 7.2 | · °C/W |
| | | TO-251 | 6.5 | |
| | | TO-220A(Non-Ins) 3.0 | | |
| | | TO-202-3 | 7.6 | |



FIG.1: Maximum power dissipation versus RMS on-state current

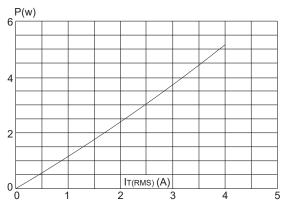


FIG.3: Surge peak on-state current versus number of cycles

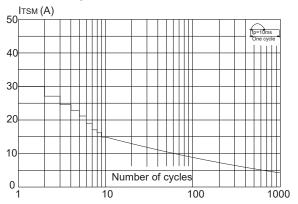


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I²t (dl/dt < 50A/μs)

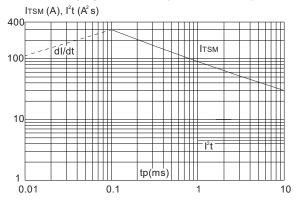


FIG.2: RMS on-state current versus case temperature

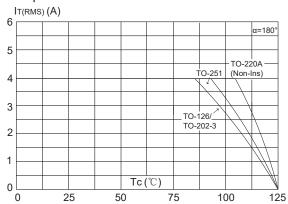


FIG.4: On-state characteristics (maximum values)

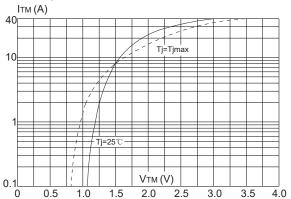


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

