### **DESCRIPTION:**

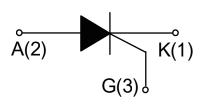
VSEEL

The P0102DA SCR provides high dv/dt rate with strong resistance to electromagnetic interface. They are especially recommended for use on residual current circuit breaker, straight hair, igniter etc.



#### **MAIN FEATURES**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	0.8	А
lgт	≤200	μΑ
V <sub>DRM</sub> /V <sub>RRM</sub>	600	V



## **ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Value	Unit
Storage junction temperature range		T <sub>stg</sub>	-40-150	$^{\circ}\!\mathbb{C}$
Operating junction temperature range		Tj	-40-125 <sup>①</sup>	$^{\circ}$ C
Repetitive peak off-state voltage		V <sub>DRM</sub>	600	V
Repetitive peak reverse voltage		V <sub>RRM</sub>	600	V
RMS on-state current	TO-92/TO-92UR (Tc=65°C)	I <sub>T(RMS)</sub>	0.8	А
Non repetitive surge peak on-state current (F=50Hz tp=10ms)		ITSM	8	А
Non repetitive surge peak on-state current (F=60Hz tp=8.3ms)		I <sub>TSM</sub>	9	А
I <sup>2</sup> t value for fusing (tp=10ms)		l <sup>2</sup> t	0.32	A <sup>2</sup> s
Critical rate of rise of on-state current		dl/dt	50	A/µs
Peak gate current (tp=20µs, T <sub>j</sub> =125℃)		I <sub>GM</sub>	0.2	Α
Peak gate power (tp=20µs, T <sub>j</sub> =125℃)		P <sub>GM</sub>	0.5	W
Average gate power dissipation(T <sub>j</sub> =125℃)		P <sub>G(AV)</sub>	0.1	W

**NOTE 1:** When we parallel connect a  $\leq 1K\Omega$  resistor between Gate and Cathode, the Tj can reach  $125^{\circ}\mathbb{C}$ ; if without this resistor, the Tj only can reach  $110^{\circ}\mathbb{C}$ .

# **ELECTRICAL CHARACTERISTICS** ( $T_j$ =25 $^{\circ}$ C unless otherwise specified)

Symbol	Toot Condition	Value			I I sa i 4
	Test Condition	MIN.	TYP.	MAX.	Unit
Іст	V-=40V D-=220	20	50	200	μA
V <sub>G</sub> T	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	-	0.6	0.8	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125℃	0.2	-	-	V
IL	I <sub>G</sub> =1.2 I <sub>GT</sub>	-	-	4	mA
Ін	I <sub>T</sub> =0.05A	-	-	3	mA
dV/dt	V <sub>D</sub> =400V T <sub>j</sub> =125°C R <sub>GK</sub> =1KΩ	600	-	-	V/µs
dV/dt	V <sub>D</sub> =400V T <sub>j</sub> =125°C R <sub>GK</sub> =220Ω	1000	-	-	V/µs
t <sub>on</sub>	I <sub>G</sub> =10mA I <sub>A</sub> =4mA I <sub>R</sub> =0.4mA	-	2	-	μs
t <sub>off</sub>	T <sub>j</sub> =25℃	-	50	-	μs
R₀	Dynamic Resistance T <sub>j</sub> =125℃	-	-	35	mΩ

## **STATIC CHARACTERISTICS**

VSEEL

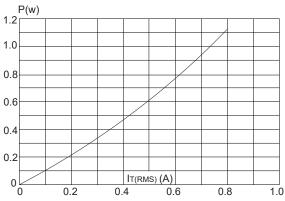
Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>T</sub> =1.1A tp=380μs	T <sub>j</sub> =25℃	1.5	V
IDRM	VD=VDRM VR=VRRM	T <sub>j</sub> =25℃	5	μΑ
IRRM		T <sub>j</sub> =125℃	100	μΑ

## THERMAL RESISTANCES

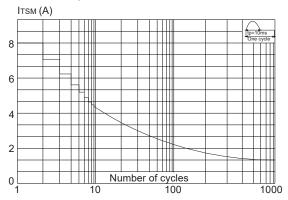
Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	junction to case	TO-92/TO-92UR	75	°C/W

**FIG.1** Maximum power dissipation versus RMS on-state current

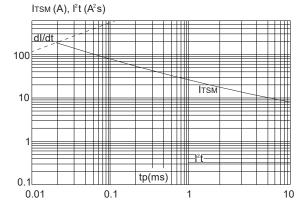
VSEE



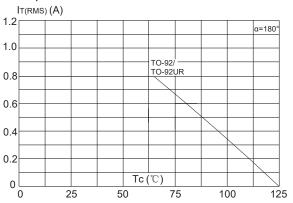
**FIG.3:** Surge peak on-state current versus number of cycles



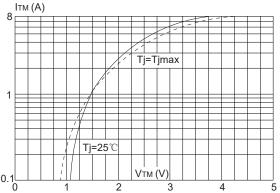
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of  $I^2t$  (dI/dt < 50A/ $\mu$ s)



**FIG.2:** RMS on-state current versus case temperature



**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

