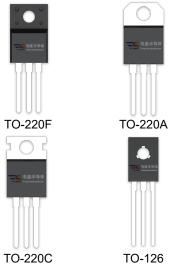


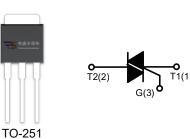
### **DESCRIPTION:**

The BTB04-600SL SCR series with the parallel resistor between Gate and Cathode are especially recommended for use on straight hair, igniter, anion generator, etc.



#### **MAIN FEATURES**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	4	А
V <sub>DRM</sub> /V <sub>RRM</sub>	600/800	V



### **ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Value	Unit
Storage junction temperature range		T <sub>stg</sub>	-40-150	$^{\circ}$ C
Operating junction temperature range		perating junction temperature range T <sub>j</sub> -40-12		$^{\circ}$ C
Repetitive peak	off-state voltage(T <sub>j</sub> =25℃)	V <sub>DRM</sub>	600/800	V
Repetitive peak	reverse voltage(T <sub>j</sub> =25°ℂ)	$V_{RRM}$	600/800	V
Non repetitive surge peak Off-state voltage		V <sub>DSM</sub>	V <sub>DRM</sub> + 100	V
Non repetitive peak reverse voltage		V <sub>RSM</sub>	V <sub>RRM</sub> + 100	V
RMS on-state current	TO-251/ TO-220A(Non-Ins) /TO-220C (Tc=105°C) TO-220A(Ins)/ TO-220F(Ins) (Tc=100°C) TO-202-3/ TO-126/SOT-82 (Tc=95°C)	I <sub>T(RMS)</sub>	4	А



Non repetitive surge peak on-st (full cycle, F=50Hz)	I <sub>TSM</sub>	35	А		
I <sup>2</sup> t value for fusing (tp=10ms)	l <sup>2</sup> t	6.1	A <sup>2</sup> s		
Critical rate of rise of on-state	I - II -III	dI/dt	50	Λ/ι.ο	
current (I <sub>G</sub> =2×I <sub>GT</sub> )	IV	dl/dt	10	A/µs	
Peak gate current	I <sub>GM</sub>	2	Α		
Average gate power dissipation		P <sub>G(AV)</sub>	0.5	W	
Peak gate power		P <sub>GM</sub>	5	W	

# **ELECTRICAL CHARACTERISTICS** (T<sub>j</sub>=25 °C unless otherwise specified)

Symbol	Test Condition	Quadrant		Value		Hoit		
Symbol	rest Condition			Т	D	Е	F	Unit
lo-	V <sub>D</sub> =12V	I - II -III	MAX	5	5	10	25	mA
I <sub>GT</sub>		IV		5	10	25	70	
V <sub>GT</sub>		ALL	MAX	1.3			V	
V <sub>GD</sub>	$V_D=V_{DRM}T_j=125$ °C RL=3.3KΩ	ALL	MIN	0.2			V	
I.	I <sub>G</sub> =1.2I <sub>GT</sub>	I -III	MAX	10	20	30	40	mA
l <sub>L</sub>		II - IV		15	35	45	60	
Ін	I <sub>T</sub> =100mA		MAX	5	15	25	30	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	20	50	100	150	V/µs
(dV/dt)c	(dl/dt)c=1.7A/ms T <sub>j</sub> =125℃		MIN	0.1	0.1	0.5	5	V/µs

## **STATIC CHARACTERISTICS**

Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>тм</sub> =5.5A tp=380µs	T <sub>j</sub> =25℃	1.6	V
I <sub>DRM</sub>	VD=VDRM VR=VRRM	T <sub>j</sub> =25℃	5	μA
I <sub>RRM</sub>		T <sub>j</sub> =125℃	0.5	mA

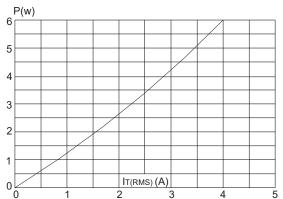


### **THERMAL RESISTANCES**

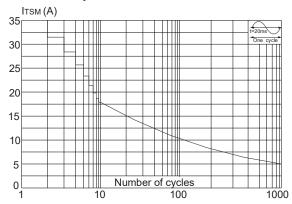
Symbol	Paramo	Value	Unit		
R <sub>th(j-c)</sub>	junction to case(AC)	TO-251	2.8		
		TO-220A(Ins)	3.5	l	
		TO-220A(Non-Ins)/ TO-220C	2.5	°C/W	
		TO-220F(Ins)	3.3	0700	
		TO-126/SOT-82	3.7		
		TO-202-3	3.9		



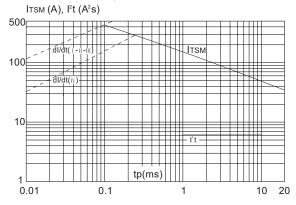
**FIG.1:** Maximum power dissipation versus RMS on-state current



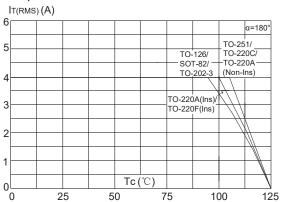
**FIG.3:** Surge peak on-state current versus number of cycles



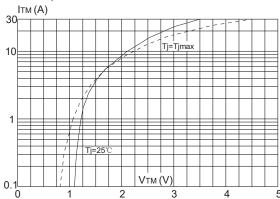
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms and corresponding value of  $I^2t$  ( I - II - III : dI/dt < 50A/ $\mu$ s; IV:dI/dt < 10A/ $\mu$ s)



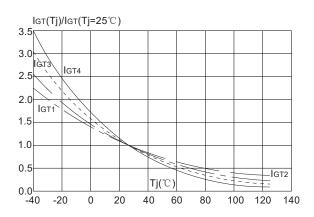
**FIG.2:** RMS on-state current versus case temperature



**FIG.4:** On-state characteristics (maximum values)

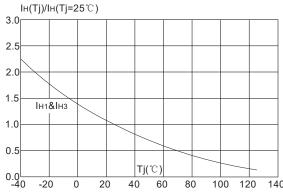


**FIG.6:** Relative variations of gate trigger current versus junction temperature





**FIG.7:** Relative variations of holding current versus junction temperature



**FIG.8:** Relative variations of latching current versus junction temperature

