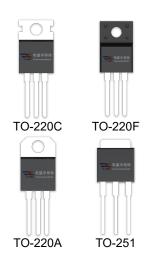
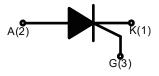


DESCRIPTION:

With high ability to withstand the shock loading of large current, TYN16X-800RT series of silicon controlled rectifiers provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.





MAIN FEATURES

Symbol	JCT616	JCT816	
V _{DRM} / V _{RRM}	600V	800V	
I _{T(RMS)}	16A		
lgt	≤15mA		

ABSOLUTE MAXIMUM RATINGS

Parame	Symbol	Value	Unit	
Storage junction temperature range		T _{stg}	-40-150	$^{\circ}\!\mathbb{C}$
Operating junction temperature range		Tj	-40-150	$^{\circ}\!\mathbb{C}$
Repetitive peak off-state voltage(T _j =25℃)		V _{DRM}	600/800	V
Repetitive peak reverse voltage(T _j =25℃)		V _{RRM}	600/800	V
RMS on-state current	TO-220A(Ins) / TO-220F(Ins) (Tc=100°C) TO-251/ TO-220C TO-220A(Non-Ins) (Tc=120°C)	I _{T(RMS)}	16	A



Non repetitive surge peak on-state current (tp=10ms)	I _{TSM}	180	А
I ² t value for fusing (tp=10ms)	l²t	162	A ² s
Critical rate of rise of on-state current $(I_G=2\times I_{GT})$	dl/dt	50	A/µs
Peak gate current	I _{GM}	4	Α
Average gate power dissipation	P _{G(AV)}	1	W
Peak gate power	Рсм	5	W

ELECTRICAL CHARACTERISTICS (T_j=25°C unless otherwise specified)

Symbol	Took Condition	Value			I I m i f
	Test Condition	MIN.	TYP.	MAX.	Unit
Ідт	V _D =12V R _L =33Ω	-	-	15	mA
V _G T	VD-12V KL-3312	-	1	1.3	V
V _{GD}	$V_D=V_{DRM}T_j=150^{\circ}C$ RL=3.3K Ω	0.2	-	-	V
IL	I _G =1.2I _{GT}	-	-	60	mA
lΗ	I _T =500mA	-	-	50	mA
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =150 ℃	200	-	-	V/µs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V _{TM}	I _{TM} =32A tp=380μs	Tj=25℃	1.55	V
IDRM	V _D =V _{DRM} V _R =V _{RRM}	Tj=25℃	5	μA
I _{RRM}		T _j =150℃	2	mA

THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
R _{th(j-c)}	junction to case(AC)	TO-220A(Ins)/ TO-220F(Ins)	2.4	°C/W
		TO-220A(Non-Ins) TO-220C	1.1	
		TO-251	1.4	



FIG.1 Maximum power dissipation versus RMS on-state current

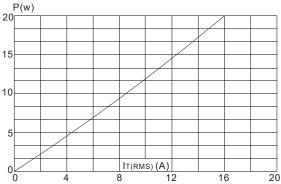


FIG.3: Surge peak on-state current versus number of cycles

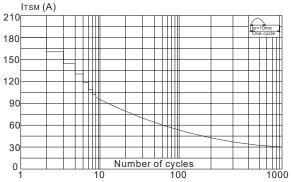


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I²t (dI/dt<50A/µs)

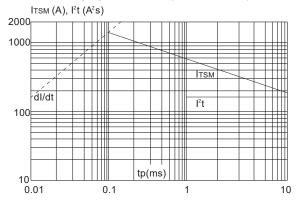


FIG.2: RMS on-state current versus case temperature

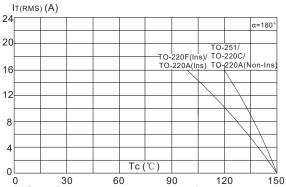


FIG.4: On-state characteristics (maximum values)

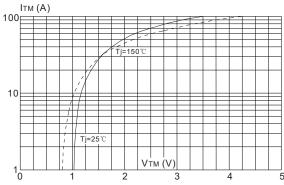


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

