Shenzhen VSEEI Semiconductor Co., Ltd

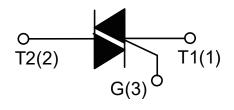
DESCRIPTION:

With high ability to withstand the shock loading of large current, T1235-600G series triacs provide high dv/dt rate with strong resistance to electromagnetic interface. With high commutation performances, 3 quadrants products especially recommended for use on inductive load.



MAIN FEATURES

Symbol	Value	Unit
I _{T(RMS)}	12	А
V _{DRM} /V _{RRM}	600/800/1200	V



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit	
Storage junction temperature ra	T _{stg}	-40-150	$^{\circ}$ C	
Operating junction temperature	Tj	-40-125	$^{\circ}$ C	
Repetitive peak off-state voltage	e (T _j =25℃)	V_{DRM}	600/800/1200	V
Repetitive peak reverse voltage	e (T _j =25℃)	V _{RRM}	600/800/1200	V
Non repetitive surge peak Off-s	V _{DSM}	V _{DRM} +100	V	
Non repetitive peak reverse vo	V _{RSM}	V _{RRM} +100	V	
RMS on-state current	TO-263 (Tc=100°C)	I _{T(RMS)}	12	Α
Non repetitive surge peak on-state current (full cycle, F=50Hz)		Ітѕм	120	А
I ² t value for fusing (tp=10ms)		l ² t	78	A ² s
Critical rate of rise of on-state current ($I_G = 2 \times I_{GT}$)		dl/dt	50	A/µs
Peak gate current	I _{GM}	4	Α	
Average gate power dissipation	P _{G(AV)}	1	W	
Peak gate power	P _{GM}	5	W	

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ELECTRICAL CHARACTERISTICS (T_j =25 $^{\circ}$ C unless otherwise specified)

3 Quadrants

Symbol	Test Condition	Quadrant		Value			Unit	
Symbol	rest Condition			BW	CW	sw	TW	Offic
Ідт	V _D =12V R _L =33Ω	I - II -III	MAX	50	35	10	5	mA
V _G T	VD - 12V RL -3312	I - II -III	MAX	1.3			V	
V _{GD}	$V_D = V_{DRM} T_j = 125^{\circ}C$ $R_L = 3.3 K\Omega$	I - II -III	MIN		0.2	2		V
IL IG=1.2IGT	1 4 01	I -III	MAX	80	50	30	20	m A
	IG = 1.2IGT	II	IVIAA	90	60	40	30	mA
Ін	I _T =100mA		MAX	60	40	20	15	mA
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =125℃		MIN	1000	500	200	100	V/µs
(dl/dt)c	Without snubber T _j =125℃		MIN	12	6.5	2.9	1	A/ms

4 Quadrants

Combal	Test Condition	Quadrant		Value		l losi4
Symbol				В	С	Unit
1		I - II -III	MAY	50	25	ъ Л
IGT	$V_D = 12V R_L = 33\Omega$	IV	MAX	70	50	mA
V _{GT}	ALL		MAX	1.3		V
V _{GD}	$V_D = V_{DRM} T_j = 125^{\circ}C$ $R_L = 3.3 K\Omega$	ALL	MIN	0.2		V
IL	I _G =1.2I _{GT}	I -III-IV	MAV	50	40	m A
		II	MAX	100	80	mA
Ін	I _T =100mA		MAX	50	25	mA
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =125℃		MIN	500	200	V/µs
(dV/dt)c	(dI/dt)c=5.3A/ms T _j =125℃		MIN	10	5	V/µs



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STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V _{TM}	I _{тм} =17A tp=380µs	Tj=25℃	1.5	V
IDRM	\/- =\/	Tj=25℃	5	μA
I _{RRM}	$V_D = V_{DRM} V_R = V_{RRM}$	Tj=125℃	1	mA

THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
R _{th(j-c)}	junction to case(AC)	TO 262	1.4	°C/W
R _{th(j-a)}	junction to ambient	TO-263	45	C/VV



FIG.1 Maximum power dissipation versus RMS on-state current

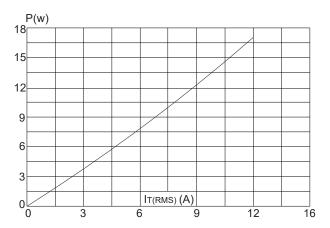


FIG.3: Surge peak on-state current versus number of cycles

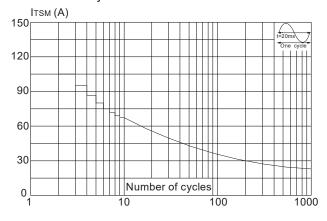


FIG.2: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness: 35µm) (full cycle)

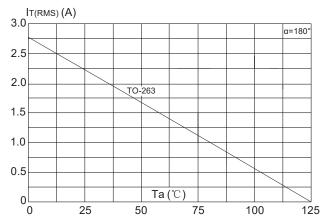


FIG.4: On-state characteristics (maximum values)

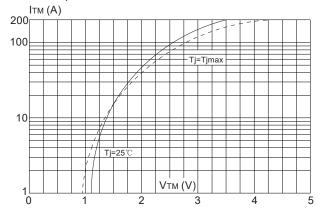




FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of I^2t (dI/dt(I-I-II) < 50A/ μ s)

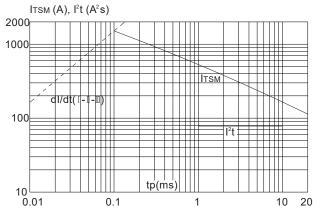
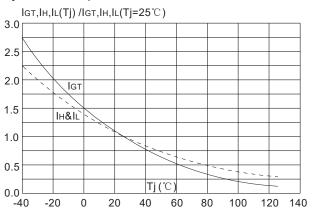


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



SOLDERING PARAMETERS

Reflow Condition		Pb-Free assembly (see figure at right)	
	-Temperature Min (T _{s(min)})	+150℃	
Pre Heat	-Temperature Max(T _{s(max)})	+200℃	
liout	-Time (Min to Max) (ts)	60-180 secs.	
	ramp up rate Temp (T _L)to peak)	3℃/sec. Max	
T _{s(max)} to	T∟ - Ramp-up Rate	3℃/sec. Max	
Reflow	-Temperature(T _L) (Liquidus)	+217℃	
	-Temperature(t _L)	60-150 secs.	
Peak Ten	np (T _p)	+260(+0/-5)°C	
Time within 5°C of actual Peak Temp (tp)		20-40secs.	
Ramp-down Rate		6℃/sec. Max	
Time 25℃ to Peak Temp (T _P)		8 min. Max	
Do not ex	ceed	+260℃	

