

DESCRIPTION:

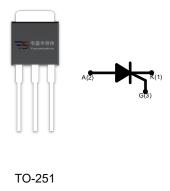
With high ability to withstand the shock loading of large current, TYN810 series of silicon controlled rectifiers provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.



TO-220F TO-220A

MAIN FEATURES

Symbol	Value	Symbol
V _{DRM} / V _{RRM}	600/800	V
I _{T(RMS)}	10	Α
lgт	≤10	mA



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit		
Storage junction temperature range		T _{stg}	-40-150	$^{\circ}$		
Operating junction temperature range		Operating junction temperature range		Tj	-40-150	$^{\circ}$
Repetitive peak off-state voltage(T _j =25℃)		V _{DRM}	600/800	V		
Repetitive peak reverse voltage(T _j =25℃)		V _{RRM}	600/800	V		
Non repetitive surge peak Off-state voltage		V _{DSM}	V _{DRM} +100	V		
Non repetitive peak reverse voltage		V _{RSM}	V _{RRM} +100	V		
RMS on-state current	TO-251 (T _C =120°C) TO-220A(Non-Ins) (T _C =125°C) TO-220A(Ins) / TO-220F(Ins) (T _C =110°C)	I _{T(RMS)}	10	А		



Non repetitive surge peak on-state current (tp=10ms)	I _{TSM}	120	А
I ² t value for fusing (tp=10ms)	l ² t	72	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT})	dl/dt	50	A/µs
Peak gate current	l _{GM}	4	Α
Average gate power dissipation	P _{G(AV)}	1	W
Peak gate power	P _{GM}	5	W

ELECTRICAL CHARACTERISTICS (T_j =25 $^{\circ}$ C unless otherwise specified)

Symbol	Test Condition	Value			Hoit
	rest Condition	MIN.	TYP.	MAX.	Unit
I _{GT}	V -40V D -220	-	-	10	mA
V _{GT}	$V_D=12V R_L=33\Omega$	-	-	1.5	V
V _{GD}	$V_D=V_{DRM}T_j=150^{\circ}C$ RL=3.3K Ω	0.2	-	-	V
IL	I _G =1.2I _{GT}	-	-	40	mA
lн	I _T =500mA	-	-	30	mA
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =150℃	200	-	-	V/µs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V _{TM}	I _{TM} =20A tp=380μs	T _j =25℃	1.55	V
I _{DRM}	V _D =V _{DRM} V _R =V _{RRM}	T _j =25℃	5	μA
I _{RRM}		T _j =150℃	1	mA



THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
R _{th(j-c)} j	junction to case(AC)	TO-220A(Ins)	2.5	°
		TO-220F(Ins)	2.8	
		TO-220A(Non-Ins) 1.	4	°C/W
		TO-251	2.0	



FIG.1 Maximum power dissipation versus RMS on-state current

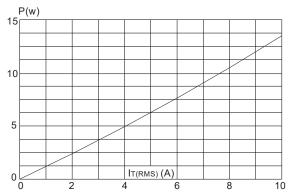


FIG.3: Surge peak on-state current versus number of cycles

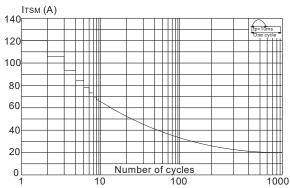


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I^2t (dI/dt < 50A/ μ s)

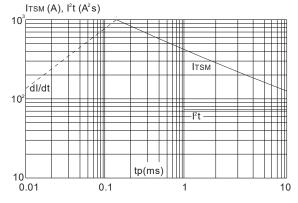


FIG.2: RMS on-state current versus case temperature

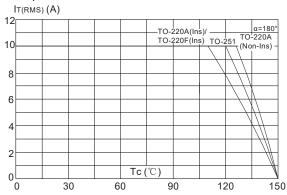


FIG.4: On-state characteristics (maximum values)

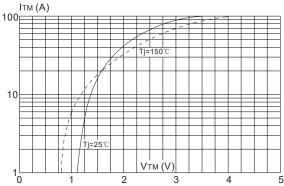


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

