Description

Features

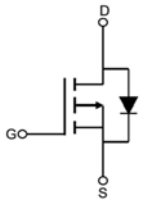
- V_{DS}=-20V, I_D=-2.5A
  $R_{DS(ON)}$ <120mΩ @ V_{GS} = -4.5V
  $R_{DS(ON)}$ <160mΩ @ V_{GS} = -2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

Application

- PWM Applications
- Load Switch
- Power Management

100% UIS
100% ΔVds

SOT-23-3



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size		(PCS) Reel	Pe(PCS) rCarton
VSM2301B-S2	VSM2301B	TAPING	SOT-23-3	7inch		3000	180000

Absolute Maximum Ratings (T_A=25℃ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		-20	V
V_{GSS}	Gate-Source Voltage		±12	V
I_D	Continuous Drain Current	T_A = 25℃	-2.5	A
		T_A = 100℃	-1.6	
I_{DM}	Pulsed Drain Current note1		-10	A
P_D	Power Dissipation	T_A = 25℃	1.2	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		104	℃/W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	℃

Electrical Characteristics (T_C=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V,I_D= -250μA	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = -20V, V_{GS} = 0V,	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS} =0V, V_{GS} = ±12V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}= V_{GS}, I_D= -10μA	-0.4	-	-1.0	V
		V_{DS}= V_{GS}, I_D= -250μA	-0.4	-0.7	-1.0	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note2	V_{GS} =-4.5V, I_D =-2.5A	-	90	120	mΩ
		V_{GS} =-2.5V, I_D =-1.5A	-	110	160	
g_{FS}	Forward Transconductance	V_{DS} =-5V, I_D = -2A	4	-	-	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = -10V, V_{GS} = 0V, f = 1.0MHz	-	285	-	pF
C_{oss}	Output Capacitance		-	58	-	pF
C_{rss}	Reverse Transfer Capacitance		-	32	-	pF
Q_g	Total Gate Charge	V_{DS} = -10V, I_D = -2.5A, V_{GS} = -4.5V	-	2.9	-	nC
Q_{gs}	Gate-Source Charge		-	0.45	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	0.75	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD} = -10V, R_L=5Ω, R_{GEN}=3Ω,V_{GS}=-4.5V,	-	9.8	-	ns
t_r	Turn-on Rise Time		-	4.9	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	20.5	-	ns
t_f	Turn-off Fall Time		-	7	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-2.5	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-10	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = -2.5A	-	-	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

Figure1: Output Characteristics

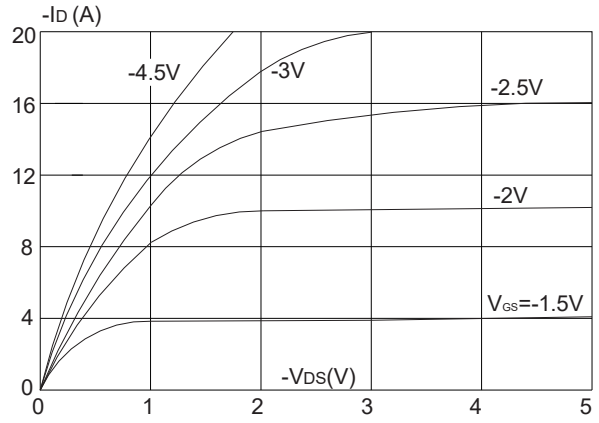


Figure 2: Typical Transfer Characteristics

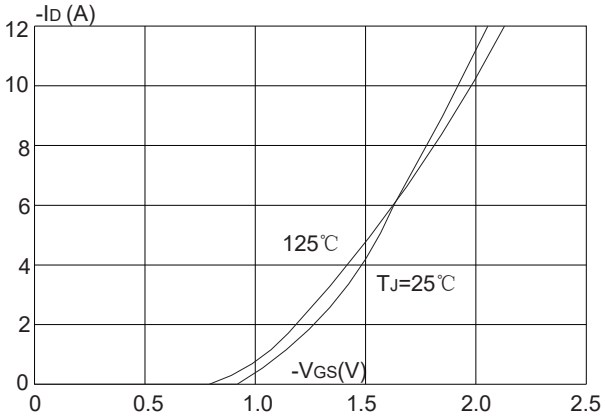


Figure 3: On-resistance vs. Drain Current

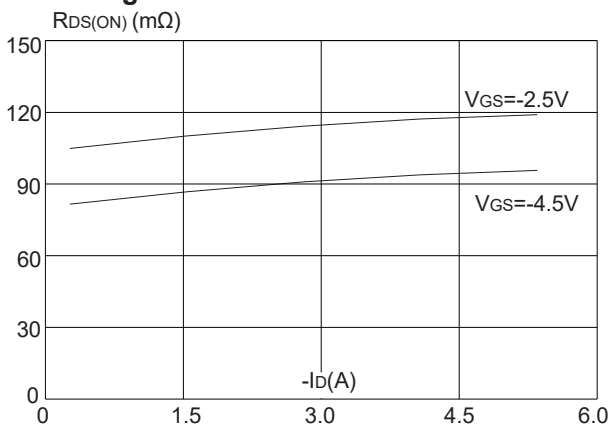


Figure 4: Body Diode Characteristics

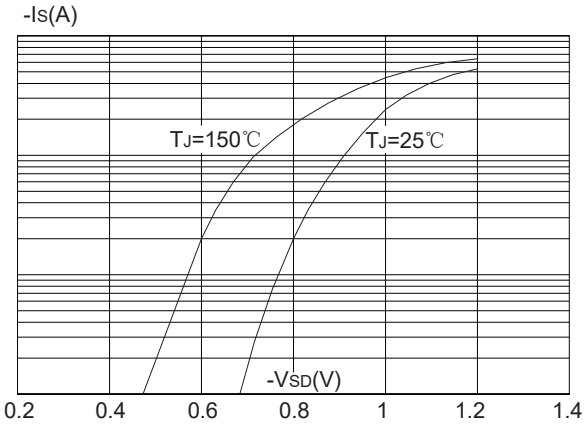


Figure 5: Gate Charge Characteristics

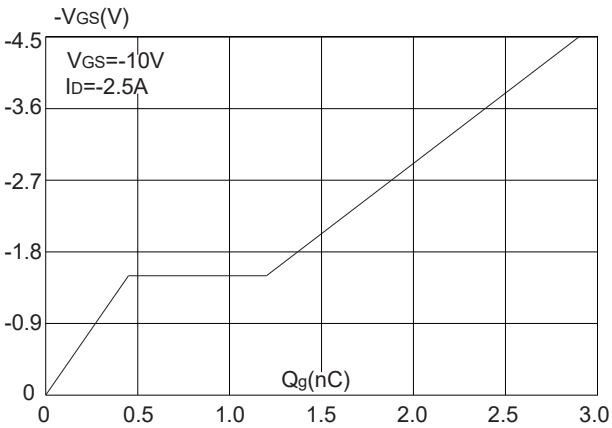


Figure 6: Capacitance Characteristics

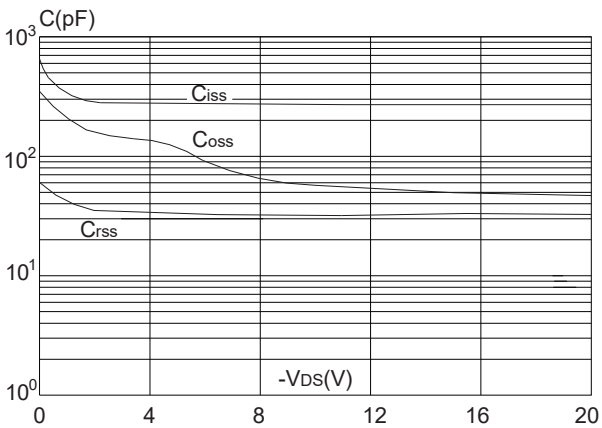


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

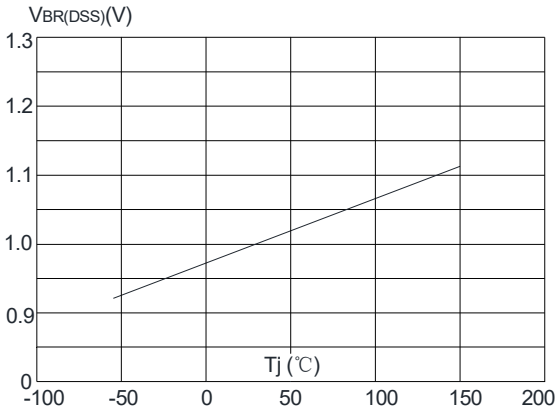


Figure 8: Normalized on Resistance vs. Junction Temperature

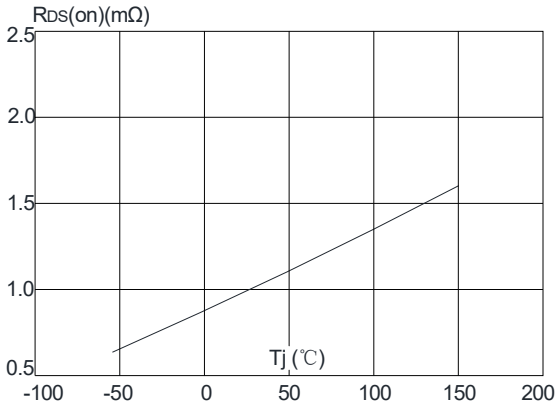


Figure 9: Maximum Safe Operating Area

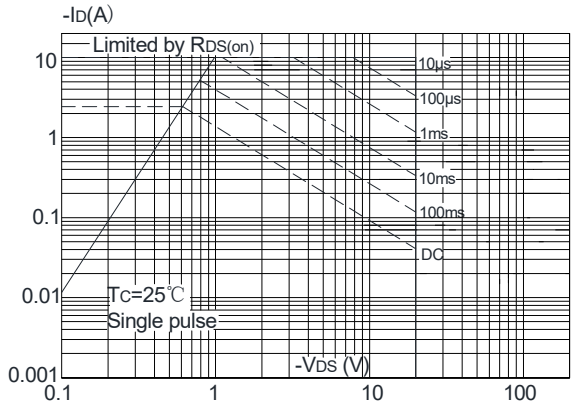


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

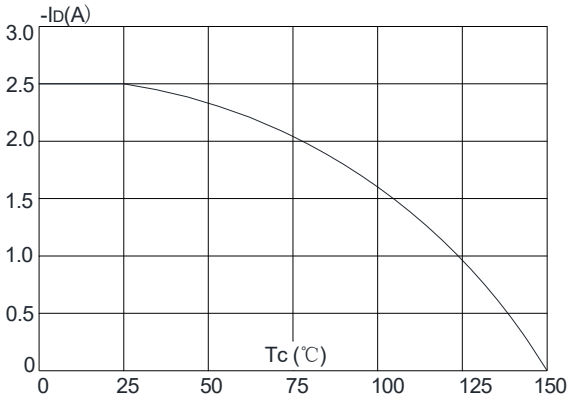
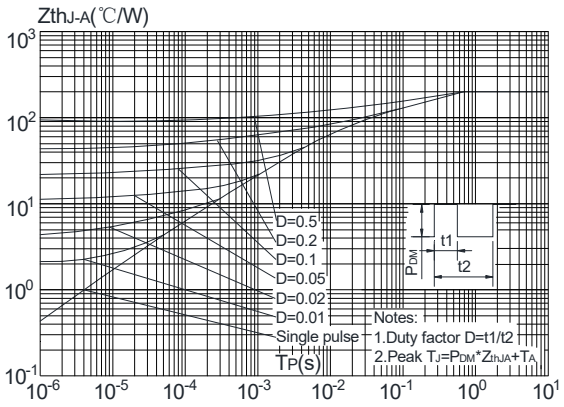
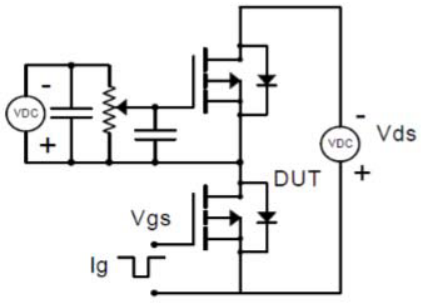
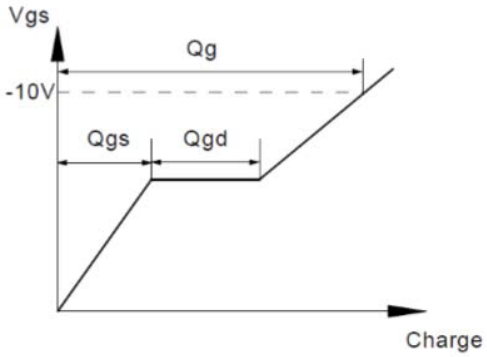


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

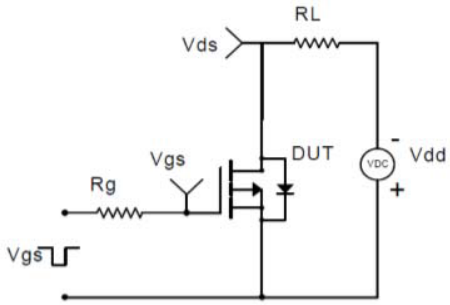


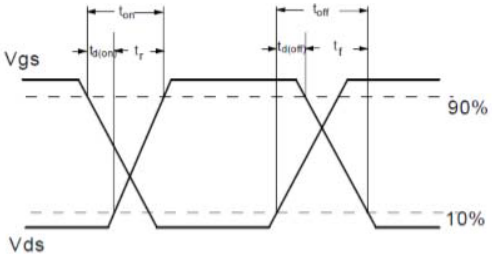
Gate Charge Test Circuit & Waveform



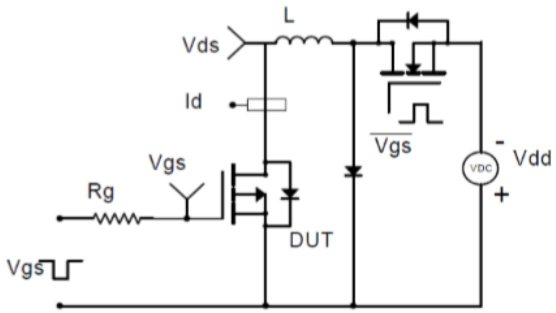


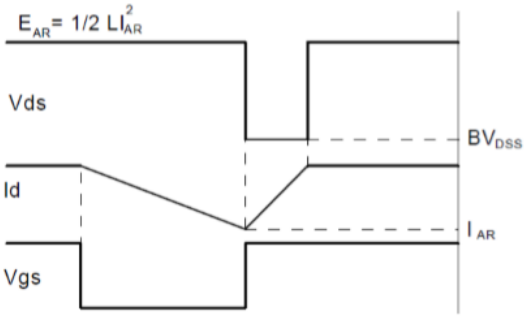
Resistive Switching Test Circuit & Waveforms





Unclamped Inductive Switching (UIS) Test Circuit & Waveforms





Diode Recovery Test Circuit & Waveforms