

## Description

### Features

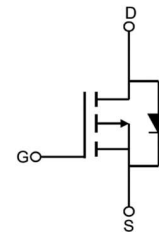
- $V_{DS} = -50V$ ,  $I_D = -0.13A$   
 $R_{DS(ON)} < 3.6\Omega$  @  $V_{GS} = -10V$   
 $R_{DS(ON)} < 5.4\Omega$  @  $V_{GS} = -4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

### Application

- PWM Applications
- Load Switch
- Power Management



SOT-23-3



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
VSM84A-S2	VSM84A	TAPING	SOT-23-3	7inch	3000	180000

## Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

Symbol	Parameter		Max.	Units
V <sub>DSS</sub>	Drain-Source Voltage		-50	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> = 25°C	-0.13	A
		T <sub>A</sub> = 100°C	-0.08	A
I <sub>DM</sub>	Pulsed Drain Current <sup>note1</sup>		-0.52	A
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> = 25°C	0.225	W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient		556	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C

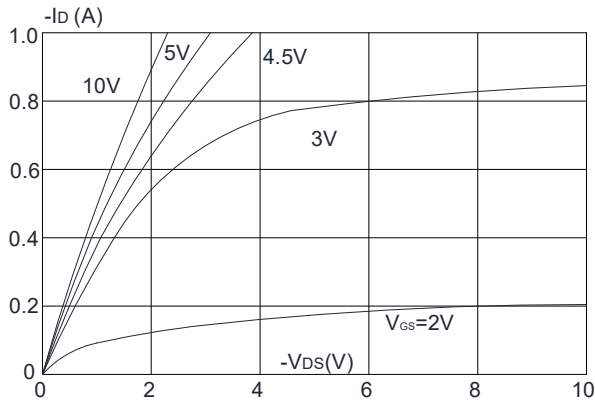
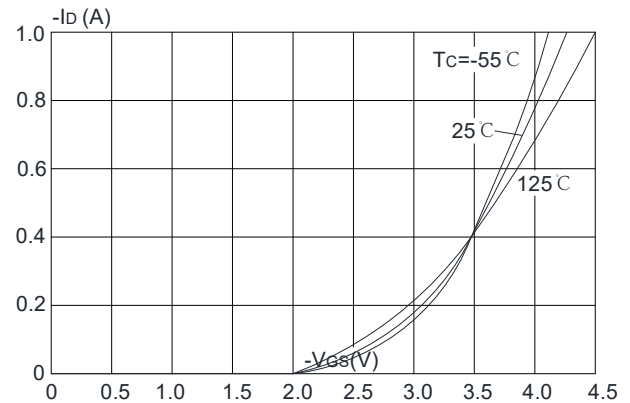
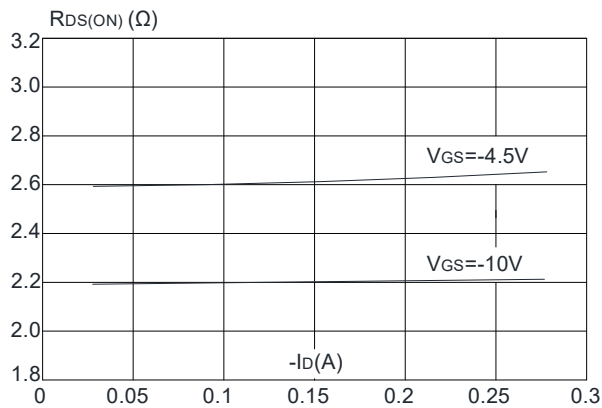
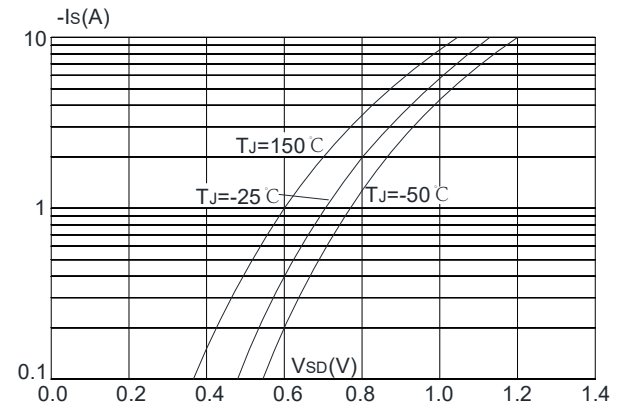
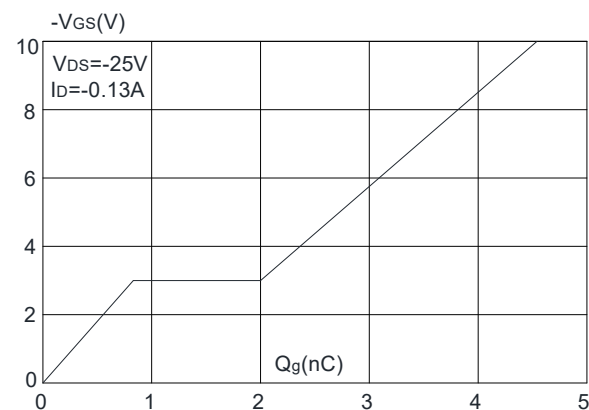
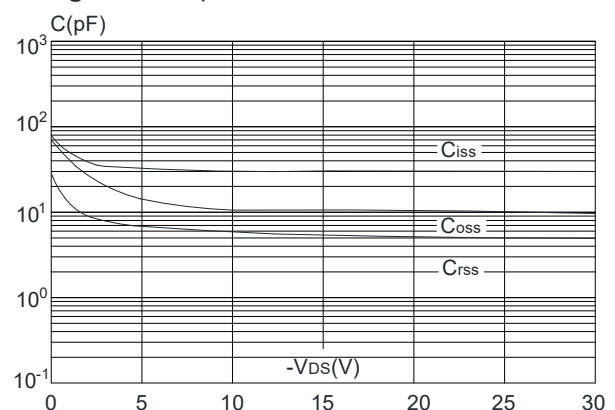
**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> = -250μA	-50	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -50V, V <sub>GS</sub> =0V,	-	-	-1	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.8	-1.5	-2.5	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note2</small>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.13A	-	2.2	3.6	Ω
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -0.1A	-	2.6	5.4	
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = -25V, V <sub>GS</sub> =0V, f=1.0MHz	-	30	-	pF
C <sub>oss</sub>	Output Capacitance		-	10	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	5	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = -25V, I <sub>D</sub> = -0.13A, V <sub>GS</sub> = -10V	-	4.5	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	0.8	-	nC
Q <sub>gd</sub>	Gate-Drain(“Miller”) Charge		-	1.2	-	nC
Switching Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> = -25V, I <sub>D</sub> = -0.1A, V <sub>GS</sub> = -10V, R <sub>GEN</sub> =2.5Ω	-	2.5	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	1	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	16	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	8	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	-0.13	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-0.52	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> = -0.13A	-	-0.8	-1.2	V

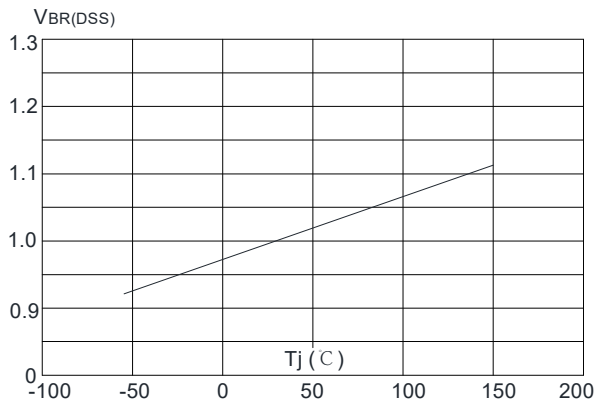
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$

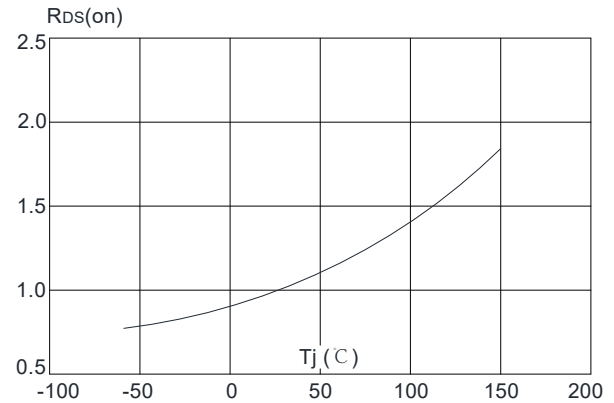
## Typical Performance Characteristics

**Figure1: Output Characteristics**

**Figure 2: Typical Transfer Characteristics**

**Figure 3: On-resistance vs. Drain Current**

**Figure 4: Body Diode Characteristics**

**Figure 5: Gate Charge Characteristics**

**Figure 6: Capacitance Characteristics**


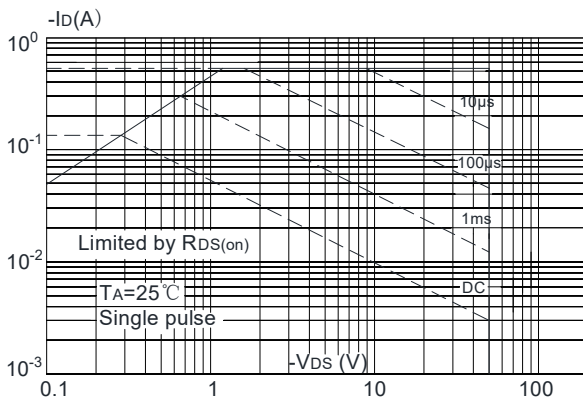
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



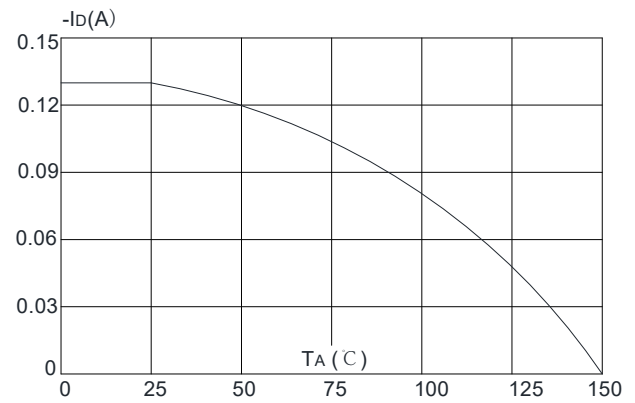
**Figure 8:** Normalized on Resistance vs. Junction Temperature



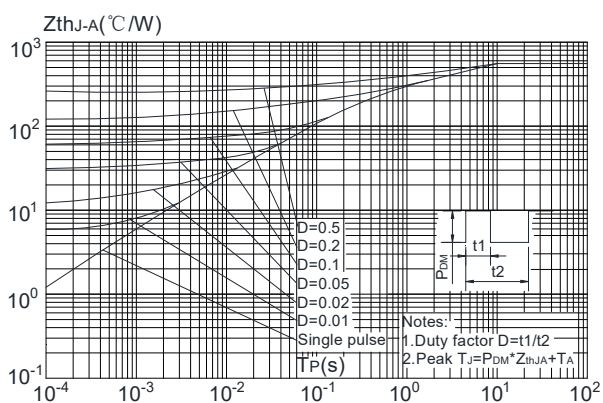
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature

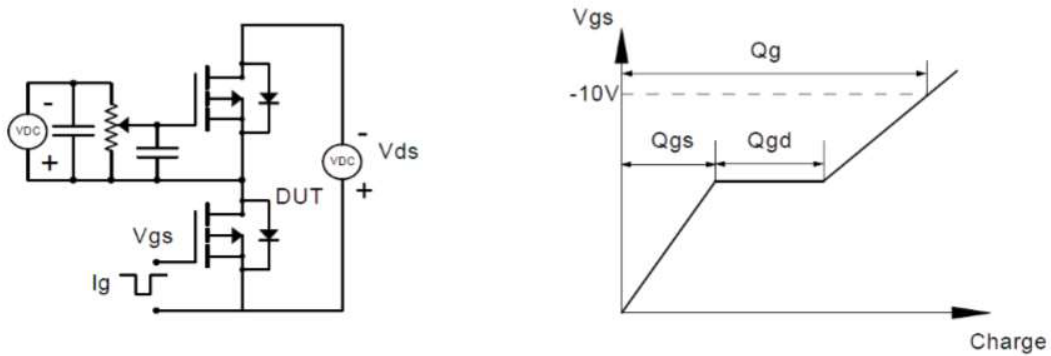


**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

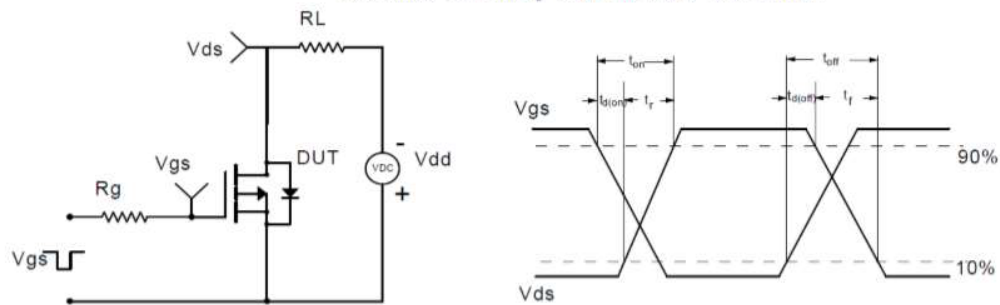


## Test Circuit

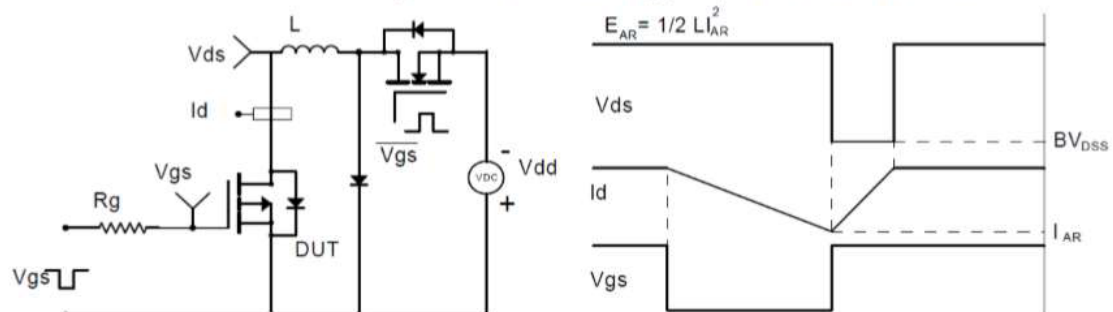
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

