
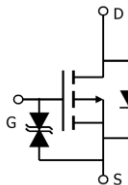


## Description

<b>Features</b> <ul style="list-style-type: none"> <li>● <math>V_{DS} = -20V</math>, <math>I_D = -4A</math>  <math>R_{DS(ON)} &lt; 48m\Omega</math> @ <math>V_{GS} = -4.5V</math>  <math>R_{DS(ON)} &lt; 65m\Omega</math> @ <math>V_{GS} = -2.5V</math></li> <li>● High Power and Current Handling Capability</li> <li>● Lead Free Product is Acquired</li> <li>● Surface Mount Package</li> <li>● ESD Rating: HBM 2.0KV</li> </ul>	<b>Application</b> <ul style="list-style-type: none"> <li>● PWM Applications</li> <li>● Load Switch</li> <li>● Power Management</li> </ul> <p>100% UIS 100% <math>\Delta V_{ds}</math></p>
 <p>SOT-23-3</p>	 <p>Schematic Diagram</p>

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	(PCS)Reel	Per(PCS)Carton
VSM3415KAEC-S2	VSM3415KAEC	TAPING	SOT-23-3	7inch	3000	180000

## Absolute Maximum Ratings ( $T_A=25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Max.	Units
$V_{DSS}$	Drain-Source Voltage	-20	V
$V_{GSS}$	Gate-Source Voltage	$\pm 8$	V
$I_D$	Continuous Drain Current	$T_C = 25^{\circ}C$	A
		$T_C = 100^{\circ}C$	
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	-24	A
$P_D$	Power Dissipation	1.67	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	75	$^{\circ}C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^{\circ}C$

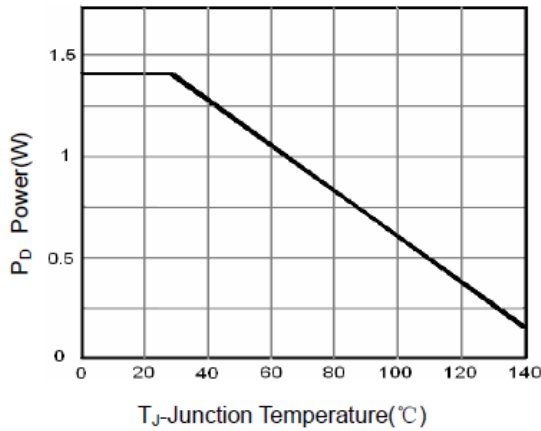
**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> = -250μA	-20	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V,	-	-	-1	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±8V	-	-	±10	μA
On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.4	-	-1.0	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note2</small>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A	-	38	48	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3A	-	48	65	
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	950	-	pF
C <sub>oss</sub>	Output Capacitance		-	165	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	120	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = -10V, I <sub>D</sub> = -4A, V <sub>GS</sub> = -4.5V	-	12	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	1.4	-	nC
Q <sub>gd</sub>	Gate-Drain(“Miller”) Charge		-	3.6	-	nC
Switching Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-10V, V <sub>GS</sub> =-4.5A, R <sub>L</sub> =2.5Ω,R <sub>GEN</sub> =3Ω	-	12	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	10	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	19	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	25	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	-4	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-16	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = -4A	-	-	-1.2	V

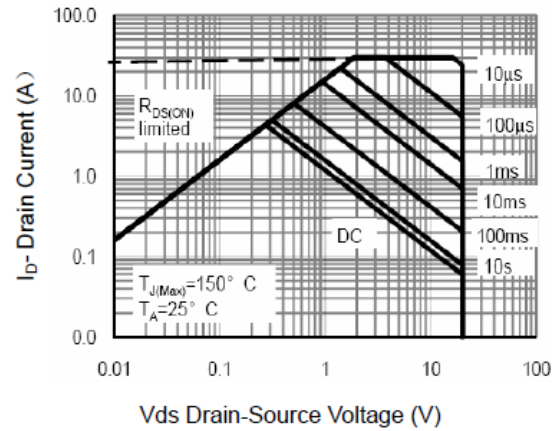
Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$

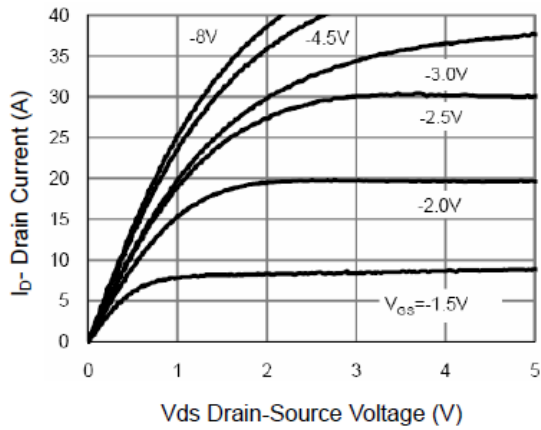
## Typical Performance Characteristics



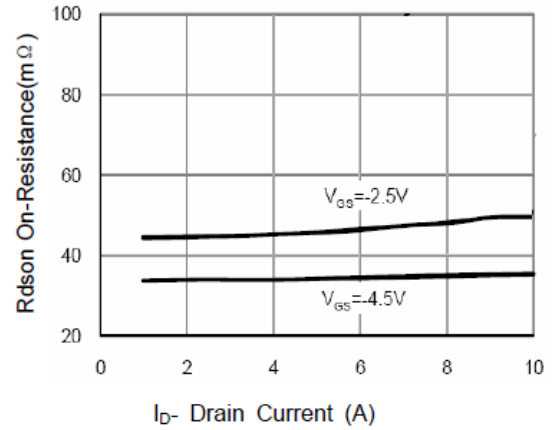
**Figure 3 Power Dissipation**



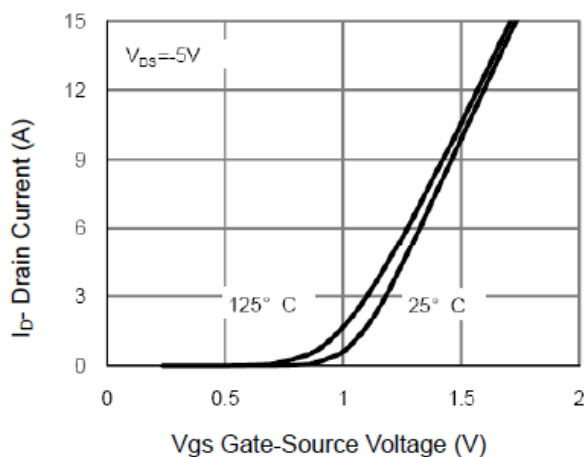
**Figure 4 Safe Operation Area**



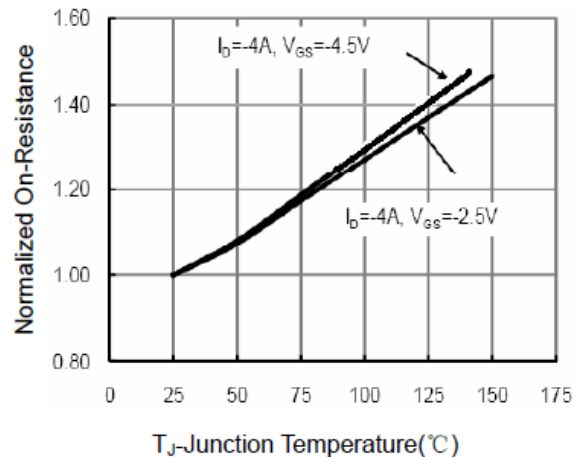
**Figure 5 Output Characteristics**



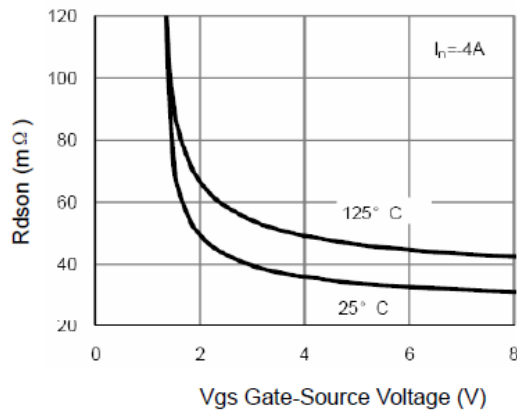
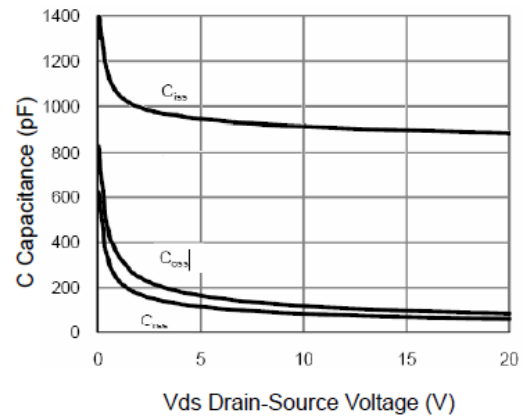
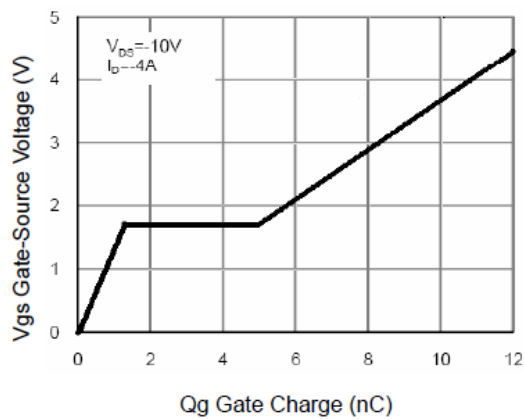
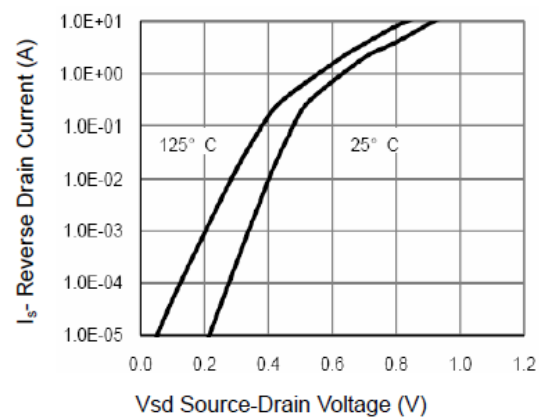
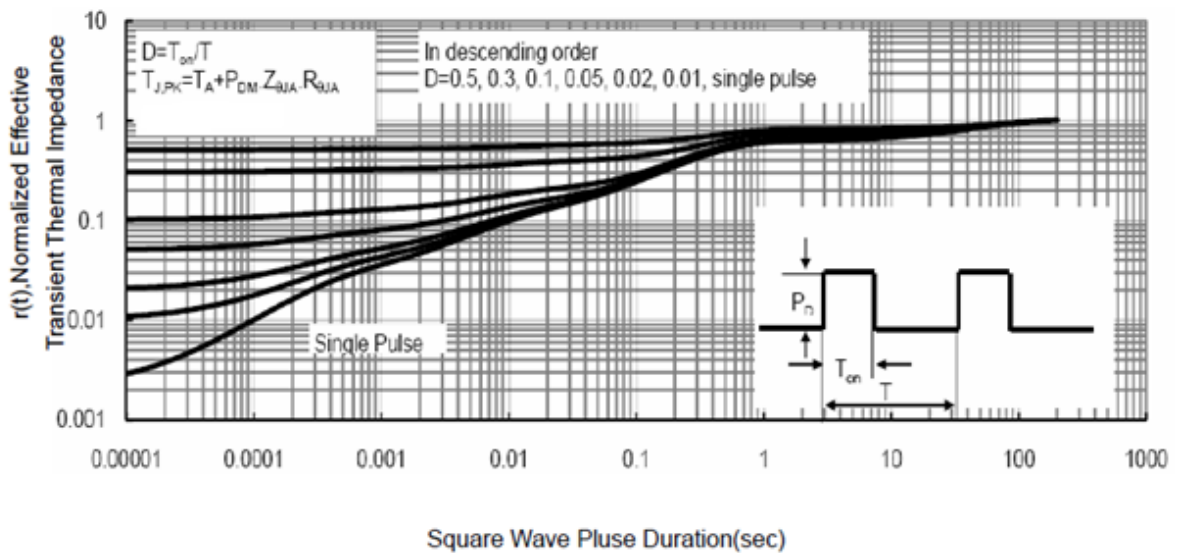
**Figure 6 Drain-Source On-Resistance**



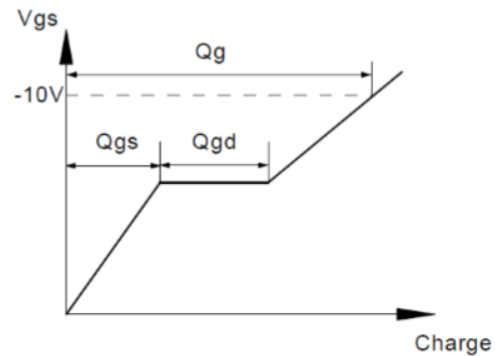
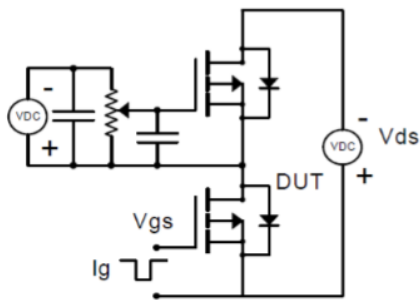
**Figure 7 Transfer Characteristics**



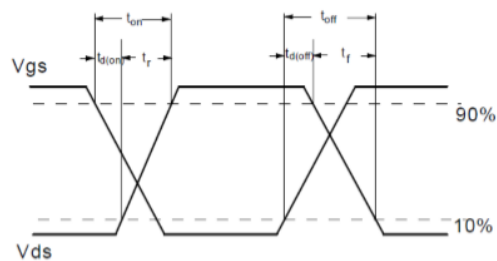
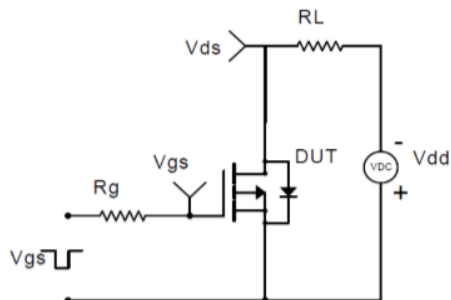
**Figure 8 Drain-Source On-Resistance**


**Figure 9 Rdson vs Vgs**

**Figure 10 Capacitance vs Vds**

**Figure 11 Gate Charge**

**Figure 12 Source-Drain Diode Forward**

**Figure 13 Normalized Maximum Transient Thermal Impedance**

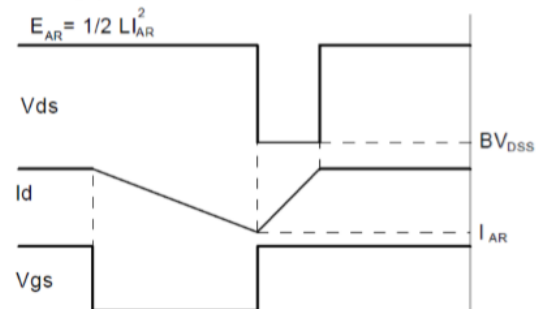
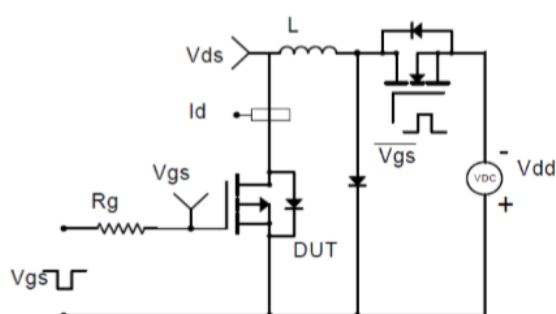
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms



### Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



### Diode Recovery Test Circuit & Waveforms

