

Description

Features

- 100V, 57A
- $R_{DS(ON)} = 21m\Omega$ (Typ.) @ $V_{GS} = 10V$, $I_D = 28.5A$
- Fast Switching
- Improved dv/dt Capability
- 100% Avalanche Tested

Application

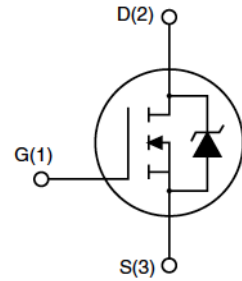
- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)



TO-220C



TO-263



Schematic Diagram

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Symbol	Parameter		Max.	Units
			TO-220C/TO-263	
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	V
I _D	Continuous Drain Current	T _C = 25°C	57	A
		T _C = 100°C	35	A
I _{DM}	Pulsed Drain Current ^{note1}		228	A
E _{AS}	Single Pulsed Avalanche Energy ^{note2}		605	mJ
P _D	Power Dissipation	T _C = 25°C	169	W
R _{θJC}	Thermal Resistance, Junction to Case		0.74	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient		62.5	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C

Electrical Characteristics ($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V,I _D =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100V, V _{GS} = 0V, T _J = 25℃	-	-	1	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =250μA	2	3	4	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =10V, I _D =28.5A	-	21	25	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	-	3140	-	pF
C _{oss}	Output Capacitance		-	360	-	pF
C _{rss}	Reverse Transfer Capacitance		-	140	-	pF
Q _g	Total Gate Charge	V _{DD} = 80V, I _D = 57A, V _{GS} = 10V	-	126	-	nC
Q _{gs}	Gate-Source Charge		-	10	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	36	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = 50V, I _D = 57A, R _G = 25Ω	-	35	-	ns
t _r	Turn-on Rise Time		-	60	-	ns
t _{d(off)}	Turn-off Delay Time		-	101	-	ns
t _f	Turn-off Fall Time		-	95	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _s	Maximum Continuous Drain to Source Diode Forward Current		-	-	57	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	228	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} =57A	-	-	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _s =57A,	-	250	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/μs	-	2	-	μC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. $V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 1\%$

Typical Performance Characteristics

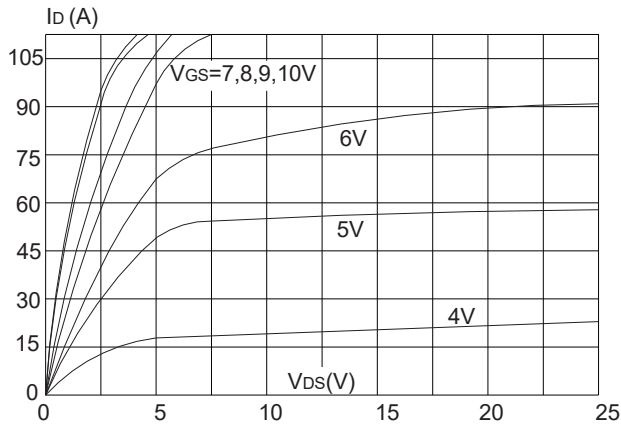
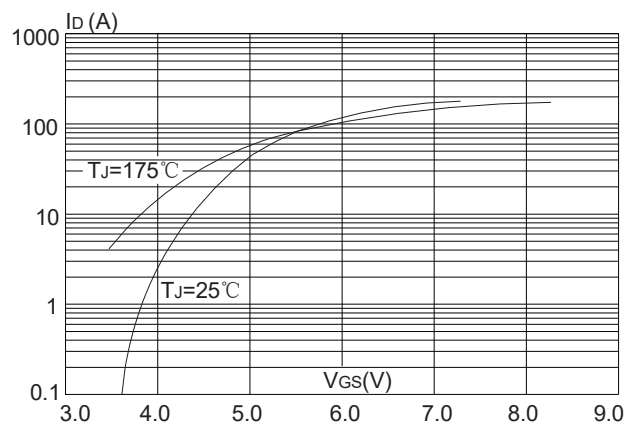
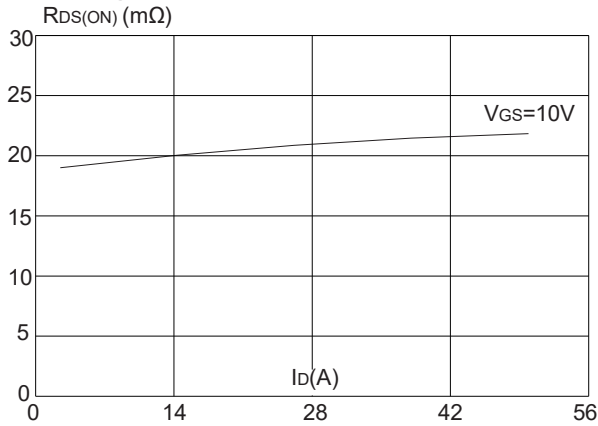
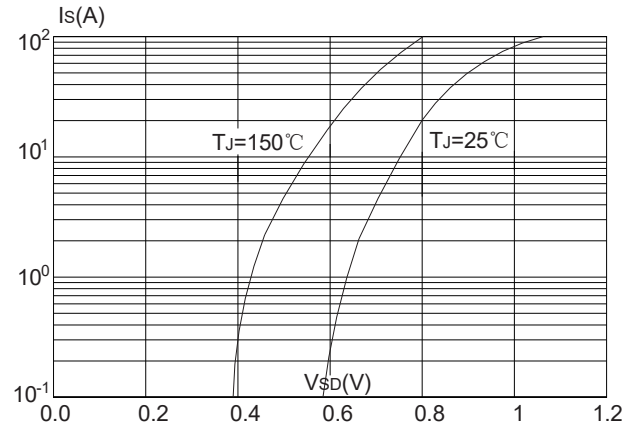
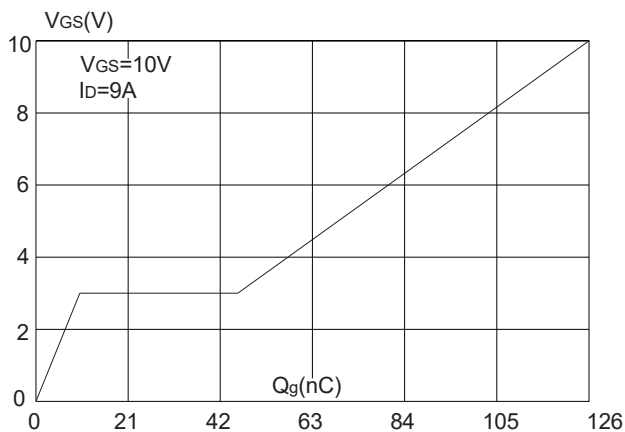
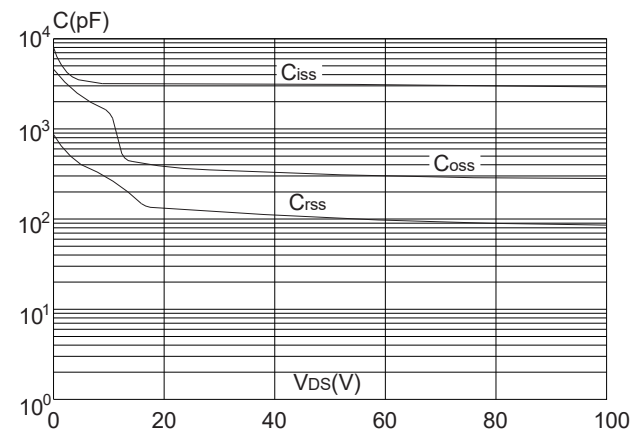
Figure1: Output Characteristics

Figure 2: Typical Transfer Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

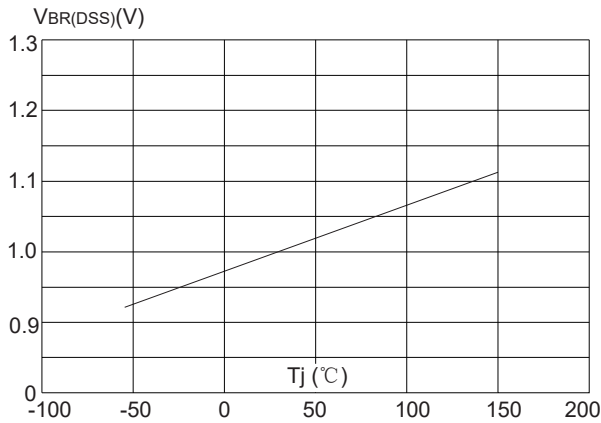


Figure 8: Normalized on Resistance vs. Junction Temperature

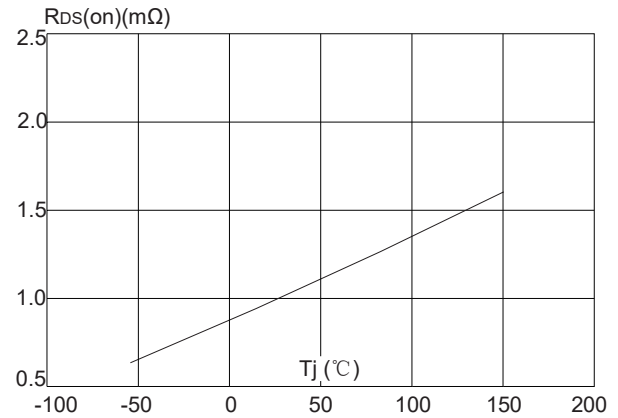


Figure 9: Maximum Safe Operating Area

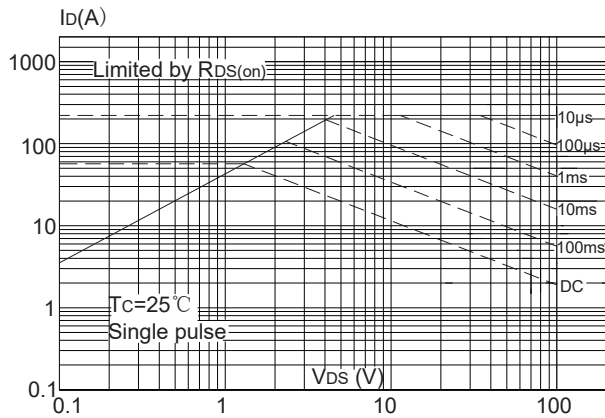


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

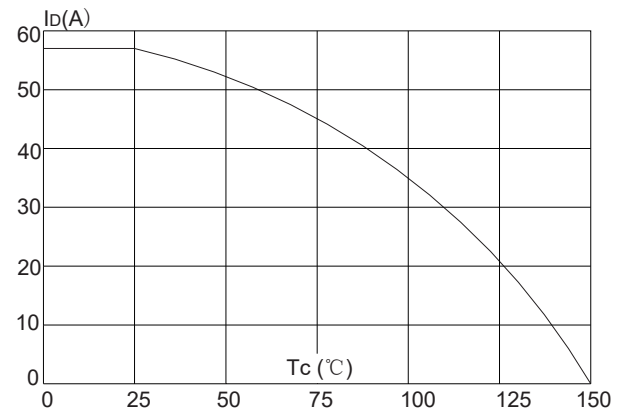
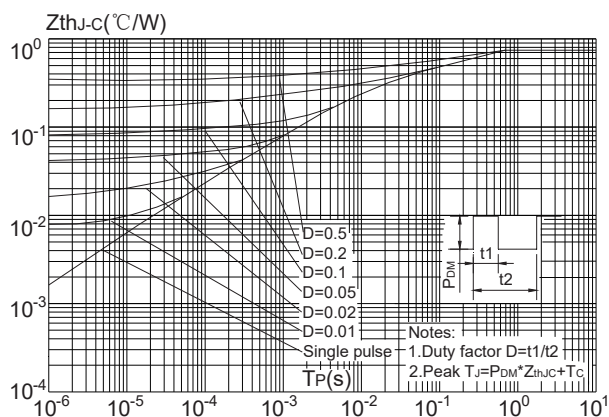


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-220C, TO-263)



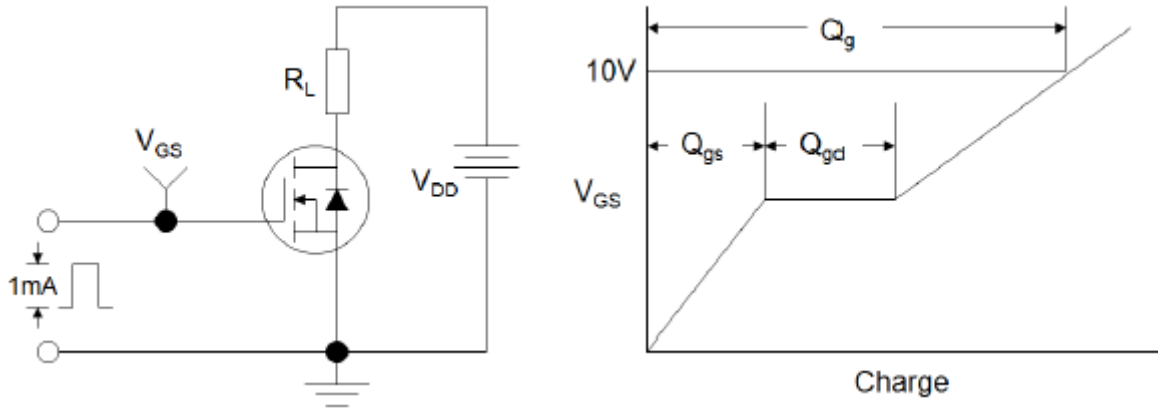


Figure1:Gate Charge Test Circuit & Waveform

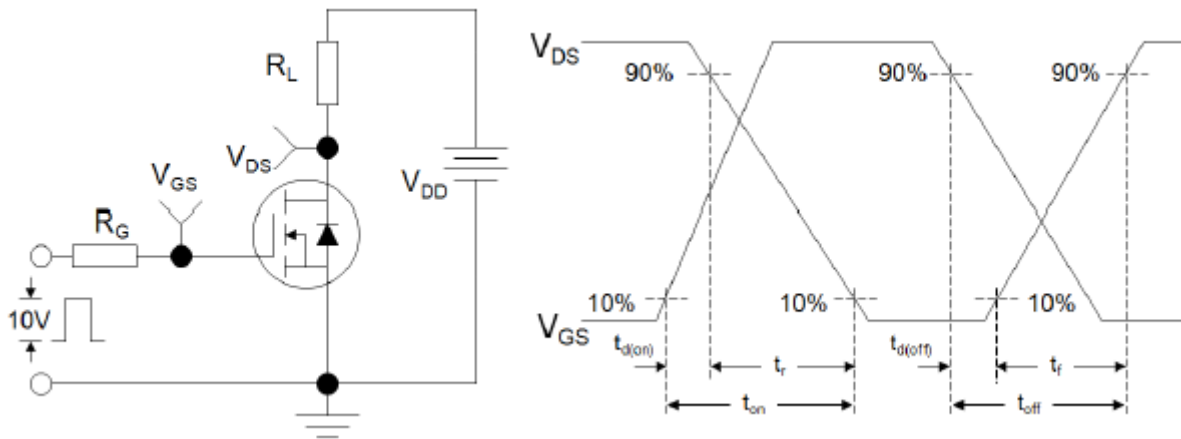


Figure 2: Resistive Switching Test Circuit & Waveforms

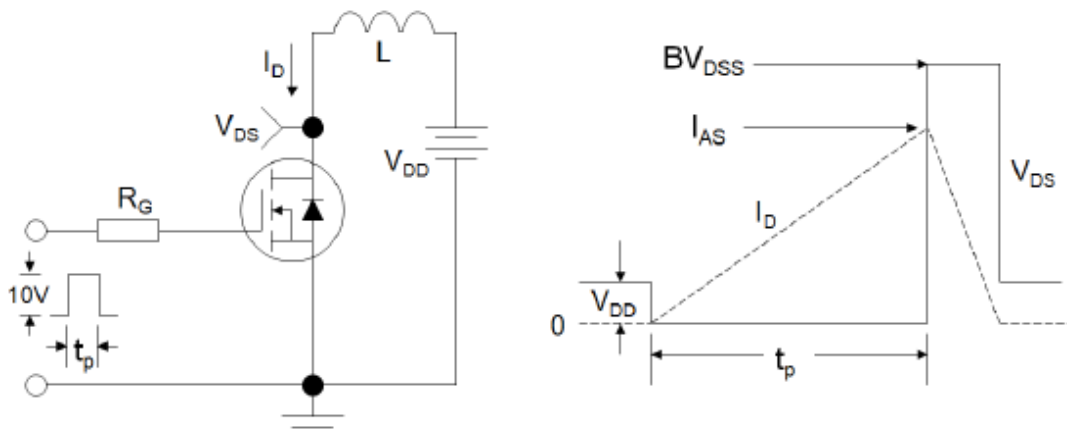


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

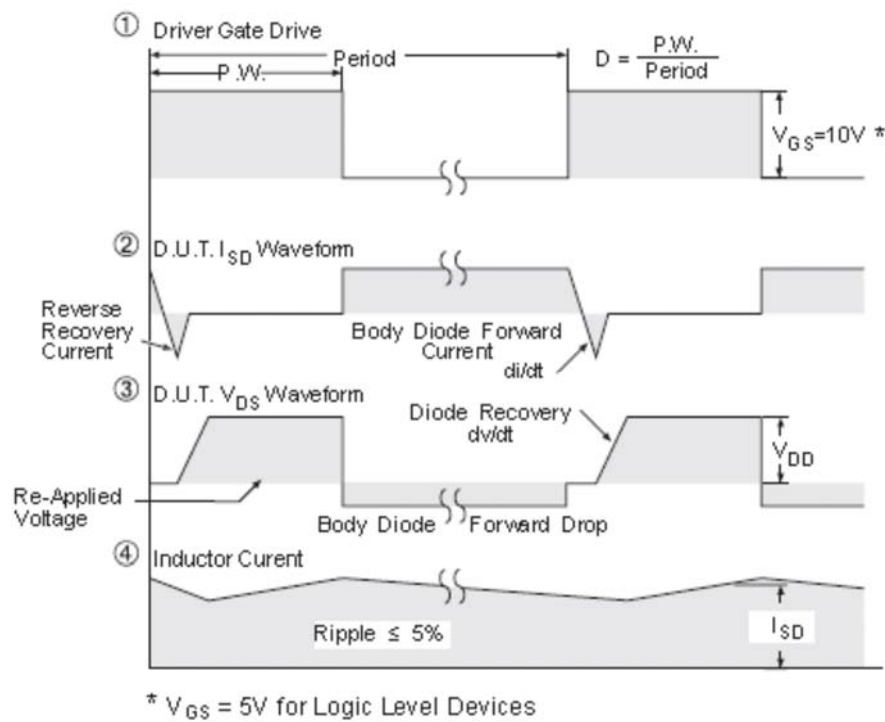
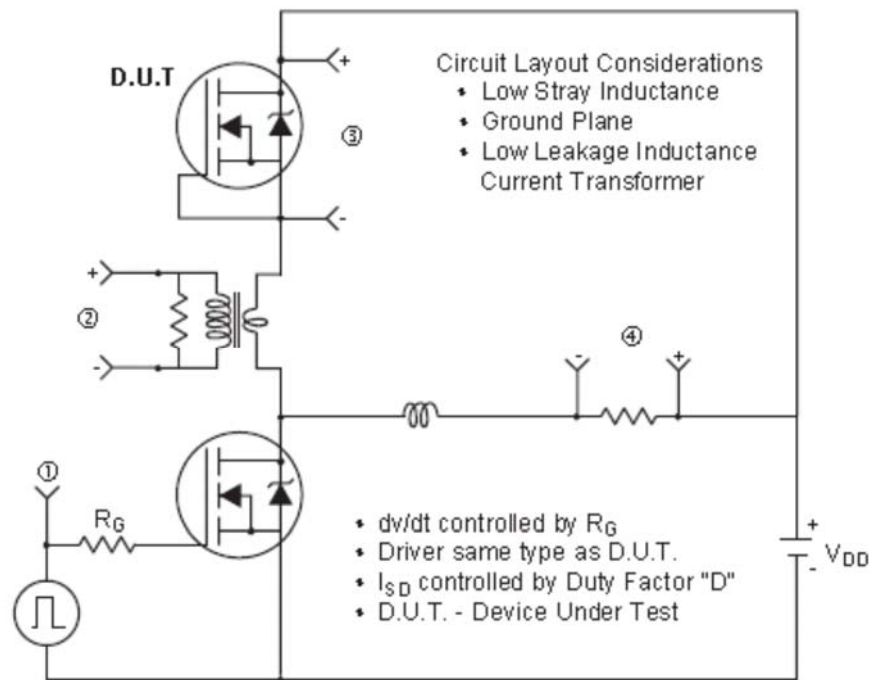


Figure 4: Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)