

## Description

### Features

- 60V, 50A
- $R_{DS(ON)} = 15m\Omega$  (Typ.) @  $V_{GS} = 10V$
- Fast Switching
- Improved dv/dt Capability
- 100% Avalanche Tested

### Application

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)



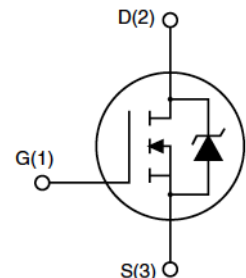
TO-220F



TO-263



TO-220C



Schematic Diagram

## Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.			Units
		TO-220C	TO-263	TO-220F	
$V_{DSS}$	Drain-Source Voltage	60			V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$			V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$			A
		$T_C = 100^\circ C$			A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	200			A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>	280			mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$			W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.87	0.87	1.5	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	62.5	62.5	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150			$^\circ C$

**Electrical Characteristics** ( $T_C=25^{\circ}\text{C}$  unless otherwise specified)

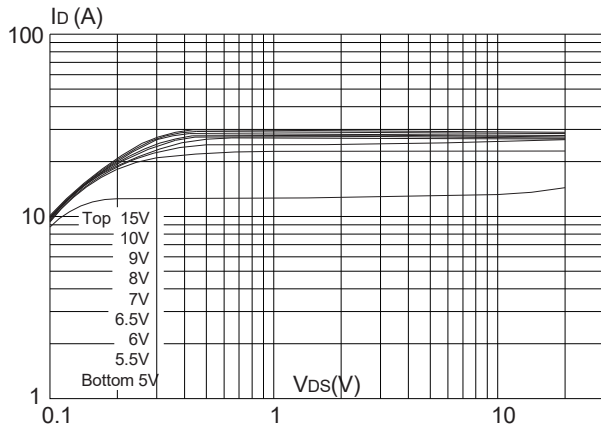
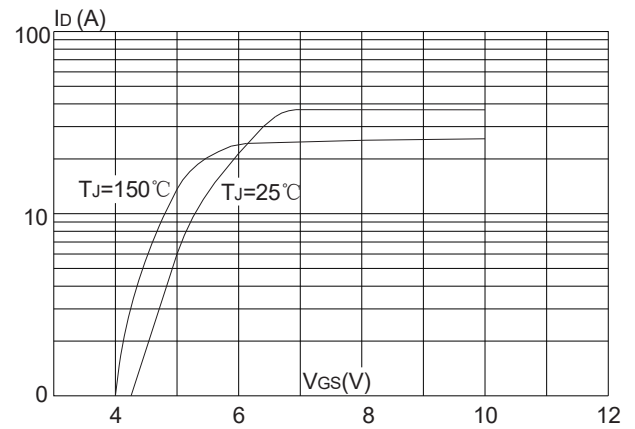
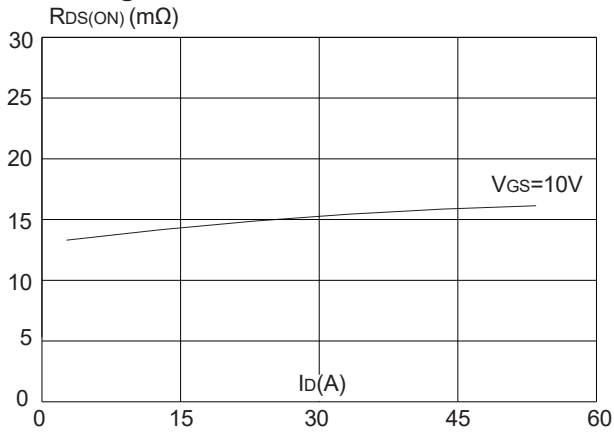
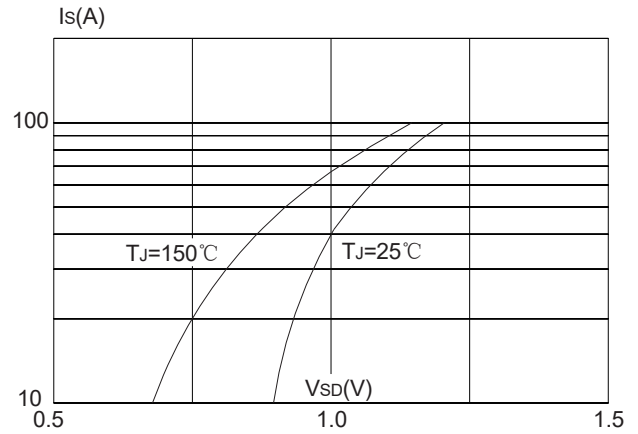
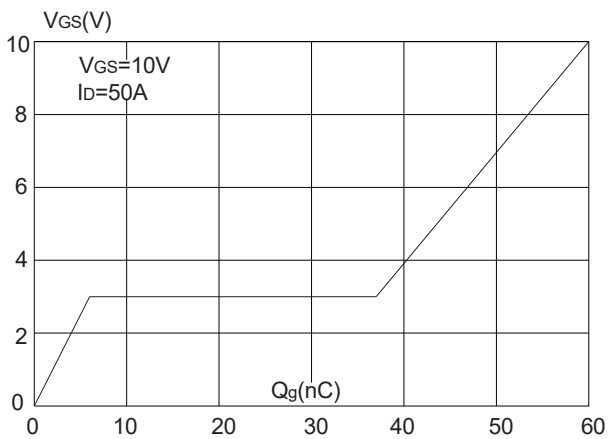
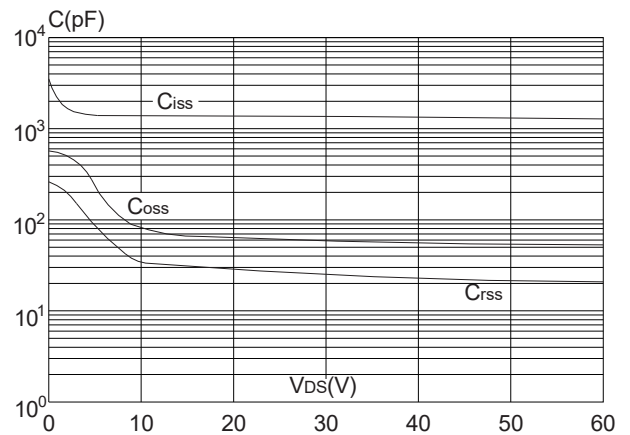
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> =250μA	60	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 25℃	-	-	5	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA	2	3	4	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance note3	V <sub>GS</sub> =10V, I <sub>D</sub> =25A	-	15	20	mΩ
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	1489	-	pF
C <sub>oss</sub>	Output Capacitance		-	608	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	275	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> = 48V, I <sub>D</sub> = 50A, V <sub>GS</sub> = 10V	-	60	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	6	-	nC
Q <sub>gd</sub>	Gate-Drain(“Miller”) Charge		-	31	-	nC
Switching Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> = 30V, I <sub>D</sub> = 50A, R <sub>G</sub> = 25Ω	-	22	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	82	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	52	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	93	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I <sub>s</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	50	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	200	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>SD</sub> =50A	-	-	2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>s</sub> =50A, di/dt=100A/μs	-	68	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	4.2	-	μC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

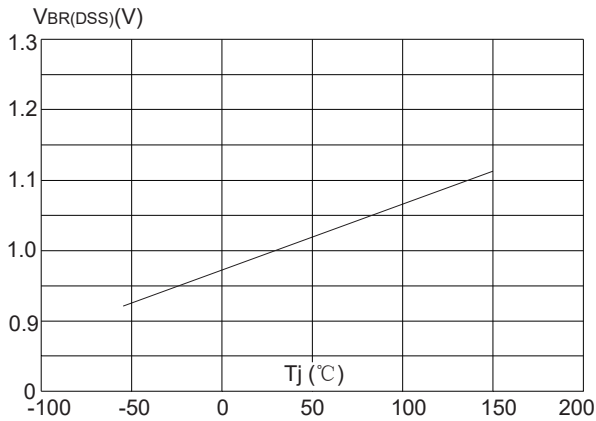
2.  $I_{AS} = 50A, R_G = 25\Omega$ , Starting  $T_J = 25^{\circ}\text{C}$ 

3. Pulse Test: Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 1\%$

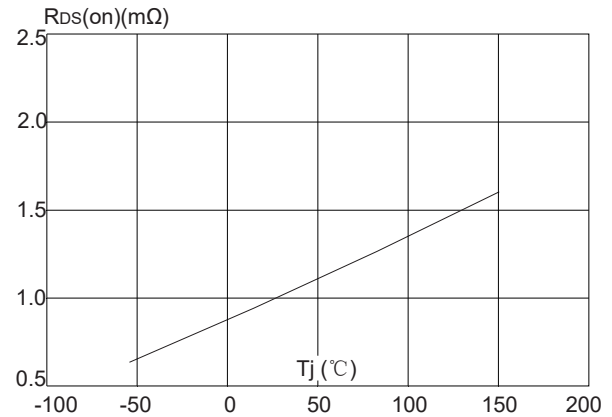
## Typical Performance Characteristics

**Figure1: Output Characteristics**

**Figure 2: Typical Transfer Characteristics**

**Figure 3: On-resistance vs. Drain Current**

**Figure 4: Body Diode Characteristics**

**Figure 5: Gate Charge Characteristics**

**Figure 6: Capacitance Characteristics**


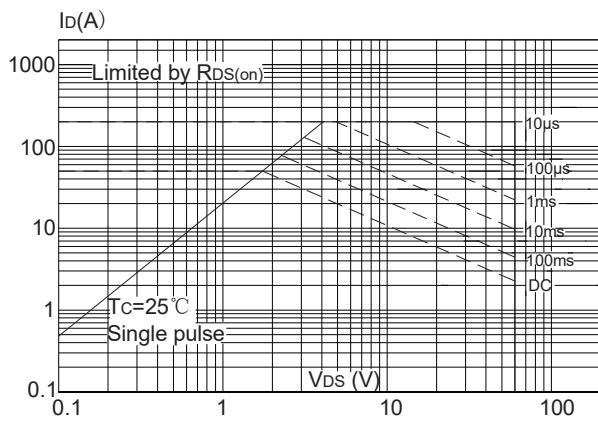
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



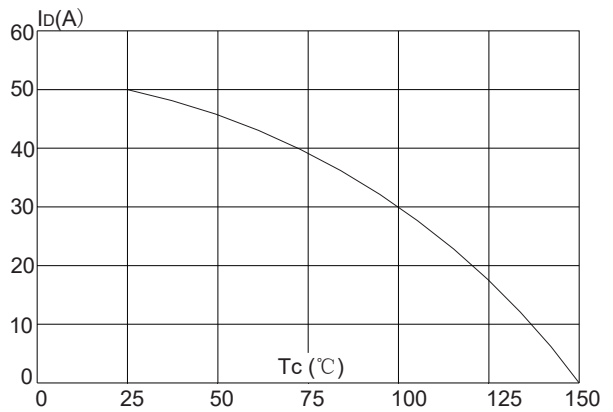
**Figure 8:** Normalized on Resistance vs. Junction Temperature



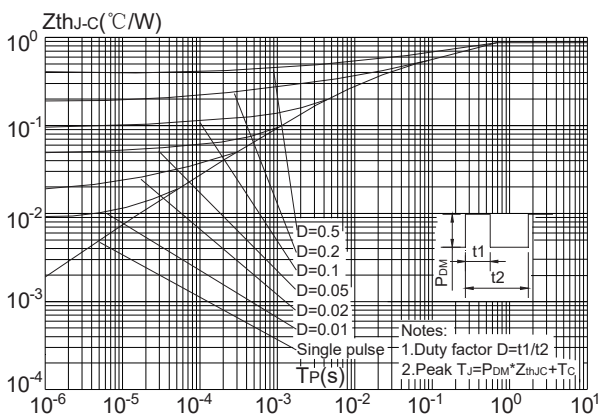
**Figure 9:** Maximum Safe Operating Area



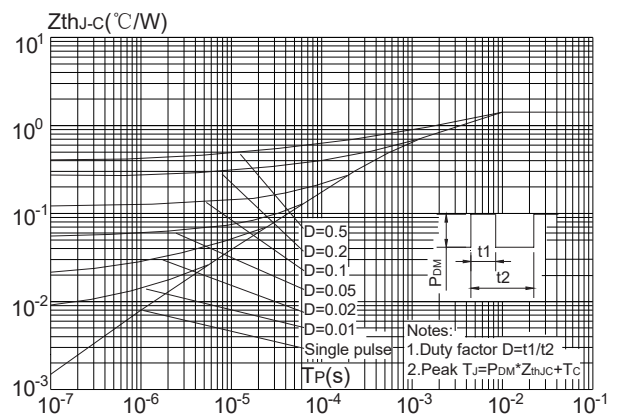
**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-220C,TO-263)



**Figure.12:** Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-220F)



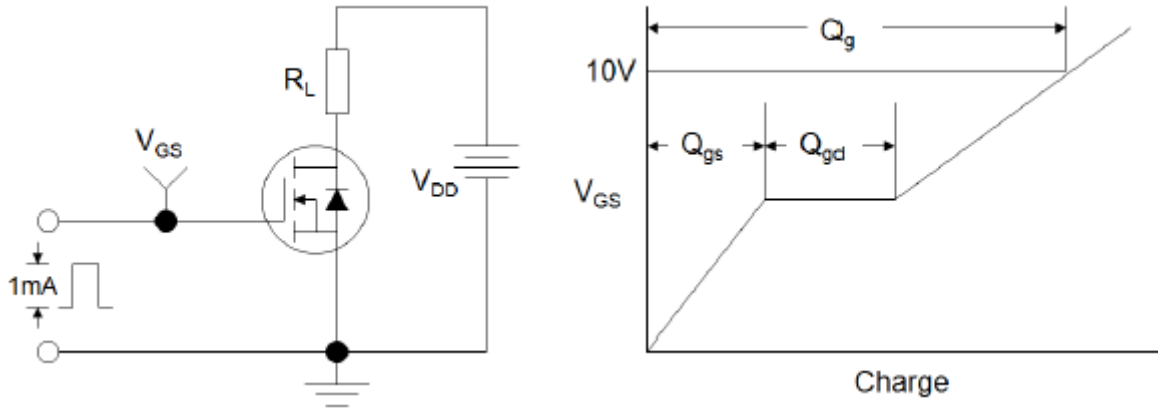


Figure1:Gate Charge Test Circuit & Waveform

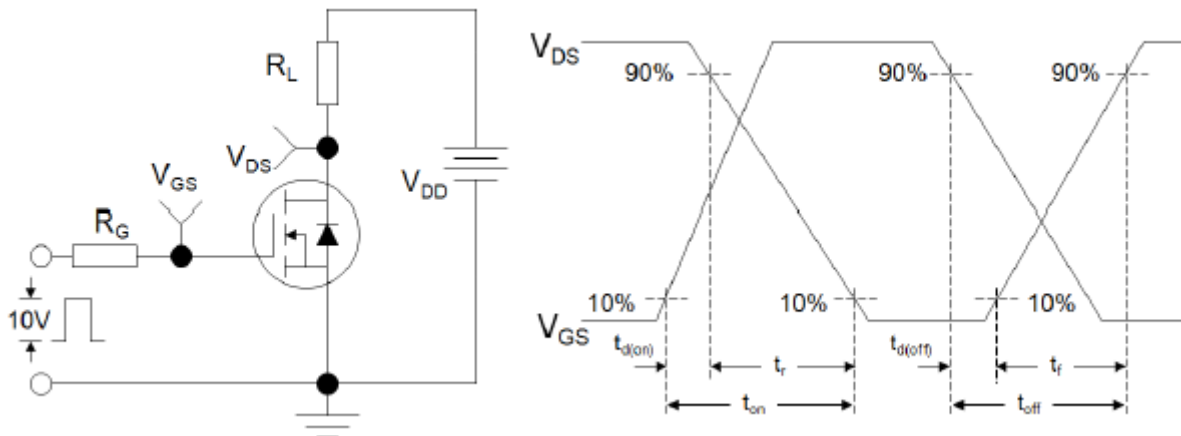


Figure 2: Resistive Switching Test Circuit & Waveforms

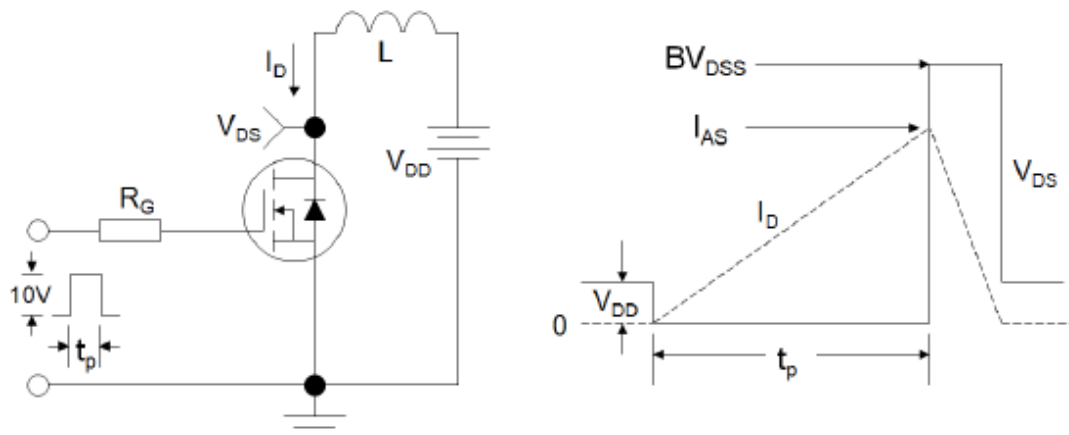
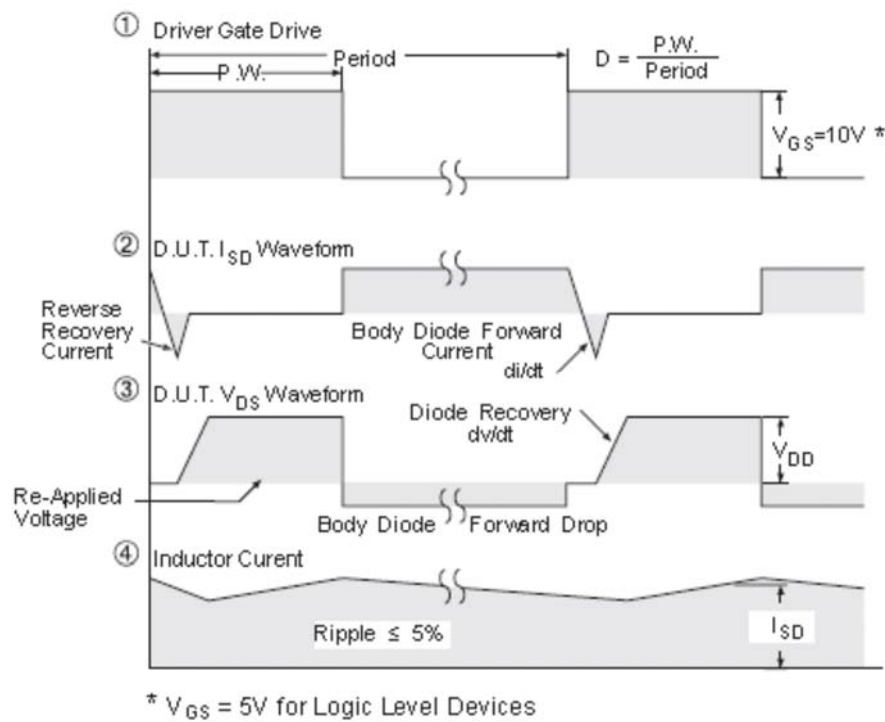
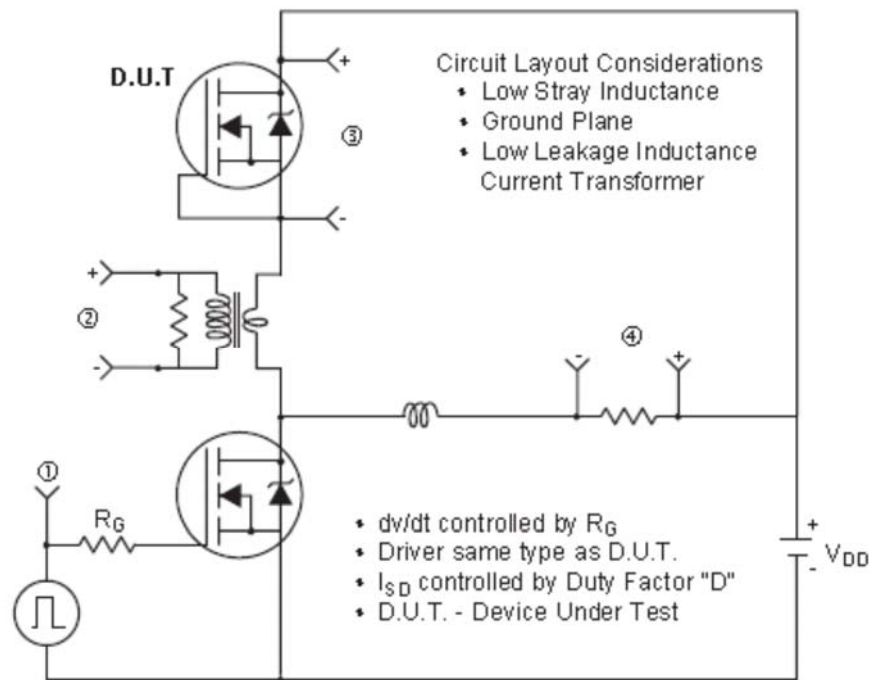


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



**Figure 4: Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms (For N-channel)**