

Description

Features

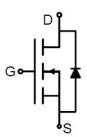
- 20V,3A
 - $R_{DS(ON)}$ < 55m Ω @ V_{GS} =4.5V
 - $R_{DS(ON)}$ < 85m Ω @ V_{GS} = 2.5V
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- Lead free product is acquired

Application

- Load Switch
- PWM Application
- Power management







Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
VSM2302B-S2	VSM2302B	TAPING	SOT-23-3	7inch	3000	180000

Absolute Maximum Ratings (T_A=25 ℃ unless otherwise specified)

Symbol	Parameter	Max.	Units	
V _{DSS}	Drain-Source Voltage	20	V	
V _{GSS}	Gate-Source Voltage		±12	V
	Continuos Proje Comment	T _A = 25°C	3	Α
l _D	Continuous Drain Current	T _A = 100°C	2	Α
I _{DM}	Pulsed Drain Current note1			Α
P _D	Power Dissipation	T _A = 25°C	0.77	W
R _{θJA}	Thermal Resistance, Junction to Case Operating and Storage Temperature Range		162	°C/W
T _J , T _{STG}			-55 to +150	$^{\circ}$



Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units		
Off Characteristic								
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V,I _D =250µA	20	-	-	V		
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 19V, V_{GS} = 0V,$	-	-	1.0	μΑ		
I _{GSS}	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 12V$	-	1	±100	nA		
On Characteristics								
V _{GS(th)}	Gate Threshold Voltage	V_{DS} = V_{GS} , I_D =250 μ A	0.4	0.7	1.0	V		
В	Static Drain-Source on-Resistance	V _{GS} =4.5V, I _D =3A	-	45	55	mΩ		
R _{DS(on)}	note2	V _{GS} =2.5V, I _D =2A	-	62	85			
Dynamic Characteristics								
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f = 1.0MHz	-	184	-	pF		
Coss	Output Capacitance		-	38	-	pF		
C _{rss}	Reverse Transfer Capacitance		-	28	-	pF		
Qg	Total Gate Charge	V _{DS} =10V, I _D =3A, V _{GS} =4.5V	-	2.7	-	nC		
Qgs	Gate-Source Charge		-	0.4	-	nC		
Q_{gd}	Gate-Drain("Miller") Charge		-	0.5	-	nC		
Switching	Characteristics							
t _{d(on)}	Turn-on Delay Time	V _{DS} =10V,	-	2.3	-	ns		
t _r	Turn-on Rise Time		-	3.1	-	ns		
t _{d(off)}	Turn-off Delay Time	$I_D=3A$, $R_{GEN}=3\Omega$,	-	9.2	-	ns		
t _f	Turn-off Fall Time	V _{GS} =4.5V	-	2.5	-	ns		
Drain-Sou	rce Diode Characteristics and Maxim	um Ratings						
Is	Maximum Continuous Drain to Source Diode Forward Current				3	Α		
IS				-	J	Α.		
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	12	Α		
V _{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V$, $I_{S}=3A$	-	-	1.2	V		

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

^{2.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



Typical Performance Characteristics

Figure1: Output Characteristics

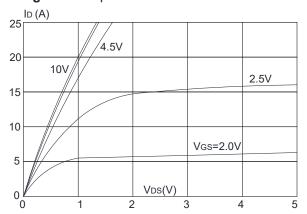


Figure 3:On-resistance vs. Drain Current

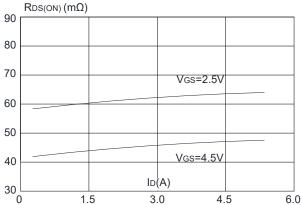


Figure 5: Gate Charge Characteristics

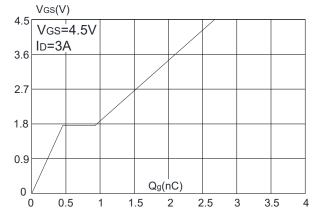


Figure 2: Typical Transfer Characteristics

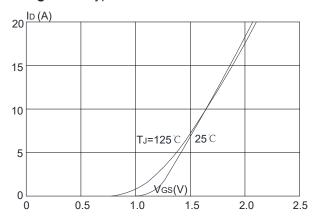


Figure 4: Body Diode Characteristics

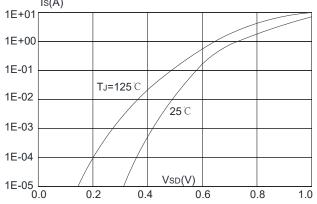


Figure 6: Capacitance Characteristics

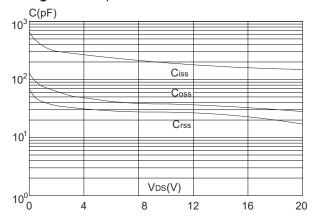




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

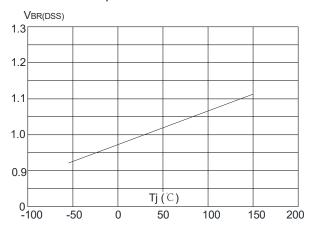


Figure 9: Maximum Safe Operating Area

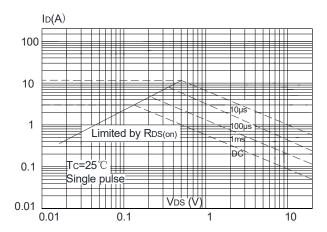


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

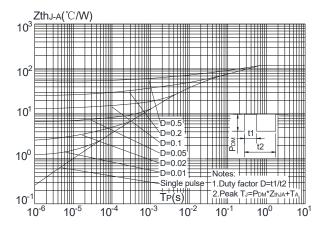


Figure 8: Normalized on Resistance vs. Junction Temperature

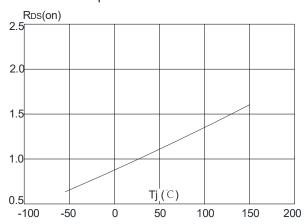
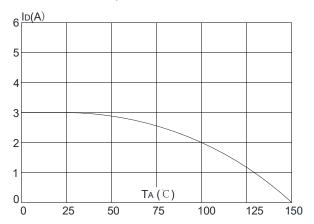


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature





Test Circuit

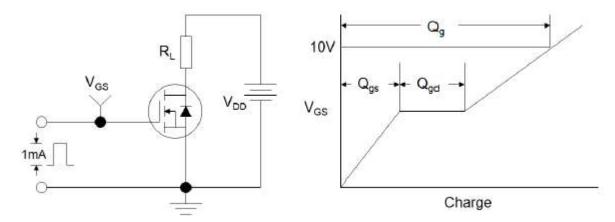


Figure1:Gate Charge Test Circuit & Waveform

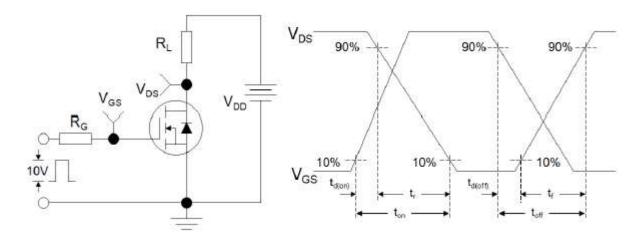


Figure 2: Resistive Switching Test Circuit & Waveforms

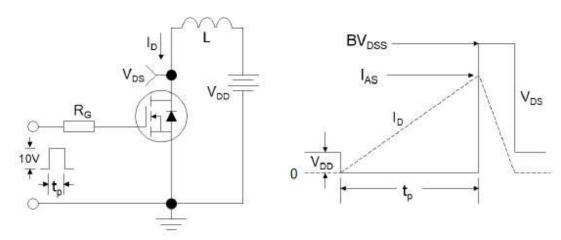


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms