

## Description

### Features

- 55V,110A
- $R_{DS(ON)} = 6.8m\Omega$  (Typ.) @  $V_{GS} = 10V, I_D = 30A$
- Fast Switching
- 100% Avalanche Tested
- Improved dv/dt Capability

### Application

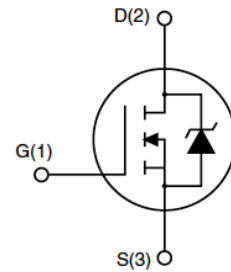
- Uninterruptible Power Supply(UPS)
- High Efficiency Switch Mode Power Supplies



TO-220C



TO-263



Schematic Diagram

## Absolute Maximum Ratings (T<sub>C</sub>=25℃ unless otherwise specified)

Symbol	Parameter		Max.	Units
			TO-220C/TO-263	
V <sub>DSS</sub>	Drain-Source Voltage		55	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 25℃	110	A
		T <sub>C</sub> = 100℃	80	A
I <sub>DM</sub>	Pulsed Drain Current <sup>note1</sup>		390	A
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25℃	200	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case		0.75	℃/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	℃

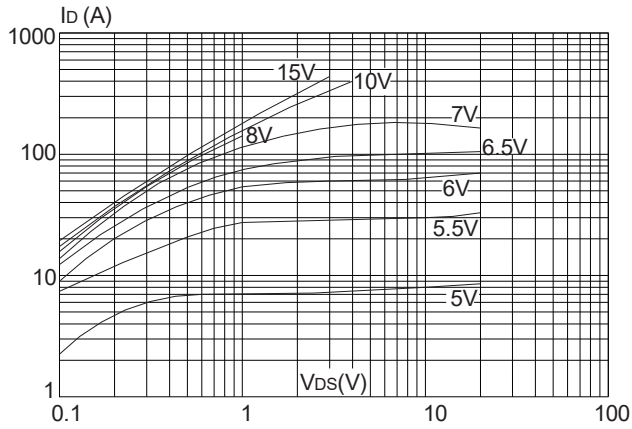
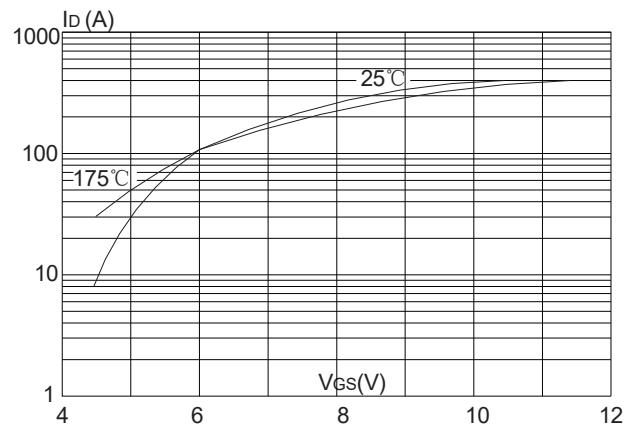
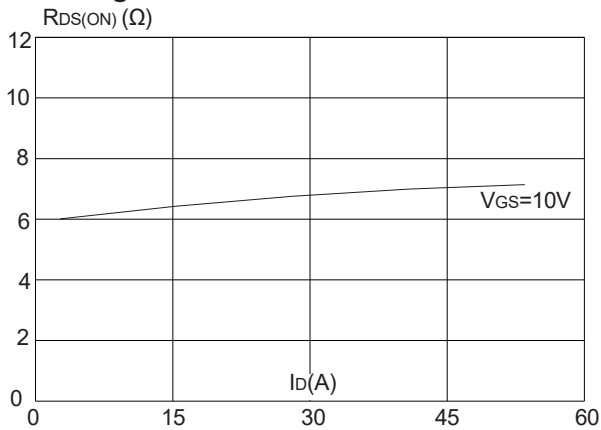
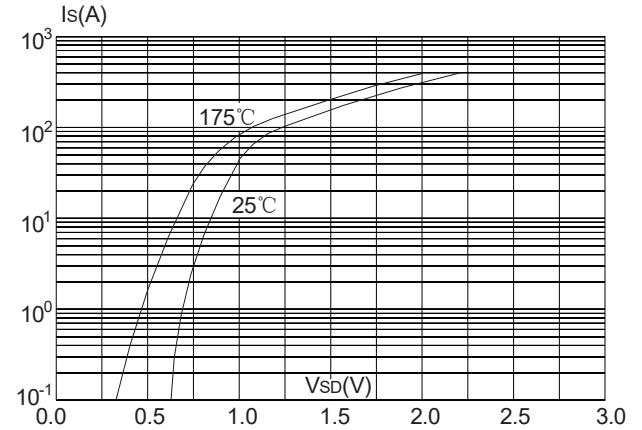
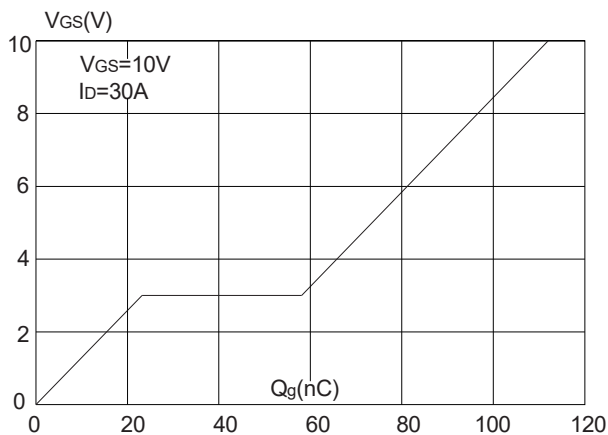
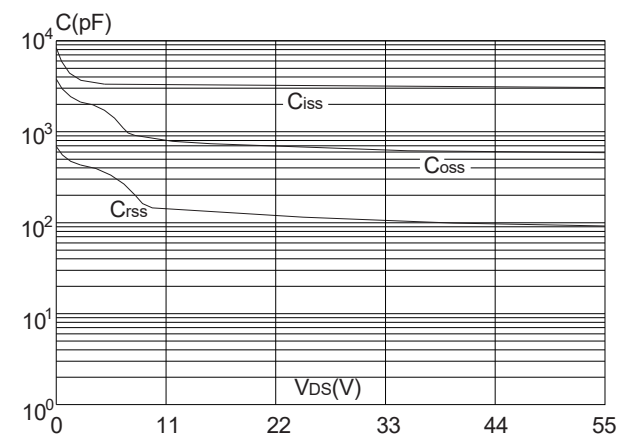
**Electrical Characteristics** ( $T_C=25^{\circ}\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> =250μA	55	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =55V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 25℃	-	-	1.0	μA
		V <sub>DS</sub> =44V, T <sub>C</sub> = 125℃	-	-	10	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V,V <sub>GS</sub> = ±20V	-	-	±100	nA
On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	-	4.0	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note2</small>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	-	6.8	8.0	mΩ
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =20V, I <sub>D</sub> =30A	45	-	-	S
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	3291	-	pF
C <sub>oss</sub>	Output Capacitance		-	671.5	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	112.1	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> =44V, I <sub>D</sub> =30A, V <sub>GS</sub> = 10V	-	112	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	23.2	-	nC
Q <sub>gd</sub>	Gate-Drain(“Miller”) Charge		-	34.9	-	nC
Switching Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =28V, I <sub>D</sub> =30V, R <sub>G</sub> =5Ω, V <sub>GS</sub> =10V,	-	19.5	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	50.7	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	55	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	24.6	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	110	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	390	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> =30A	-	-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>F</sub> =30A, di/dt=100A/μs	-	62.3	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	137	-	nC

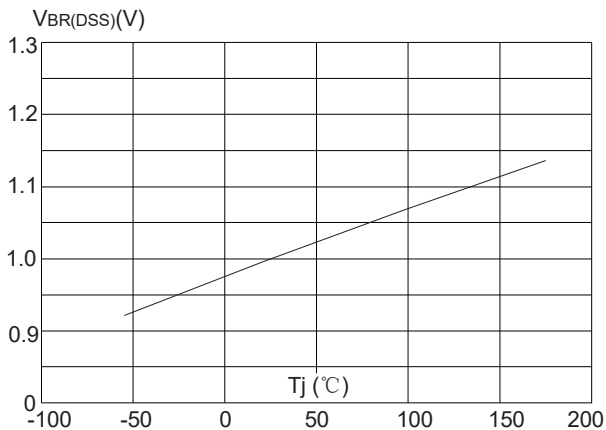
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$

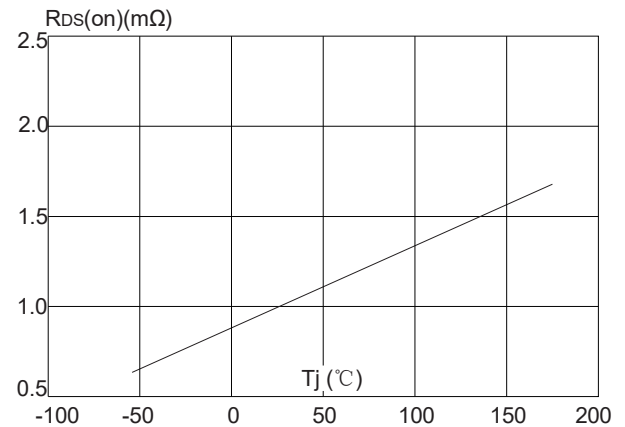
## Typical Performance Characteristics

**Figure1: Output Characteristics**

**Figure 2: Typical Transfer Characteristics**

**Figure 3: On-resistance vs. Drain Current**

**Figure 4: Body Diode Characteristics**

**Figure 5: Gate Charge Characteristics**

**Figure 6: Capacitance Characteristics**


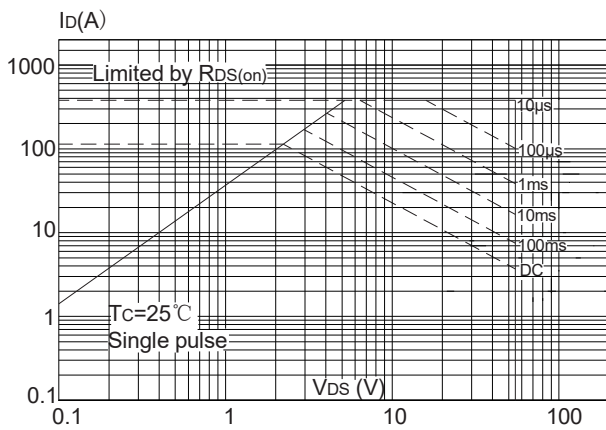
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



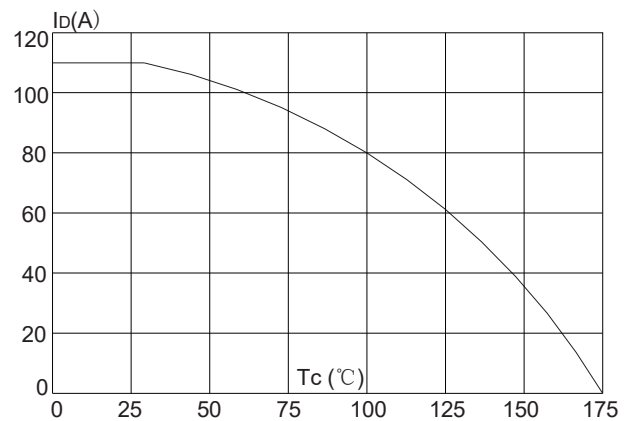
**Figure 8:** Normalized on Resistance vs. Junction Temperature



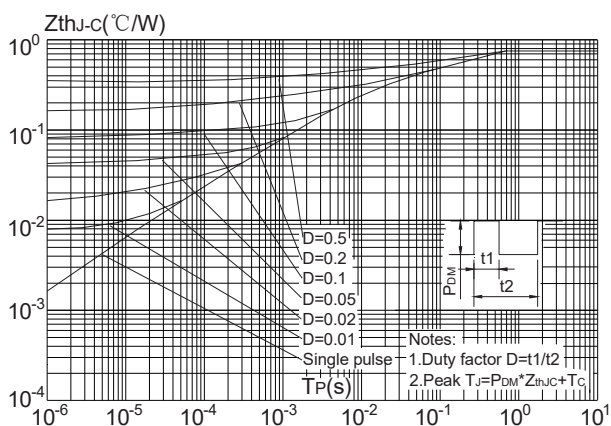
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-220C, TO-263)



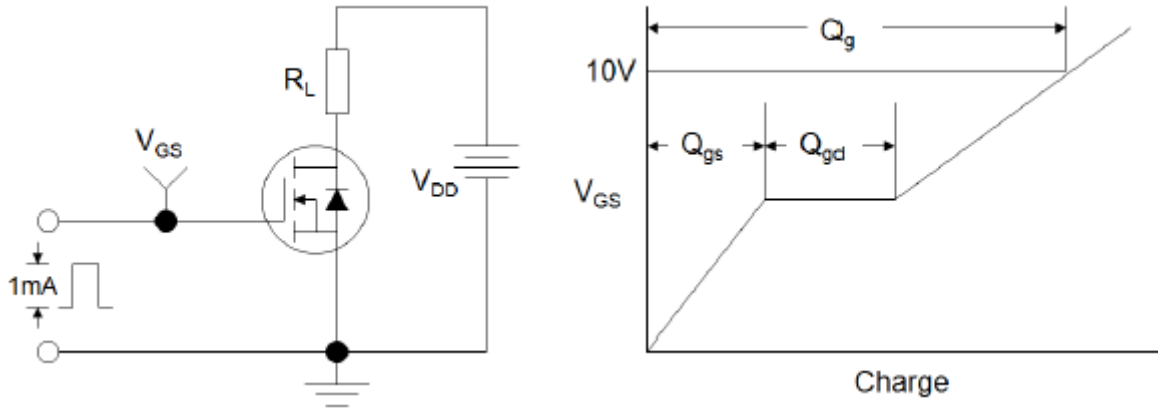


Figure1:Gate Charge Test Circuit & Waveform

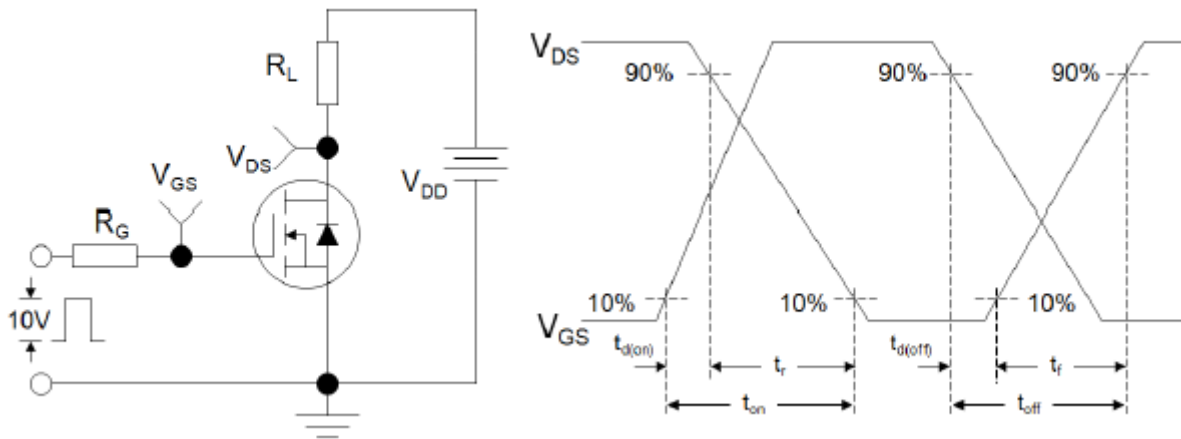


Figure 2: Resistive Switching Test Circuit & Waveforms

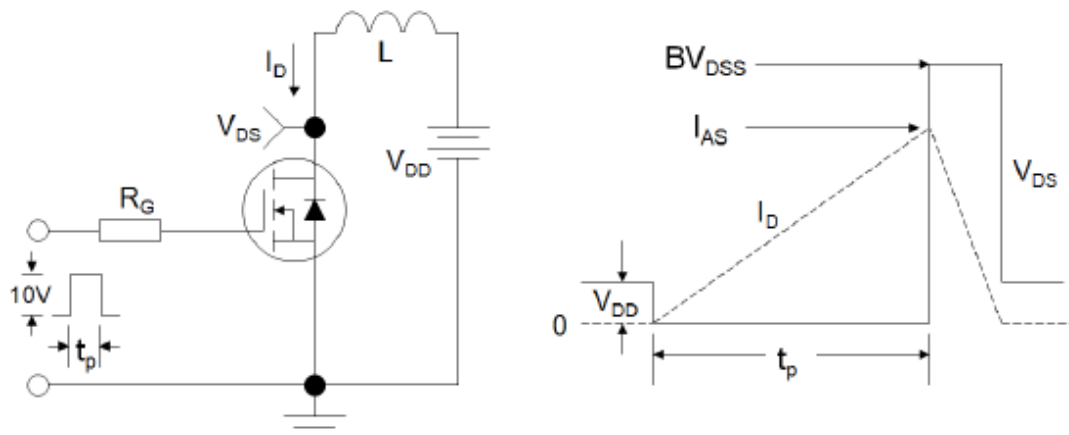
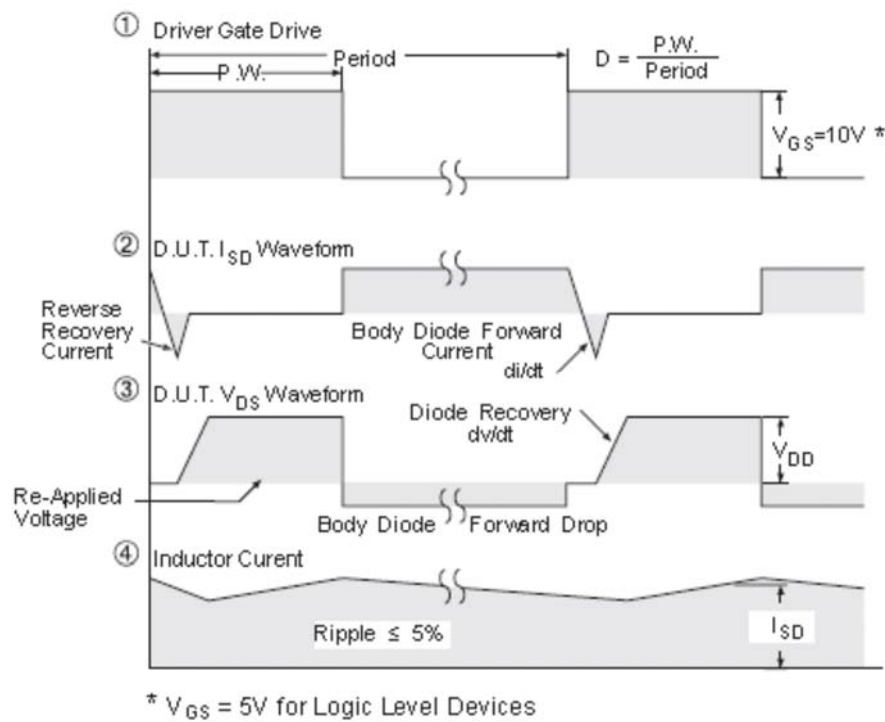
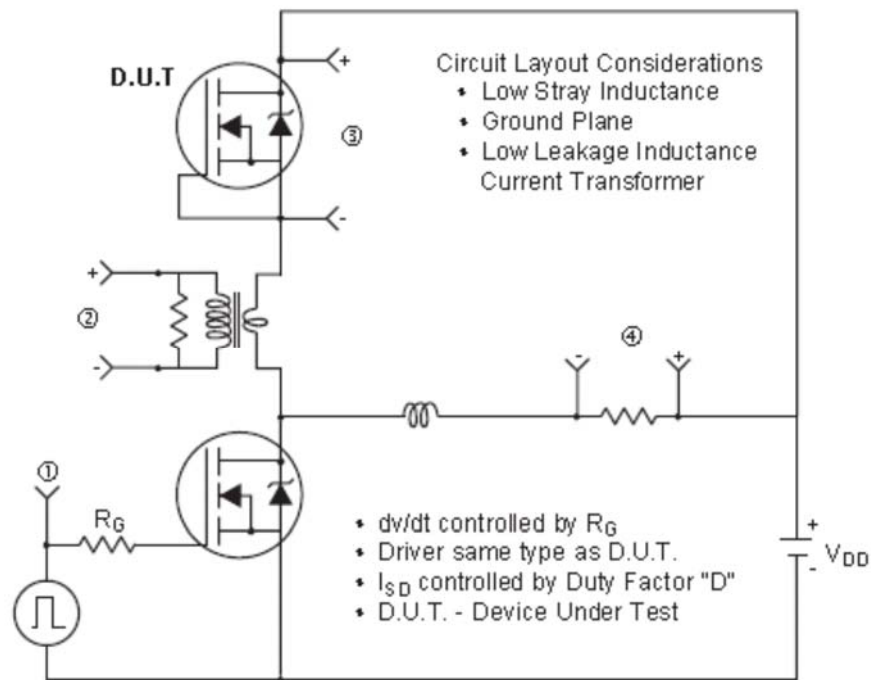


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



**Figure 4: Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms (For N-channel)**