

#### **Description**

### **Application Features** ● 600V, 2A Switch Mode Power Supply(SMPS) • $R_{DS(ON)} = 4.3\Omega$ (Typ.) @ $V_{GS} = 10V$ , $I_D = 1A$ Charger, LED Fast Switching Power Factor Correction (PFC) Improved dv/dt Capability • 100% Avalanche Tested SOT-223 SOT-89 TO-251 TO-252 D(2) Q G(1) S(3) O TO-220F TO-220C Schematic Diagram

### **Absolute Maximum Ratings** (Tc=25°C unless otherwise specified)

	Parameter		Max.				
Symbol			TO-251/	TO-220F		SOT-89/	Units
			TO-252/		TO-220C	SOT-223	
			TO-251			301-223	
VDSS	Drain-Source Voltage		600				V
Vgss	Gate-Source Voltage	±30				V	
lD	Continuous Drain Current	T <sub>C</sub> = 25 °C	2				Α
	T <sub>C</sub> = 100°C			Α			
I <sub>DM</sub>	Pulsed Drain Current note1			Α			
E <sub>AS</sub>	Single Pulsed Avalanche Energy note2		6.4				mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	25	20	32.5	9.6	W
Rejc	Thermal Resistance, Junction to Case		5	6.25	3.85	13	°C/W
Reja	Thermal Resistance, Junction to Ambient		60	62.5	60	65	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temper	-55 to +150				$^{\circ}\!\mathbb{C}$	



## **Electrical Characteristics** ( $T_C=25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units				
Off Characteristic										
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> =250µA	600	-	_	V				
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 600V, V_{GS} = 0V,$ $T_{J} = 25^{\circ}C$	-	-	1	μΑ				
Igss	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±30V	-	-	±100	nA				
On Characteristics										
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250µA	2.0	3.0	4.0	V				
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =1.0A	-	4.3	5	Ω				
Dynamic C	haracteristics			•	•					
Ciss	Input Capacitance	05)/ )/ 05)/	-	310	_	рF				
Coss	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$	-	39	-	pF				
Crss	Reverse Transfer Capacitance	f = 1.0MHz	-	6	-	pF				
Qg	Total Gate Charge	\/ = 400\/ I= = 2.0A	-	8	-	nC				
Qgs	Gate-Source Charge	$V_{DD} = 480V, I_{D} = 2.0A,$ $V_{GS} = 10V$	-	1.2	-	nC				
$Q_{\text{gd}}$	Gate-Drain("Miller") Charge	VGS - 10V	1	5.0	-	nC				
Switching Characteristics										
t <sub>d(on)</sub>	Turn-on Delay Time		-	7.8	-	ns				
t <sub>r</sub>	Turn-on Rise Time	$V_{DD} = 250V, I_D = 2.0A,$	-	33	-	ns				
t <sub>d(off)</sub>	Turn-off Delay Time	$R_G = 25\Omega$	-	23	-	ns				
t <sub>f</sub>	Turn-off Fall Time		1	59	-	ns				
Drain-Sour	ce Diode Characteristics and Maxim	um Ratings								
ls	Maximum Continuous Drain to Source Diode Forward Current		-	-	2	Α				
Ism	Maximum Pulsed Drain to Source Dio	-	-	8	Α					
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>SD</sub> = 2.0A	-	-	1.4	V				
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>S</sub> =2.0A,	-	80	-	ns				
Qrr	Reverse Recovery Charge	di/dt=100A/μs		1.8	-	μC				

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2.  $I_{AS}$  = 2.0A,  $V_{DD}$  = 50V,  $R_{G}$  = 25  $\Omega$ , Starting  $T_{J}$  = 25°C
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤1%



# **Typical Performance Characteristics**

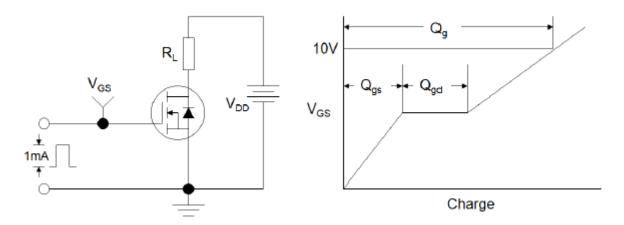


Figure1:Gate Charge Test Circuit & Waveform

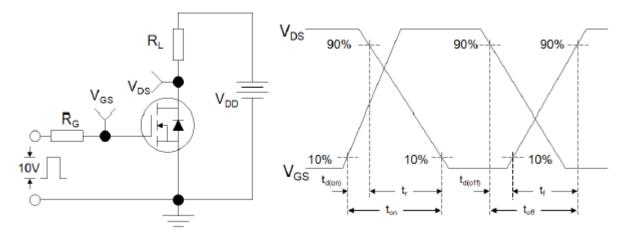


Figure 2: Resistive Switching Test Circuit & Waveforms

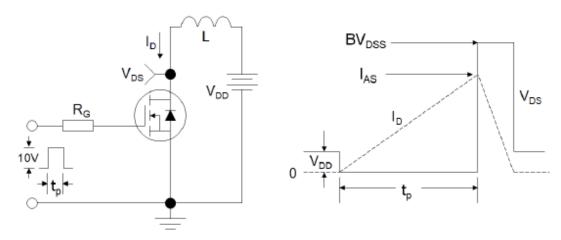
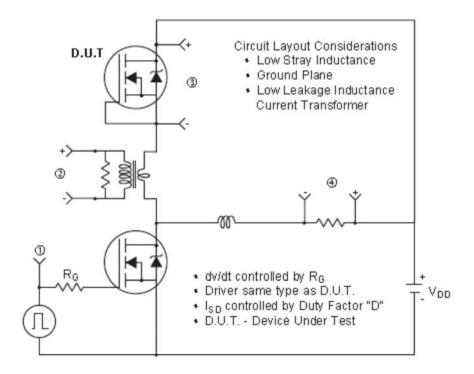


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms





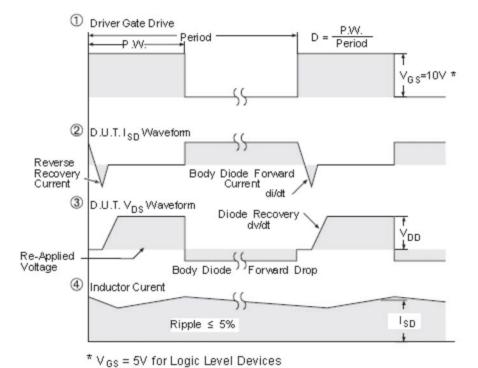


Figure 4:Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)