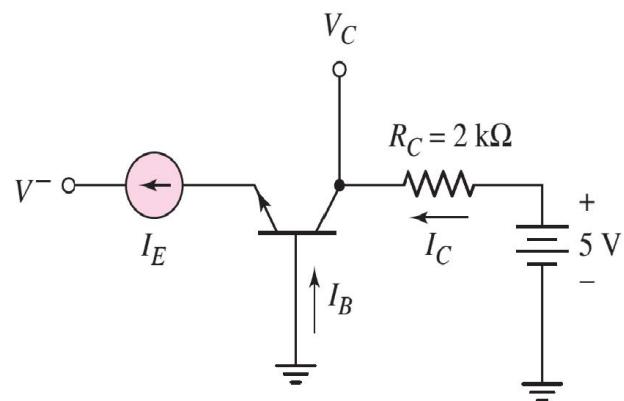
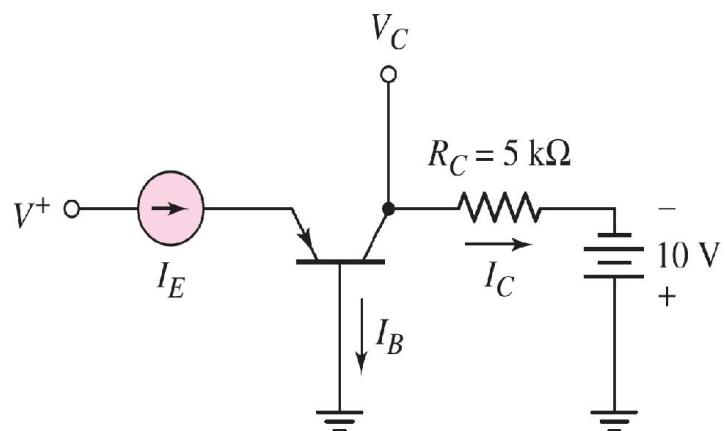


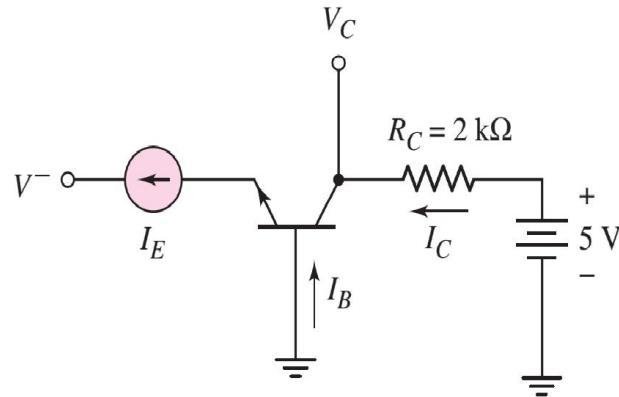
An npn transistor with $\beta = 80$ is connected in a common-base configuration as shown in Figure. (a) The emitter is driven by a constant-current source with $I_E = 1.2 \text{ mA}$. Determine I_B , I_C , α , and V_C . (b) Repeat part (a) for $I_E = 0.80 \text{ mA}$. (c) Repeat parts (a) and (b) for $\beta = 120$.



A pnp transistor with $\beta = 60$ is connected in a common-base configuration as shown in Figure. (a) The emitter is driven by a constant-current source with $I_E = 0.75 \text{ mA}$. Determine I_B , I_C , α , and V_C . (b) Repeat part (a) if $I_E = 1.5 \text{ mA}$. (c) Is the transistor biased in the forward-active mode for both parts (a) and (b)? Why or why not?



An npn transistor with $\beta = 80$ is connected in a common-base configuration as shown in Figure. (a) The emitter is driven by a constant-current source with $I_E = 1.2$ mA. Determine I_B , I_C , α , and V_C . (b) Repeat part (a) for $I_E = 0.80$ mA. (c) Repeat parts (a) and (b) for $\beta = 120$.



$$(a) \quad I_B = \frac{I_E}{1+\beta} = \frac{1.2}{81} \Rightarrow I_B = 14.8 \mu A$$

$$I_C = \left(\frac{\beta}{1+\beta} \right) \cdot I_E = \left(\frac{80}{81} \right) (1.2) = 1.185 \text{ mA}$$

$$\alpha = \frac{80}{81} = 0.9877$$

$$V_C = 5 - (1.185)(2) = 2.63 \text{ V}$$

$$(b) \quad I_B = \frac{0.8}{81} \Rightarrow I_B = 9.88 \mu A$$

$$I_C = \left(\frac{80}{81} \right) (0.8) = 0.790 \text{ mA}$$

$$\alpha = 0.9877$$

$$V_C = 5 - (0.790)(2) = 3.42 \text{ V}$$

$$(c) \quad (i) \quad I_B = \frac{1.2}{121} \Rightarrow I_B = 9.92 \mu A$$

$$I_C = \left(\frac{120}{121} \right) (1.2) = 1.19 \text{ mA}$$

$$\alpha = \frac{120}{121} = 0.9917$$

$$V_C = 5 - (1.19)(2) = 2.62 \text{ V}$$

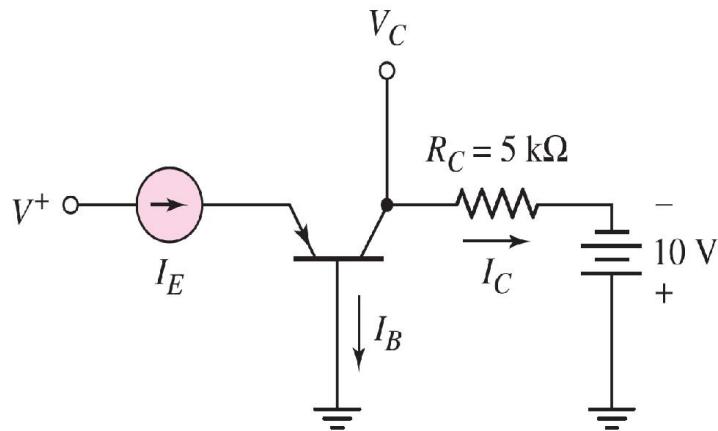
$$(ii) \quad I_B = \frac{0.8}{121} \Rightarrow I_B = 6.61 \mu A$$

$$I_C = \left(\frac{120}{121} \right) (0.8) = 0.7934 \text{ mA}$$

$$\alpha = 0.9917$$

$$V_C = 5 - (0.7934)(2) = 3.41 \text{ V}$$

A pnp transistor with $\beta = 60$ is connected in a common-base configuration as shown in Figure. (a) The emitter is driven by a constant-current source with $I_E = 0.75$ mA. Determine I_B , I_C , α , and V_C . (b) Repeat part (a) if $I_E = 1.5$ mA. (c) Is the transistor biased in the forward-active mode for both parts (a) and (b)? Why or why not?



$$I_B = \frac{0.75}{61} \Rightarrow 12.3 \mu\text{A}$$

$$I_C = (0.75) \left(\frac{60}{61} \right) = 0.738 \text{ mA}$$

$$\alpha = \frac{60}{61} = 0.9836$$

$$V_C = I_C R_C - 10 = (0.738)(5) - 10$$

a) $V_C = -6.31 \text{ V}$

$$I_B = \frac{1.5}{61} \Rightarrow 24.6 \mu\text{A}$$

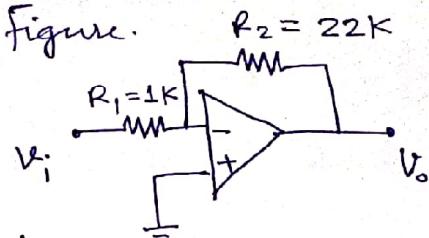
$$I_C = (1.5) \left(\frac{60}{61} \right) = 1.475 \text{ mA}$$

$$\alpha = \left(\frac{60}{61} \right) = 0.9836$$

b) $V_C = (1.475)(5) - 10 \Rightarrow V_C = -2.625 \text{ V}$

c) Yes, $V_C < 0$ in both cases so that B-C junction is reverse biased.

① Consider the circuit shown in figure.



a) Determine the ideal output-voltage v_o if $v_i = -0.40\text{ V}$

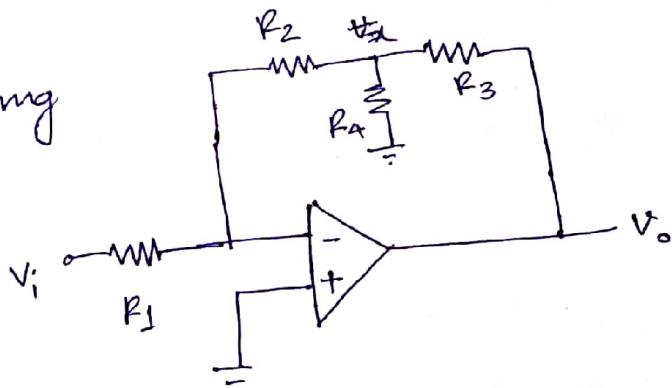
b) Determine the actual output voltage if the open-loop gain of the op-amp is

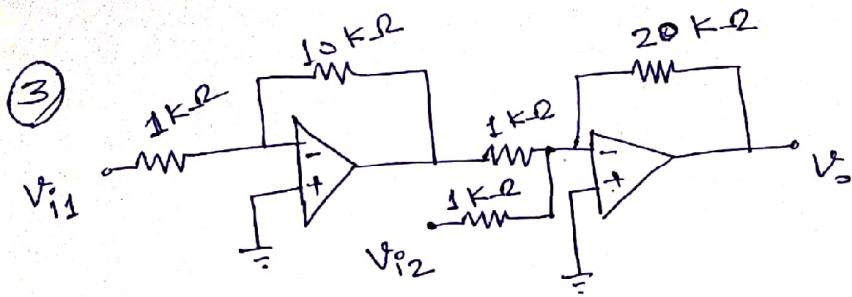
$$A_{od} = 5 \times 10^3$$

c) Determine the required value of A_{od} in order that the actual voltage gain be within 0.2% of the ideal value.

2) For the ideal inverting op-amp circuit with T-network, shown in Figure,

the circuit parameters are $R_1 = 10\text{ k}\Omega$, $R_2 = R_3 = 50\text{ k}\Omega$, and $R_4 = 5\text{ k}\Omega$. Determine the closed loop-voltage gain.





Determine V_o for

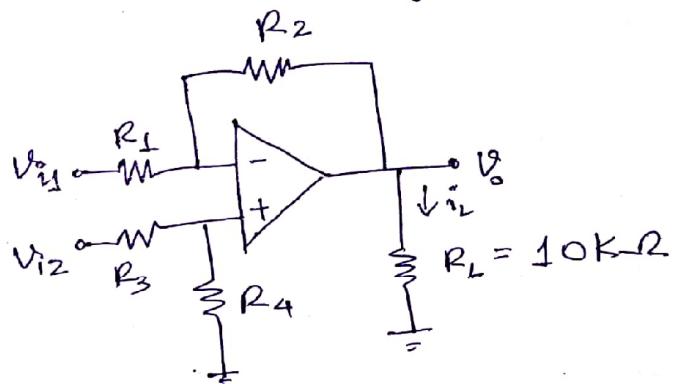
$$V_{i1} = +5 \text{ mV} \text{ and}$$

$$V_{i2} = -25 - 50 \sin \omega t \text{ mV}$$

(4) Consider the difference amplifier given.

Let $R_1 = R_3$ and

$$R_2 = R_4 .$$



a) Design the circuit

such that the difference voltage gain is $A_d = 15$
and the minimum difference input resistance
is $30 \text{ k}\Omega$.

b) If the load current is $i_L = 0.25 \text{ mA}$,
what is the differential input voltage
 $(V_{i2} - V_{i1})$?

Ans:

① a) $v_o = \frac{-R_2}{R_1} \cdot v_i = \left(\frac{-22}{1} \right) (-0.40) = 8.8 V$

b)

$$A_v = \frac{-R_2}{R_1} \cdot \frac{1}{1 + \frac{1}{A_{od}} \left(1 + \frac{R_2}{R_1} \right)} = \frac{-22 \cdot \frac{1}{1 + \frac{1}{5 \times 10^3} \times 23}}{1 + \frac{1}{5 \times 10^3}}$$
$$= -21.8993$$

$$v_o = (-21.8993)(-0.40) = 8.7597 V$$

c) $A_v = -0.998 \times 22 = -21.956$

$$-21.956 = -22 \times \frac{1}{1 + \frac{1}{A_{od}} \times 23} \Rightarrow A_{od} = 1.1477 \times 10^4$$

② $A_v = -\frac{R_2}{R_1} \left(1 + \frac{R_3}{R_4} + \frac{R_3}{R_2} \right)$

$$= \frac{-50}{10} \left(1 + \frac{50}{5} + \frac{50}{50} \right) = -60$$

$$3) V_o = \left(\frac{-10}{1}\right) \left(\frac{-20}{1}\right) \times V_{i1} + \left(\frac{-20}{1}\right) V_{i2}$$

$$= 200 V_{i1} - 20 V_{i2}$$

$$= 200 \times 5 - 20 (-25 - 50 \sin \omega t)$$

$$= 1000 + 500 + 1000 \sin \omega t \text{ (mV)}$$

$$= 1.5 + 1 \sin \omega t \text{ (V)}$$

④

$$a) R_{id} = R_1 + R_3 = 30 \text{ k}\Omega$$

$$\therefore R_1 = R_3 = 15 \text{ k}\Omega$$

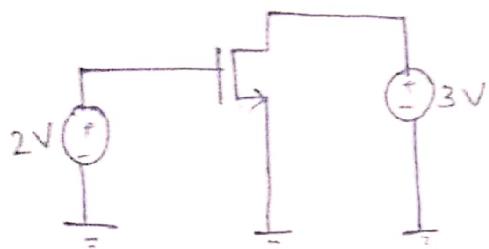
$$A_d = \frac{R_2}{R_1} = \frac{R_4}{R_3} = 15 \Rightarrow R_2 = R_4 = 225 \text{ k}\Omega$$

$$b) V_o = i_L R_L = 0.25 \times 10 = 2.5 \text{ V}$$

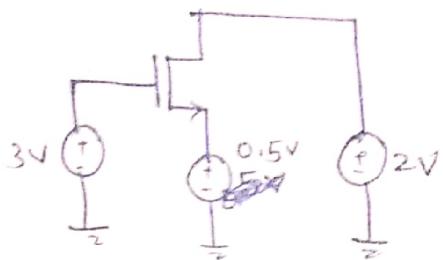
$$V_{i2} - V_{i1} = \frac{V_o}{A_d} = \frac{2.5}{15} = 0.1667 \text{ V}$$

1. For the following circuit configurations, find the region of operation of the transistor. Consider threshold voltage of n-MOS transistor to be 0.6V.

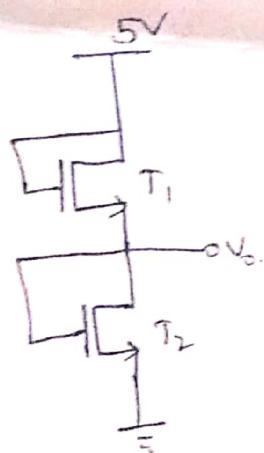
(a)



(b)



2. Both transistors T_1 & T_2 shown in the figure have a threshold voltage of 1V. The device parameters K_1, ϵ_1, K_2 of transistors T_1 & T_2 are $36\mu A/V^2$ & $9\mu A/V^2$ respectively. Find the voltage V_o .



$$1. a) V_{GS} = 2 - 0 = 2V$$

$$V_{DS} = 3V$$

$$V_{GS} = 2V > V_T (0.6V) \Rightarrow \text{Transistor is on}$$

$$V_{GS} - V_T = 2 - 0.6 = 1.4 < V_{DS} \Rightarrow \text{Transistor is in Saturation region}$$

$$b) V_{GS} = 3 - 0.5 = 2.5V$$

$$V_{DS} = 2 - 0.5 = 1.5V$$

$$V_{GS} > V_T \Rightarrow \text{Transistor is on}$$

$$V_{GS} - V_T = 2.5 - 0.6 = 1.9V > V_{DS} \Rightarrow \text{Transistor is in Linear region}$$

$$2) I_{DS}(T_1) = I_{DS}(T_2)$$

& T_1, T_2 are in saturation (Gate & drain are connected)

$$\frac{1}{2} K_1 [V_{GS_1} - V_T]^2 = \frac{1}{2} K_2 [V_{GS_2} - V_T]^2$$

$$36 [5 - V_o - 1]^2 = A [V_o - 1]^2$$

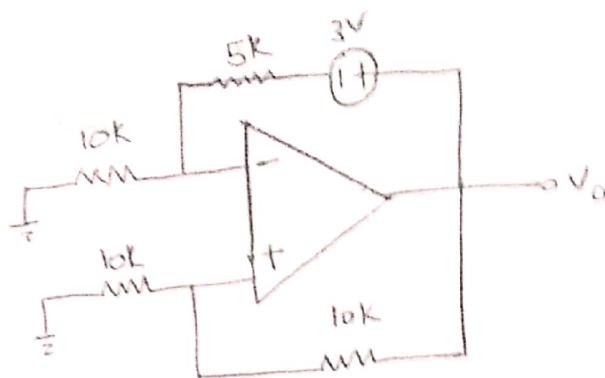
$$\Rightarrow 2 = \frac{V_o - 1}{4 - V_o}$$

$$\Rightarrow 8 - 2V_o = V_o - 1$$

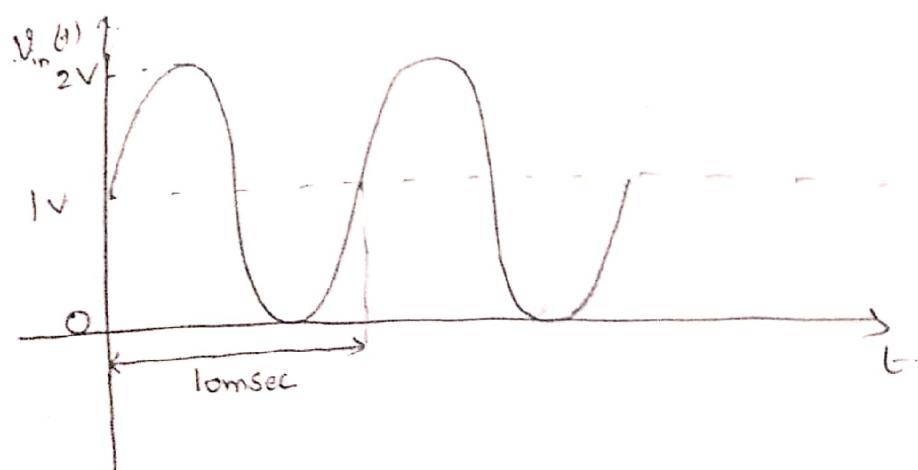
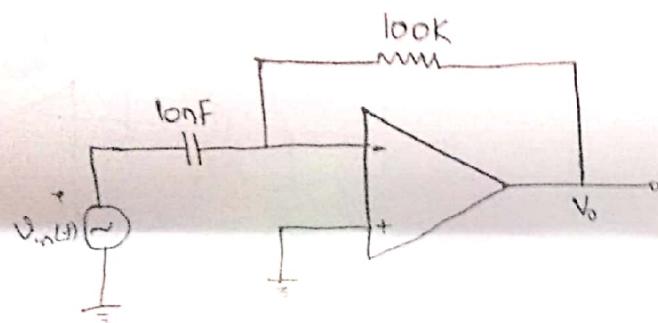
$$\Rightarrow \boxed{V_o = 3V}$$

Exercise

1. For the circuit shown below, find the voltage V_o



2. For the differentiator circuit, shown below, sketch the op waveform for the given iip voltage.



$$1) A) I_1 = I_f \\ \frac{V_o - V_1}{10k} = \frac{V_1 - V_o + 3}{5k}$$

$$-V_1 = 2V_1 - 2V_o + 6$$

$$\Rightarrow 2V_o = 3V_1 + 6 \rightarrow ①$$

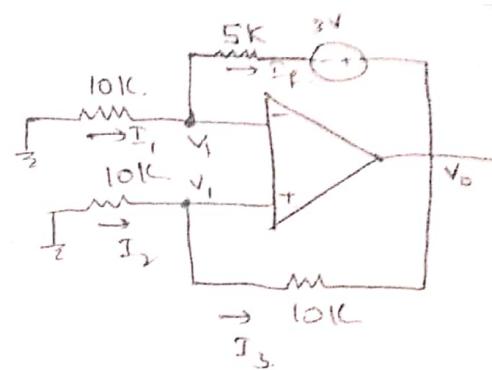
$$I_2 = I_3$$

$$\Rightarrow \frac{0 - V_1}{10k} = \frac{V_1 - V_o}{10k}$$

$$\Rightarrow 2V_1 = V_o \Rightarrow \boxed{V_1 = \frac{V_o}{2}} \rightarrow ②$$

put ② in ①, we get

$$2V_o = \frac{3V_o}{2} + 6 \Rightarrow \frac{V_o}{2} = 6 \Rightarrow \boxed{V_o = 12V}$$

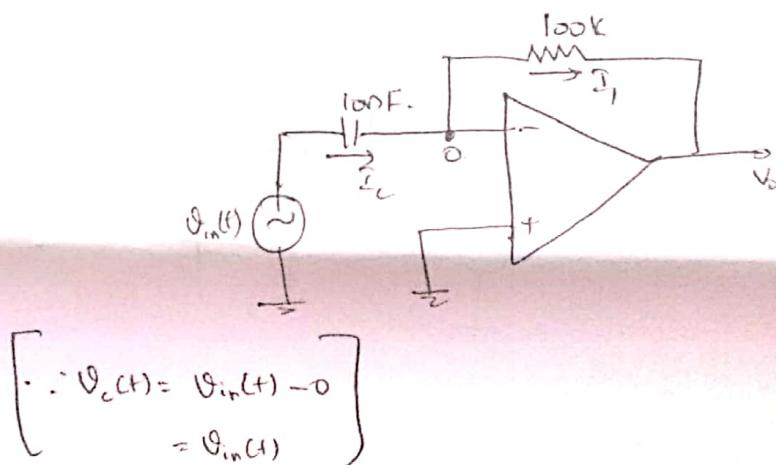


$$2) A) I_c = I_1$$

$$\Rightarrow C \frac{dV_c(t)}{dt} = -\frac{V_o}{100k}$$

$$\Rightarrow V_o = -10^5 \times 10^{-8} \times \frac{dV_{in}(t)}{dt}$$

$$\Rightarrow \boxed{V_o = -10^{-3} \frac{dV_{in}(t)}{dt}}$$



$$\begin{aligned} \therefore V_c(t) &= V_{in}(t) - 0 \\ &= V_{in}(t) \end{aligned}$$

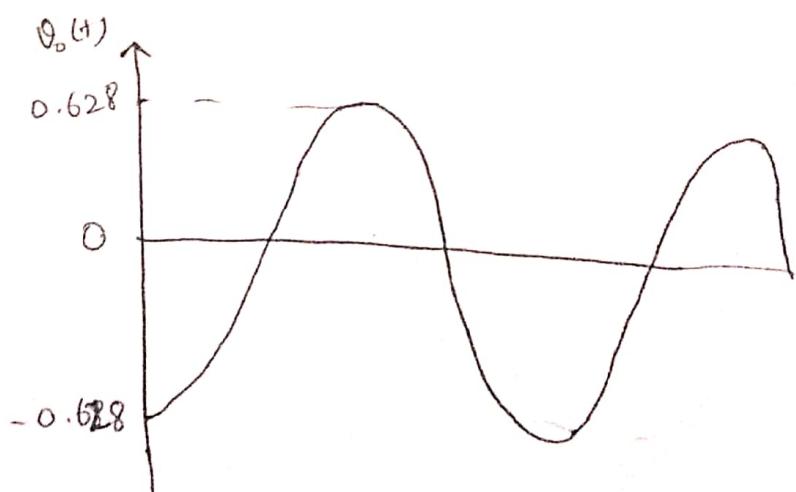
$$V_{in}(t) = 1 + \sin 2\pi \left(\frac{1}{100 \times 10^3} \right) t$$

$$= 1 + \sin(200\pi t)$$

$$\Rightarrow V_o = -10^{-3} \frac{d}{dt} (1 + \sin 200\pi t)$$

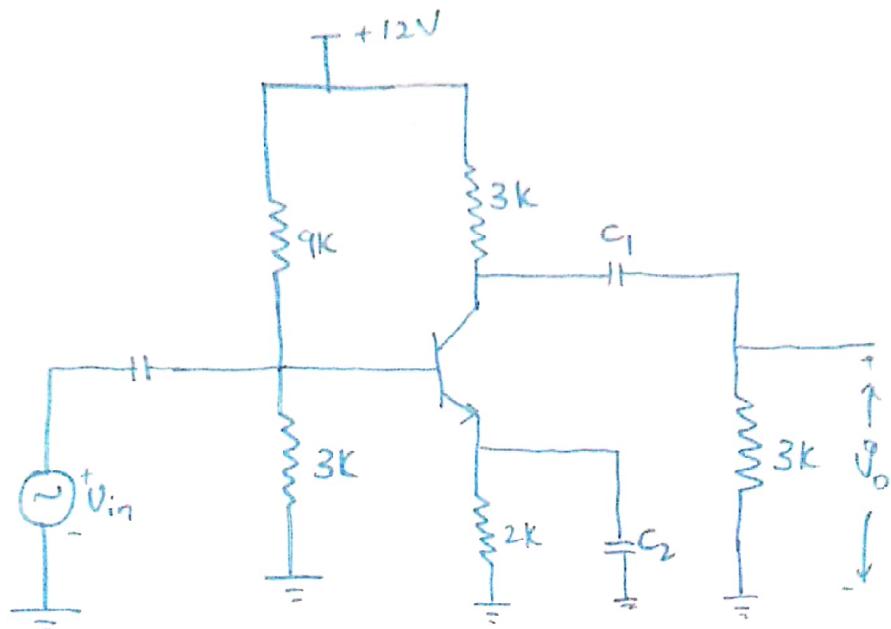
$$= -10^{-3} [(\cos(200\pi t)) * 200\pi]$$

$$\boxed{V_o = -0.628 \cos(200\pi t)}$$



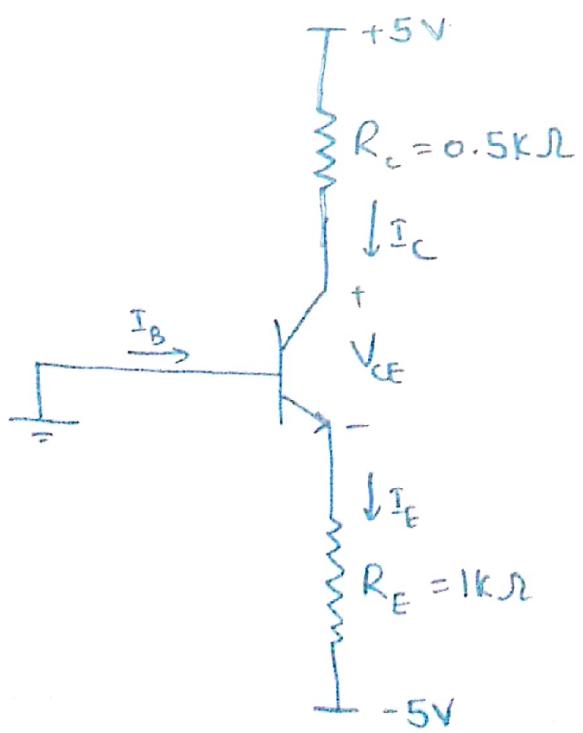
*1 For the CE Amplifier given, calculate Voltage gain, Current gain, i/p resistance \mathcal{E}_i o/p resistance if $\beta = 100$, $E_f = 25mV$

Neglect early effect. $V_{BE(on)} = 0.7V$

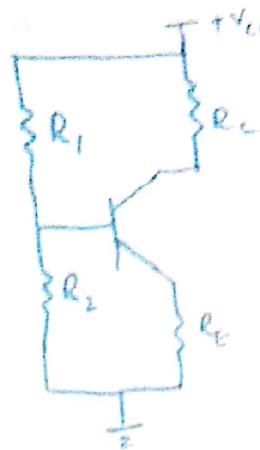
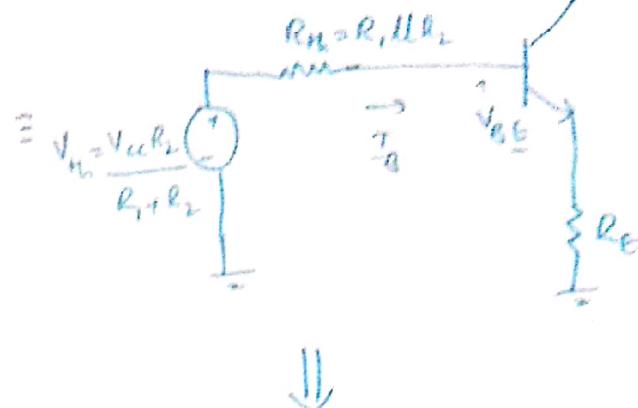


*2 Calculate the characteristics of the circuit shown below

Consider $V_{BE(on)} = 0.65V$ & $\beta = 100$. (Draw the Load line)



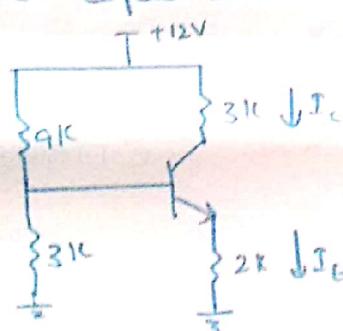
1) A)

Thevenin Equivalent:

$$\text{KVL} \Rightarrow V_{in} - I_B R_{Th} - V_{BE} - I_E R_E = 0$$

$$[I_B = \frac{V_{in}}{R_{Th}}]$$

$$\Rightarrow I_E = \frac{V_{in} - V_{BE}}{R_E + \frac{R_{Th}}{\beta+1}}$$

Dc Analysis: O.C capacitors

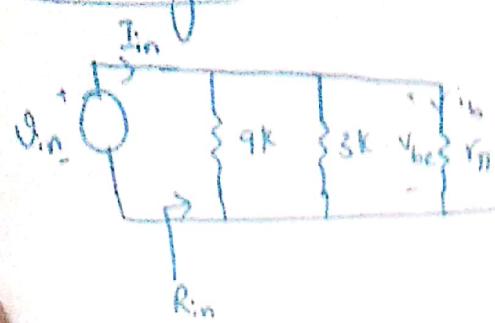
$$\begin{aligned} I_E &= \frac{12(3k)}{12k} = 0.7 \\ &\quad = \frac{2k + (3k)(9k)}{(12k)(10k)} = 1.137 \text{ mA} \end{aligned}$$

$$\Rightarrow I_C = \left(\frac{\beta}{\beta+1}\right) I_E = 1.126 \text{ mA}$$

$$g_m = \left| \frac{I_C}{V_T} \right| = \frac{1.126}{25}$$

$$\Rightarrow r_{\pi} = \left| \frac{\beta}{g_m} \right| = \frac{100}{\frac{1.126}{25}} = 2220.24$$

AC-Analysis: S.C. Capacitors



I_p Resistance:

$$R_{in} = 9k \parallel 3k = 2.22k$$

$$\Rightarrow R_{in} = 1.11k\Omega$$

o/p resistance:

S.C. I_p source i.e., $v_{be} > 0$

$$\Rightarrow V_{in} = i_b \cdot R_{in} \Rightarrow i_b > 0$$

$\Rightarrow 100i_b$ source will be D.C.

$$\Rightarrow R_o = 3k\Omega$$

Voltage gain:

$$V_{be} = V_{in}$$

$$\Rightarrow V_{be} = i_b \cdot R_{in}$$

$$\Rightarrow V_{in} = i_b \cdot 2.22k$$

$$i_b = \frac{V_{in}}{2.22k}$$

$$V_o = -100i_b [3k \parallel 3k]$$

$$= -100kV_{in} [1.5k]$$

$$\Rightarrow A_V = \left[\frac{V_o}{V_{in}} \right] = -67.5$$

Current gain:

$$9k \parallel 3k = 2.25k\Omega$$

$$i_b = \frac{I_{in} (2.25k)}{2.25k + 2.22k}$$

$$I_L = \frac{-100i_b + 3k}{3k + 3k} = -50i_b$$

$$A_2 = \left[\frac{I_L}{I_{in}} \right] = \frac{I_L}{i_b} \cdot \frac{i_b}{I_{in}}$$

$$= -50 \times \frac{2.25k}{4.57k}$$

$$A_2 = -25.14$$

2) A) KVL

$$0 - V_{BE} - I_E R_E + 5 = 0$$

$$\Rightarrow I_E = \frac{5 - 0.65}{1k} = 4.35 \text{ mA}$$

$$I_B = \frac{I_E}{\beta+1} = \frac{4.35}{101} = 43.1 \mu\text{A}$$

$$I_C = \left(\frac{\beta}{\beta+1}\right) I_E = 4.31 \text{ mA}$$

KVL

$$5 - I_C R_C - V_{CE} - I_E R_E + 5 = 0$$

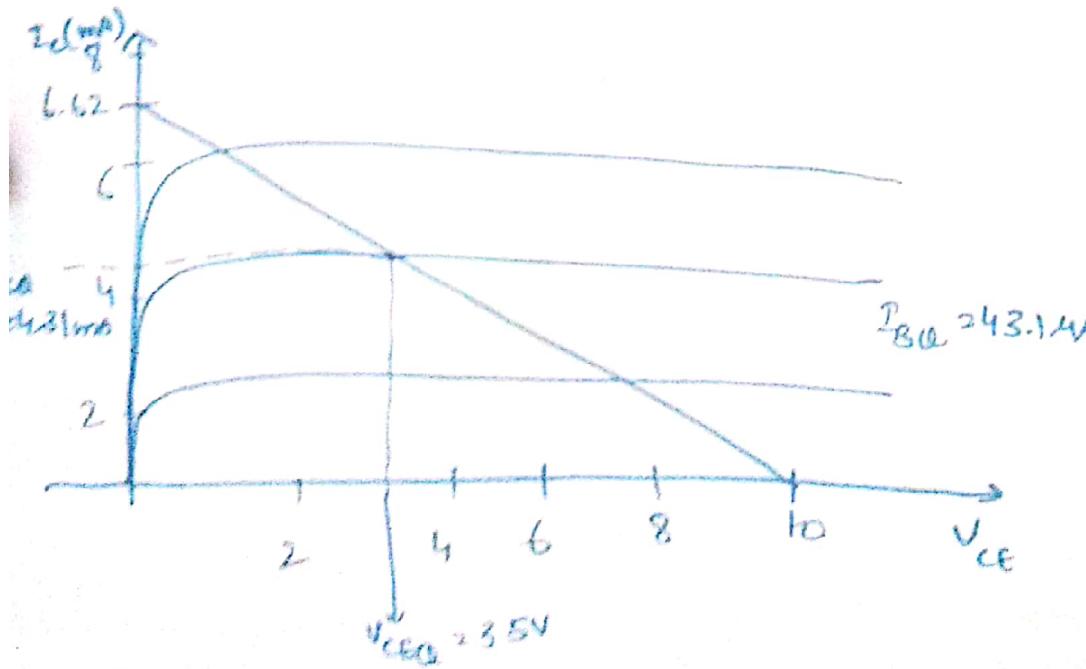
$$\Rightarrow V_{CE} = 10 - I_C R_C - I_E R_E$$

$$= 10 - (4.31)(0.5) - (4.35)(1) = 3.50 \text{ V}$$

Equation of load line is

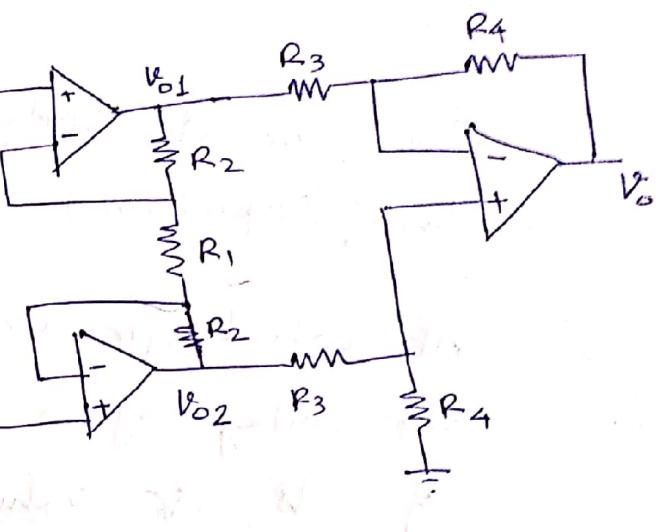
$$V_{CE} = 10 - I_C \left[R_C + \left(\frac{\beta+1}{\beta} \right) R_E \right]$$

$$= 10 - 1.51 I_C$$



Q)

For the instrumentation amplifier shown,



if the circuit parameters are:

$$R_1 = 10 \text{ k}\Omega \text{ and } R_2 = 40 \text{ k}\Omega,$$

$$R_3 = 40 \text{ k}\Omega \text{ and } R_4 = 120 \text{ k}\Omega$$

$$R_f = 120 \text{ k}\Omega.$$

Determine V_{o1} , V_{o2} , V_o and the current in R_1 for

$$V_{i1} = 1.2 - 0.08 \sin \omega t \text{ (V)} \text{ and } V_{i2} = 1.2 + 0.08 \sin \omega t$$

Ans:

$$i_1 = \frac{V_{i1} - V_{i2}}{R_1} = \frac{1.2 - 0.08 \sin \omega t - 1.2 + 0.08 \sin \omega t}{10 \text{ k}}$$

$$= -16 \mu\text{A} - 16 \sin \omega t \mu\text{A}$$

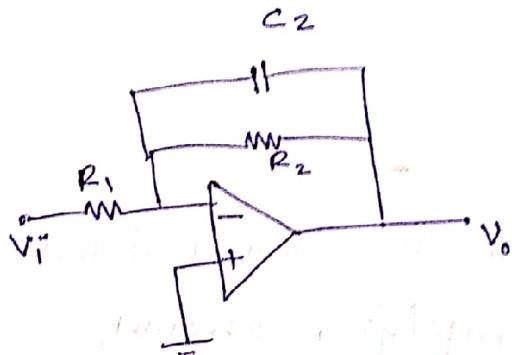
$$\begin{aligned} V_{o1} &= (1.2 - 0.08 \sin \omega t) + (-0.016 \sin \omega t) \times 40 \\ &= 1.2 - 0.72 \sin \omega t \text{ (V)} \end{aligned}$$

$$\begin{aligned} V_{o2} &= (1.2 + 0.08 \sin \omega t) - (-0.016 \sin \omega t) \times 40 \\ &= 1.2 + 0.72 \sin \omega t \text{ (V)} \end{aligned}$$

$$\begin{aligned} V_o &= \frac{R_4}{R_3} (V_{o2} - V_{o1}) = \frac{120}{40} \times 2 \times 0.72 \sin \omega t \\ &= 4.32 \sin \omega t \text{ (V)} \end{aligned}$$

2)

Find the dc voltage gain? (expression)



At what frequency is the magnitude of the ~~voltage~~ voltage gain a factor of $\sqrt{2}$ less than that of its dc value? (ie, its -3 dB frequency).

Ans:

$$\text{gain} = \frac{-Z_2}{R_1}; \quad Z_2 = R_2 \parallel X_{C_2} = \frac{R_2 \cdot \frac{1}{j\omega C_2}}{R_2 + \frac{1}{j\omega C_2}}$$

$$\Rightarrow Z_2 = \frac{R_2}{1 + j\omega R_2 C_2}$$

$$\Rightarrow A_V = \frac{-R_2}{R_1} \cdot \frac{1}{1 + j\omega R_2 C_2}$$

$$\text{At DC, } \omega = 0 \Rightarrow A_V = \frac{-R_2}{R_1}$$

$$|A_V| = \frac{R_2}{R_1} \cdot \frac{1}{\sqrt{1 + (\omega R_2 C_2)^2}}$$

$$1 + (\omega R_2 C_2)^2 = 2 \Rightarrow \omega = \frac{1}{\sqrt{\omega R_2 C_2}} \Rightarrow f = \frac{1}{2\pi R_2 C_2}$$