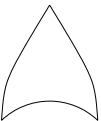


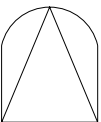
### **AND gate**

All of the inputs must occur for the output to occur



### **OR gate**

One or more of the input events must occur for the output to occur



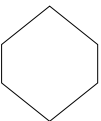
### **PRIORITY AND gate**

The output occurs if the input events occur in a specific sequence which is described in a conditioning event.



### **EXCLUSIVE OR gate**

The output occurs if exactly one of the input events occurs.



### **INHIBIT gate**

The output occurs if the single input event occurs in the presence of the enabling condition which is described in the conditioning event.