

OBJECTIVE

Graduate Architecture Intern at Hillsboro or Santa Clara

EDUCATION

- **Portland State University, Portland, OR** - / 4.0
M.Sc Electrical and Computer Engineering Fall 2021 – Spring 2023
 - Relevant Courses -
 - * ECE 581 - ASIC Modelling and Synthesis (Dr. Xaiyou Song) Fall 2021
 - * ECE 585 - Microprocessor System Design (Mark Faust) Fall 2021
 - * (tentative) ECE 586 - Computer Architecture (Mark Faust) Winter 2022
 - * (tentative) ECE 587 - Advanced CompArch I (Yuchen Huang) Spring 2022
 - * (tentative) ECE 588 - Advanced CompArch II (Yuchen Huang) Fall 2022
- **College of Engineering, Pune, Maharashtra, India** 3.2 / 4.0
B.Tech Electronics and Telecommunication Engineering May 2015 – June 2019

EXPERIENCE

- **Tejas Networks** Mumbai, Maharashtra, India
Research and Development Engineer August 2019 - August 2021
 - Integrated a new network switching product into Tejas ecosystem.
 - Lead Engineer on implementation of feature request by Tejas network's clients.
 - Train new recruits to the team and enable them to contribute meaningfully.

Technologies: C/C++, Bash, SQL, and Python.
Theory: Device Drivers, VLAN traffic filtering, DHCP, and Bandwidth Management.
- **DOT Sys Technologies** Mumbai, Maharashtra, India
Design Intern May 2018 - August 2018
 - Programmable Battery Charger -

Technologies: Arduino.
Theory: Transistor theory and Analog Circuits.
- **Eduvance** Mumabai, Maharashtra, India
Intern (part-time to full-time) May 2017 - January 2020
 - Developed a Smart Paper Tracking System.
 - Presented my B.Tech Final Project.

Technologies: Python, Cypress PSOC4.
Theory: Internet of Things, Bluetooth Low Energy, and Communication Protocols.

PROJECTS

- hamming_code: Asynchronous Hamming Encoder and Decoder in System Verilog.
- i2c: I2C master and slave implemented in Verilog, currently re-writing code in to System Verilog.
- ffind: Small wrapper for find command in linux to make find accept grep-like arguments.
- Music-Player-GO: Feature Contributions to open source Music Player app.

PERSONAL PUBLICATIONS

- Modified MD5 Algorithm for Low-End IoT Edge Devices. Viraj Khatri, Dr. Vanita Agarwal. [ICCCNT2019](#)

OTHER PROGRAMMING TOOLS

- **Neural Network Frameworks:** Keras, Tensorflow.