Nano CH340, R3 (Arduino-compatible), schematics

rev.1 (corrected pins 2&3 on CH340) Copyright 2016, actrl.cz VIN TxD RxD GND RST RST GND 5V D2 A7 D3 A6 D4 A5 D5 **A4** A3 D₆ D7 A2 D8 A1 D9 A0 SD101CWS D10 REF D11 3V3 D13 D12 LM1117-5.0 OUT GND MISO 5V0 1k 10k SCK MOSI 100n 10u RST GND POW PD3 PD2 1 32 PD4 PD1(TxD) 2 31 GND PD0(RxD) 3 30 RESET VCC 4 29 GND PC5(ADC5) 16MHz 5 28 VCC PC4(ADC4) HOP 6 27 XT1 PC3(ADC3) 7 26 1k 1k XT2 PC2(ADC2) 8 25 GND VCC PD5 PC1(ADC1) 9 TxD RS232 PD6 PC0(ADC0) 2 15 10 23 RxD PD7 RTS ADC7 3 14 11 22 AGND V33 DTR 13_{100n} PB0 4 12 21 D+ DCD PB1 AREF 5 12 13 20 D-RI PB2 ADC6 6 11 14 19 Xi DSR PB3(MOSI) AVCC 7 10 15 18 101 Xo CTS PB4(MISO) PB5(SCK)

Reset

16

ATMEGA 328P-32TQFP

100n

8

100n

* - not used

CH340 (SOP-16)

10u 100n