

Details and examples for the Adaptive PEBS capability follow below.

### 18.9.2.1 Adaptive\_Record Counter Control

- IA32\_PERFEVTSELx.Adaptive\_Record[34]: If this bit is set and IA32\_PEBS\_ENABLE.PEBS\_EN\_PMCx is set for the corresponding GP counter, an overflow of PMCx results in generation of an adaptive PEBS record with state information based on the selections made in MSR\_PEBS\_DATA\_CFG. If this bit is not set, a basic record is generated.

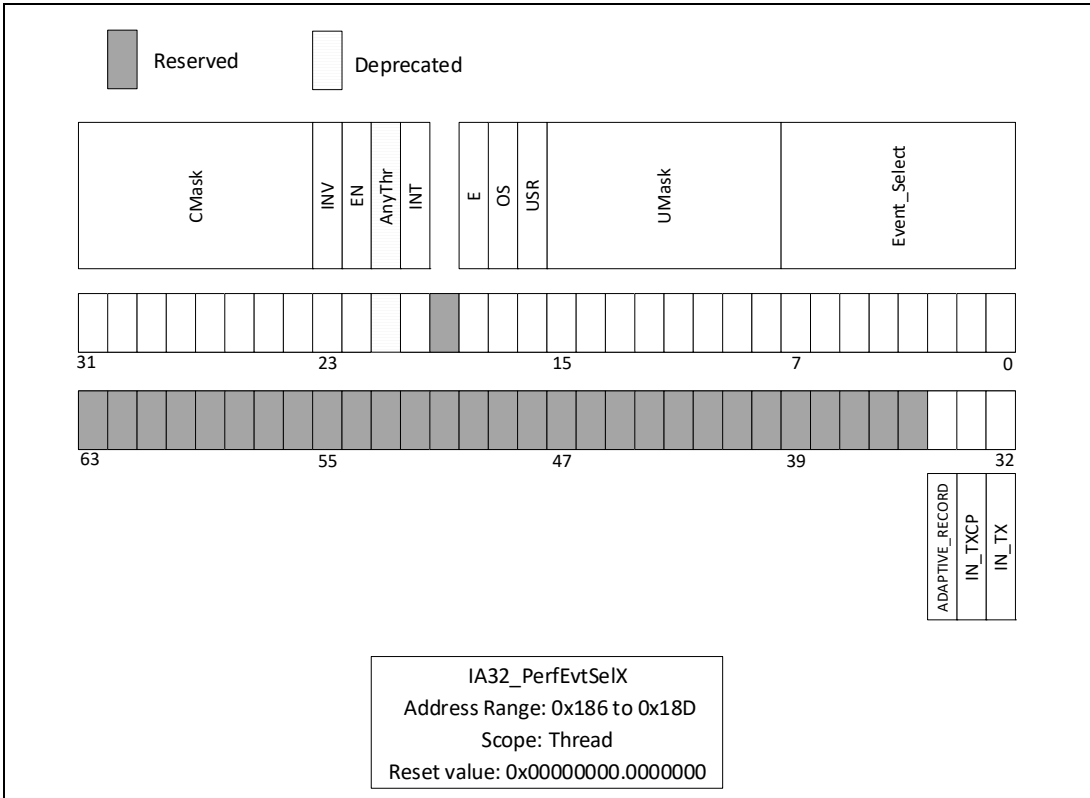


Figure 18-66. Layout of IA32\_PerfEvtSelX MSR Supporting Adaptive PEBS

- IA32\_FIXED\_CTR\_CTRL.FCx\_Adaptive\_Record: If this bit is set and IA32\_PEBS\_ENABLE.PEBS\_EN\_FIXEDx is set for the corresponding Fixed counter, an overflow of FixedCtrx results in generation of an adaptive PEBS record with state information based on the selections made in MSR\_PEBS\_DATA\_CFG. If this bit is not set, a basic record is generated.