

DUE IN CLASS

STUDENTS IDENTIFICATION:

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2.1 Simple execution, without data forwarding techniques

e)

Clock cycles	18	Instructions	6	Average CPI	3.0
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f)

Clock cycles	174	Stalls: - Data	101
Instructions	61	- Structural	0
Average CPI	2.852	- Branch Taken	8

- g) The branch policy adopted is Predict Not Taken: whenever a branch instruction is executed, it has already fetched the next sequential instruction. If the branch is not taken, the sequential instruction continues to be executed. However, if the branch is taken, the execution of the sequential instruction is cancelled and proceeds to execute the instruction with the address present in the branch instruction.

2.2 Application of data forwarding techniques

c)

Clock cycles	136	Stalls: - Data	63
Instructions	61	- Structural	9
Average CPI	2.230	- Branch Taken	8

d)

$$\text{Speedup} = \frac{\text{clock cycles}_{\text{old}} \times \text{clock rate}}{\text{clock cycles}_{\text{new}} \times \text{clock rate}} = \frac{174}{136} \approx 1.2794 //$$

2.3 Source code optimization: minimization of data and structural hazards

- a) Attach a copy of the new assembly program. On dedicated page. (Further) →

c)

Clock cycles	118	Stalls: - Data	36
Instructions	61	- Structural	9
Average CPI	1.934	- Branch Taken	8

d)

$$\text{Speedup} = \frac{174}{118} \approx 1.4746 //$$

2.4 Source code optimization: loop unrolling

a) Attach a copy of the new assembly program. *On dedicated page. (further) →*

c)

Clock cycles	100
Instructions	43
Average CPI	2.326

Stalls: - Data	42
- Structural	9
- Branch Taken	2

d)

$$\text{Speedup} = \frac{174}{100} = 1.74 //$$

2.5 Source code optimization: branch delay slot

a) Attach a copy of the new assembly program. *On dedicated page. (further) →*

d)

Clock cycles	101
Instructions	61
Average CPI	1.656

Stalls: - Data	27
- Structural	9
- Branch Taken	0

e)

$$\text{Speedup} = \frac{174}{101} \approx 1.7228 //$$

(2.2)

Table 1: Pipeline time diagram, with data forwarding techniques.

[illegible]

Table 3: Pipeline time diagram: usage of loop unrolling minimization techniques to reduce the control hazards.

[illegible]

Table 4: Pipeline time diagram: usage of branch delay slot techniques to reduce the control hazards.

INSTRUCTIONS	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40					
1 $Op, \$r2, O(\$r1)$	F	D	D	D	X	M	W																																						
2 $addi \$5, \$5, 1$		F	F	F	F	D	X	M	W																																				
3 $mul \$r2, \$r2, \$r1$						F	D	X	X	X	X	X	X	M	W																														
4 $addi \$1, \$1, 8$							F	D	X	M	W																																		
5 $bne \$6, \$5, loop$								F	D	X	M	W																																	
6 $adda $0, $0, $r2$									F	F	D	X	X	X	X	M	W																												
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(29) **Table 5:** Pipeline time diagram, without data forwarding techniques.

Table 5: Pipeline time diagram, without data forwarding techniques.

	INSTRUCTIONS	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	
1	lw \$t2, 0(\$t1)	F	D	X	M	W														F	D	X	M	W																		
2	dread \$t2, \$t2, \$t4	F	D	D	D	X	X	X	X	X	X	M	W																													
3	dread \$t4, \$t4, \$t2	F	F	F	D	D	D	D	D	D	D	D	X	M	W																											
4	daddi \$t5, \$t5, 1				F	F	F	F	F	F	F	F	F	D	X	M	W																									
5	daddi \$t1, \$t1, 8															F	D	X	M	W																						
6	bne \$t6, \$t5, loop															F	D	X	M	W																						
7	sw \$t4, mult(\$t0)																F	D	X	M	W																					
8																																										
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2.3 a)

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1      .data
2  A:   .word 1, 3, 1, 6, 4
3      .word 2, 4, 3, 9, 5
4  mult: .word 0
5
6      .code
7  daddi $1, $0, A      ; *A[0]
8  daddi $5, $0, 1      ; $5 = 1 ;; i
9  daddi $6, $0, 10     ; $6 = N ;; N = 10
10 lw    $9, 0($1)      ; $9 = A[0] ;; mult
11 daddi  $1, $1, 8      ;
12
13 loop: lw    $12, 0($1) ; $12 = A[i]
14       daddi  $5, $5, 1 ; i++
15       dmul   $12, $12, $9 ; $12 = $12*$9 ;; $12 = A[i]*mult
16       daddi  $1, $1, 8 ;
17       dadd   $9, $9, $12 ; $9 = $9 + $12 ;; mult = mult + A[i]*mult
18       bne    $6, $5, loop ; Exit loop if i == N
19
20       sw     $9, mult($0) ; Store result
21       halt
22
23 ;; Expected result: mult = f6180 (hex), 1008000 (dec)
24

```

2.4 a)

```

1      .data
2  A:   .word 1, 3, 1, 6, 4
3      .word 2, 4, 3, 9, 5
4  mult: .word 0
5
6      .code
7  daddi $1, $0, A      ; *A[0]
8  daddi $5, $0, 1      ; $5 = 1 ;; i
9  daddi $6, $0, 10     ; $6 = N ;; N = 10
10 lw    $9, 0($1)      ; $9 = A[0] ;; mult
11 daddi  $1, $1, 8      ;
12
13 loop: lw    $12, 0($1) ; $12 = A[i]
14       lw    $13, 8($1) ; $13 = A[i+1]
15
16       dmul   $12, $12, $9 ; $12 = $12*$9 ;; $12 = A[i]*mult
17       lw    $14, 16($1) ; $14 = A[i+2]
18       dadd   $9, $9, $12 ; $9 = $9 + $12 ;; mult = mult + A[i]*mult
19
20       daddi  $5, $5, 3 ; i+=3
21       dmul   $13, $13, $9 ; $13 = $13*$9 ;; $13 = A[i+1]*mult
22       dadd   $9, $9, $13 ; $9 = $9 + $13 ;; mult = mult + A[i+1]*mult
23
24       daddi  $1, $1, 24 ;
25       dmul   $14, $14, $9 ; $14 = $14*$9 ;; $14 = A[i+2]*mult
26       dadd   $9, $9, $14 ; $9 = $9 + $14 ;; mult = mult + A[i+2]*mult
27
28       bne    $6, $5, loop ; Exit loop if i == N
29
30       sw     $9, mult($0) ; Store result
31       halt
32
33 ;; Expected result: mult = f6180 (hex), 1008000 (dec)
34

```

2.5 a)

```

1      .data
2  A:   .word 1, 3, 1, 6, 4
3      .word 2, 4, 3, 9, 5
4  mult: .word 0
5
6      .code
7  daddi $1, $0, A      ; *A[0]
8  daddi $5, $0, 1      ; $5 = 1 ;; i
9  daddi $6, $0, 10     ; $6 = N ;; N = 10
10 lw    $9, 0($1)      ; $9 = A[0] ;; mult
11 daddi  $1, $1, 8      ;
12
13 loop: lw    $12, 0($1) ; $12 = A[i]
14       daddi  $5, $5, 1 ; i++
15       dmul   $12, $12, $9 ; $12 = $12*$9 ;; $12 = A[i]*mult
16       daddi  $1, $1, 8 ;
17       bne    $6, $5, loop ; Exit loop if i == N
18       dadd   $9, $9, $12 ; $9 = $9 + $12 ;; mult = mult + A[i]*mult
19
20       sw     $9, mult($0) ; Store result
21       halt
22
23 ;; Expected result: mult = f6180 (hex), 1008000 (dec)
24

```