

DIGITAL_CLOCK_V1

10 / 01 /2025

REALESED 07 – 09 - 2025

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DESIGN CONSIDERATIONS –


DESIGN NOTE
Example text for informational design notes.

DESIGN NOTE
Example text for debug notes.

DESIGN NOTE
Example text for critical layout guidelines.

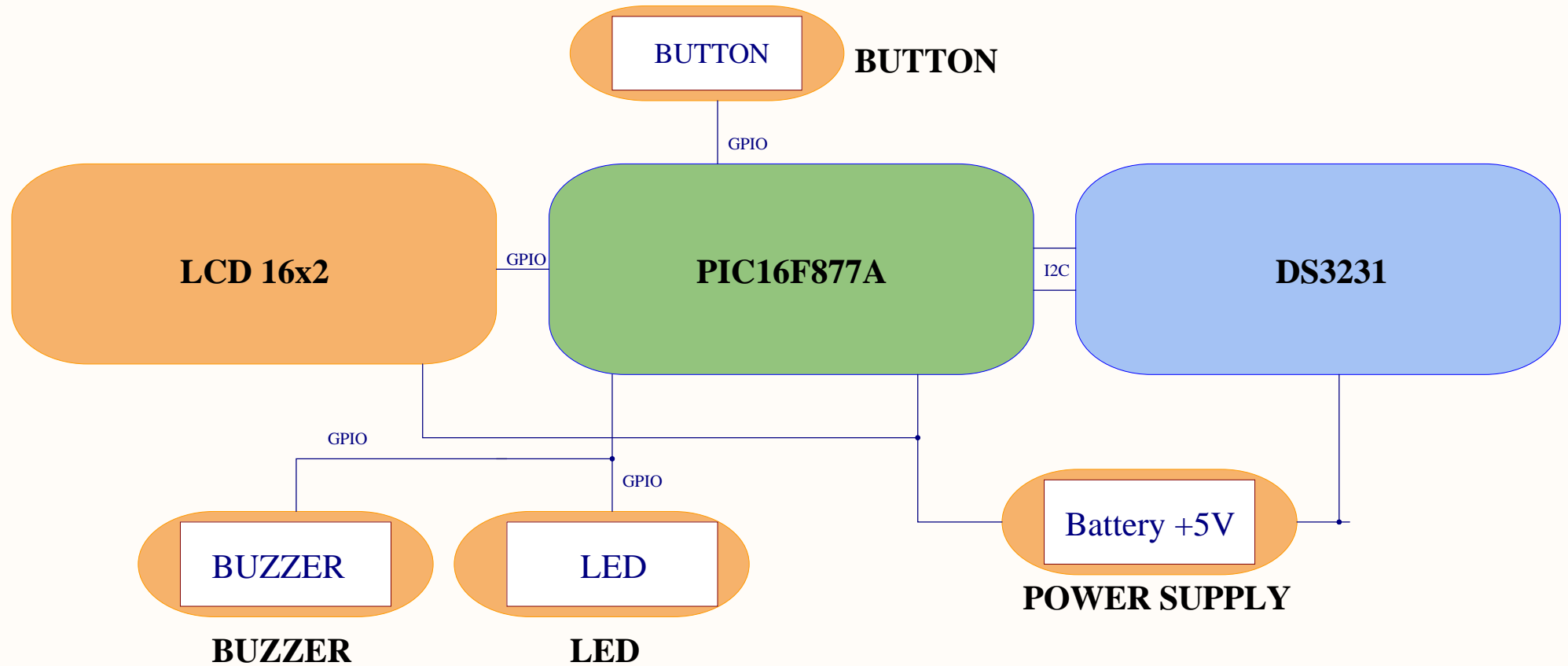
DESIGN NOTE
Example text for cautionary design notes.

DESIGN NOTE
Example text for critical design notes.

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DIGITAL_CLOCK_V1

(Block Diagram)



Project: **Digital_Clock.PrjPcb**

Page Contents: **[02] - BLOCK DIAGRAM.SchDoc**

Revision: **V1**

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Date: **10/01/2025**

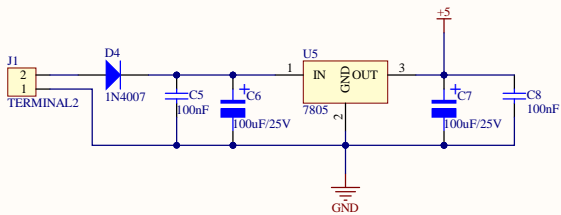
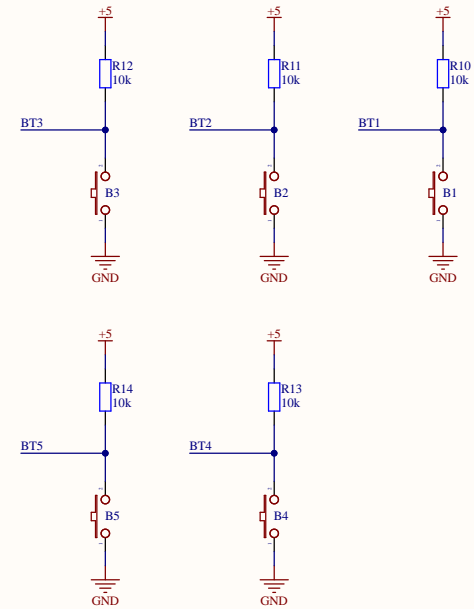
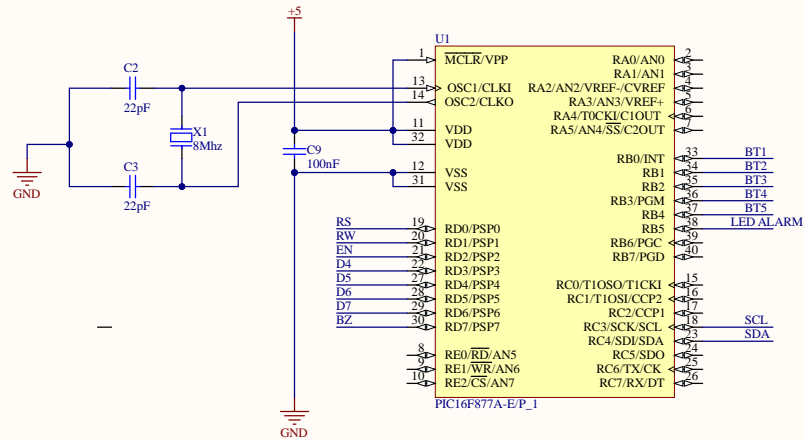
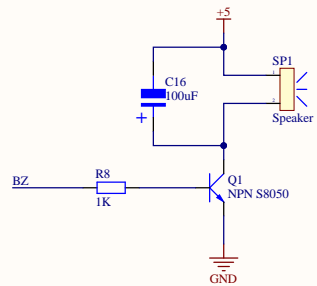
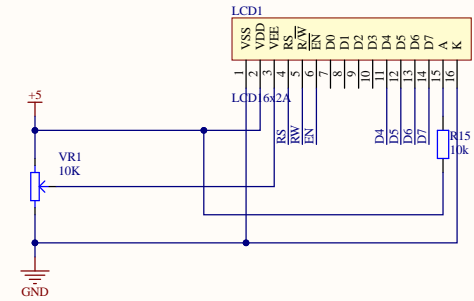
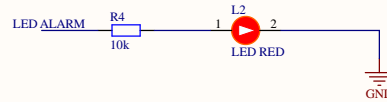
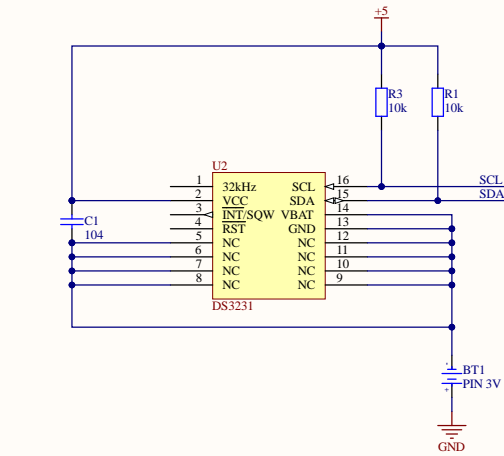
Size: **A3**

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Circuit Diagram

Project: **Digital_Clock.PrjPcb**

Page Contents: [03] Circuit Diagram.SchDoc

Revision: V3

Design: **Nhóm 1**

Date: 28/10/2025

Size: A3

Document number: 3

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REVISION HISTORY

09/30/2025 (GROUP 1) - Initial release. Board

10/01/2025 (GROUP 1) - Added block diagram

10/01/2025 (GROUP 1) - Added Circuit diagram

Project: **Digital_Clock.PrjPcb**

Page Contents: **[4] REVISION HISTORY.SchDoc**

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