

# Retriggerable Tasks in NI-DAQmx

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The National Instruments Getting Started with NI-DAQmx Series is aimed at helping you learn NI-DAQmx programming fundamentals. Through video and text tutorials, this series will take you from verifying your device's operation in Measurement & Automation Explorer (MAX) to programming data acquisition applications using LabVIEW. It is intended for both the beginner who wants to learn how to use the DAQ Assistant, as well as the experienced user who wishes to take advantage of advanced NI-DAQmx functionality.

This document is part of the  
**Getting Started with  
NI-DAQmx Series**

### 1. Overview

With NI-DAQmx it is possible to define hardware triggered analog, digital, and counter tasks. Additionally, X Series multifunction data acquisition devices (DAQ) natively support retriggerable tasks. A retriggerable task will acquire or generate a finite number of samples each time a digital trigger signal is received. An example of this can be seen in Figure 1. For each edge of the trigger signal, there will be four pulses of the sample clock (the device will acquire or generate four samples). This document discusses how to configure retriggerable tasks with X Series DAQ devices, as well as how to achieve similar functionality with previous generation DAQ devices using onboard counters.

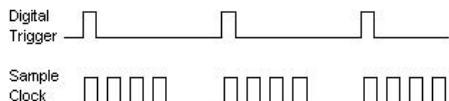


Figure 1: Retriggered Sample Clock

### 2. Hardware and Software Requirements

Only National Instruments X Series multifunction DAQ devices (63xx) natively support retriggerable tasks. However, most National Instruments multifunction DAQ devices, such as M Series (62xx), can be used to implement similar functionality. To determine if your device supports this functionality, refer to the product manual to verify that it has at least two onboard counters and supports hardware triggering.

The NI-DAQmx API is available for LabVIEW, along with text-based development environments such as LabWindows/CVI, .NET, and C++. Refer to the NI-DAQ Readme to ensure that you have a version of NI-DAQmx that supports your device.

### 3. Application Architecture

To create a retriggerable Task with an X Series device, use the Start:More:Retriggerable property shown in Figure 2. This property specifies whether a finite task resets and waits for another hardware start trigger after the task completes. When you set this property to TRUE, the device performs a finite acquisition or generation each time the start trigger occurs until the task stops. The device ignores a trigger if it is in the process of acquiring or generating signals.

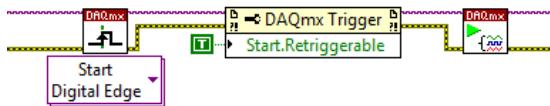


Figure 2: Retriggerable Task Creation With an X Series DAQ Device

To achieve retriggerable functionality with previous generation DAQ devices, the hardware start trigger must be used to generate finite digital pulses using the two onboard counters. These pulses will internally be used as the clock source for the analog input task. Figure 3 illustrates this application architecture.

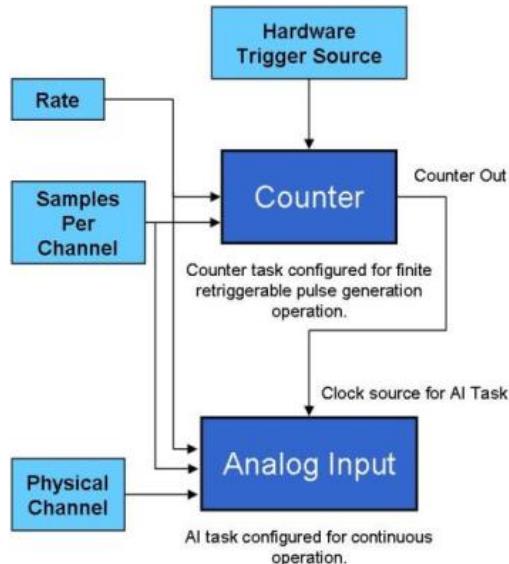


Figure 3: Retriggerable Analog Acquisition Using Onboard Counters

#### 4. Shipping Example

NI-DAQmx versions 9.5 and earlier ship with LabVIEW examples that demonstrate using retriggerable analog input and output tasks with X Series DAQ devices. For previous generation DAQ devices, the example Multi-Function-Ctr Retrigg Pulse Train Generation for AI Sample Clock.vi demonstrates retriggerable functionality using counters. This example can be accessed through the LabVIEW Example Finder (Help»Find Examples»Hardware Input and Output»DAQmx»Synchronization»Multi-function).

NI-DAQmx versions 9.5.5 and newer do not include retriggerable examples for LabVIEW 2012. The previous examples are available in the Related Links section along with other useful information.

#### 5. Related Links

[NI Community: Index of DAQmx Shipping Examples \(2011 and previous\)](#)

[KnowledgeBase 2P5EJFUV: How Can I Set the Counters on My E Series DAQ Card to Achieve a Re-Triggerable Analog Input Acquisition?](#)

[KnowledgeBase 1VQ9J3LL: How Do I Configure an Analog Input Acquisition with Multiple Triggers on an E Series DAQ Board?](#)

[White Paper: Retriggerable AI Using Retriggerable Counter \(LabVIEW, CVI, ANSI C, C#.NET\)](#)

[White Paper: Retriggerable Pulse Train Generation \(LabVIEW, CVI, ANSI C, VB.NET, C#.NET\)](#)

[White Paper: Retriggerable Digital Input \(LabVIEW\)](#)

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