Basic Information

Name	:	MAYANK SINGH
Course	:	PG-DAC,February-2019
Address	:	341, Narsi Vihar, Sonkh Road,Mathura,Uttar Pradesh

CCPP ID : Not Assigned

Work Details

Company Name	Designation	IT Related	From	To	Nature of Work
Samsung India Electronics Pvt. Ltd.	Senior Assistant Engineer	No	02/06/2017	15/02/2019	Production Planning and Production Progress Management.
Samsung India Electronics Pvt. Ltd.	Graduate Engineer Trainee	No	02/06/2016	01/06/2017	Production Material Management and OEM.

Academic Details

Level	Stream	Institute	Board/University	Passing Year	Degree %	Division
BTech	Electronics & Communication	GLA University, Mathura, Uttar Pradesh	GLA University , Mathura , Uttar Pradesh	2016	60.8 %	I
XII	PCM and Computer Science	St. Mary's Convent School, Agra, Uttar Pradesh	Indian School Certificate Examination	2012	75.33 %	I
X	General	St. Mary's Convent School, Agra, Uttar Pradesh	Indian Certificate of Secondary Education	2010	87.57 %	I

Academic Projects

Title	:	**Online Parking Booking System**
Platform	:	J2EE
Duration	:	1 Month
Description	:	The Web application was designed for a hassle free online parking space booking prior to time. Technologies used for front end and back end are Angular and Spring Boot with JPA Repository respectively. The entire Architecture is based on the 3 Tier model i.e.(Presentation, Service, DAO).

Title	:	**CNTFET-Based Design of Ternary Logic Gates and Arithmetic Circuits**
Platform	:	VLSI Design
Duration	:	6 Months
Description	:	In this project, we compared the performance of different CNTFET based binary and ternary logic gates and arithmetic circuits with their respective CMOS based binary and ternary logic gates and arithmetic circuits.

Personal Information

Date of Birth	:	06-09-1995	Gender	:	Male
Nationality	:	Indian	Passport	:	Available

I hereby declare that the information given above is true to the best of my Information knowledge belief.

Date :

Signature :