

# Rudi: Calculadora Rudimentar

Projetando um caminho de dados

Prof. Roberto de Matos

[roberto.matos@ifsc.edu.br](mailto:roberto.matos@ifsc.edu.br)



**INSTITUTO  
FEDERAL**

Santa Catarina

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Câmpus  
São José

# Objetivo

- Construir circuitos utilizando blocos de construção digital.
- Introdução ao caminho de dados e à unidade de controle.



# Implementação paralela

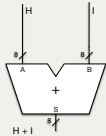
- Implementar um hardware que faça a seguinte operação:

$$Y = H + I + J + K + L - N - O$$

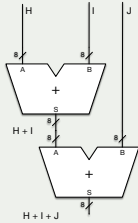
- Considere todas as variáveis de 8 bits.



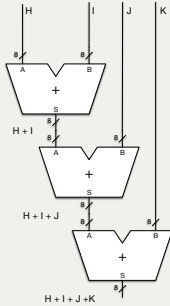
Implementando:  $Y = H + I + J + K + L - N - O$



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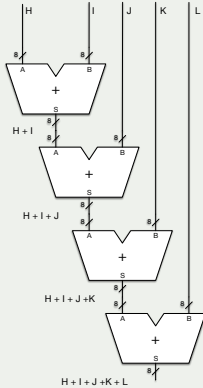


# Implementando: $Y = H + I + J + K + L - N - O$

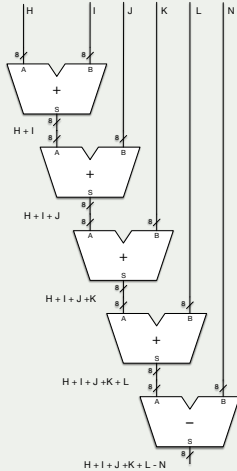




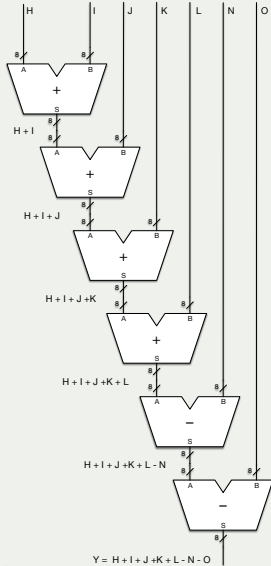
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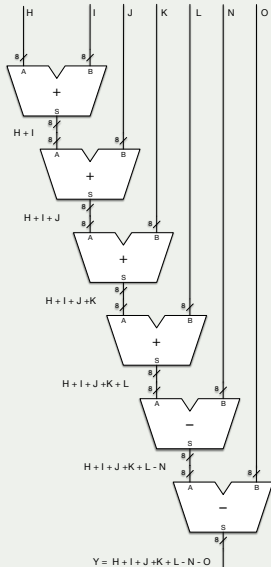
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- Rápido, mas grande consumo de lógica.
- Modificação da expressão implica em modificação do hardware.
- Entrada de todos os operandos de uma vez.



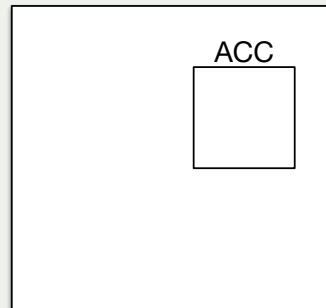
## Como melhorar?

- Reutilizar ao máximo o hardware.
- Executar uma operação por vez.
- Exemplo: Calculadora simples.



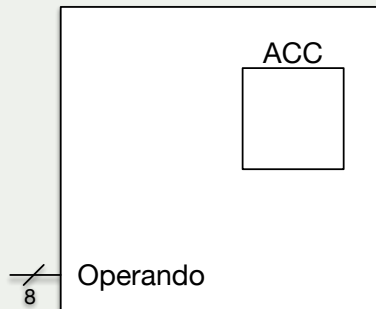
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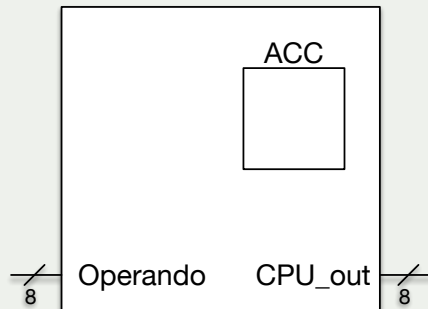
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- Exemplo: Calculadora simples.
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  - $ACC \leftarrow Operando$



# Como melhorar?

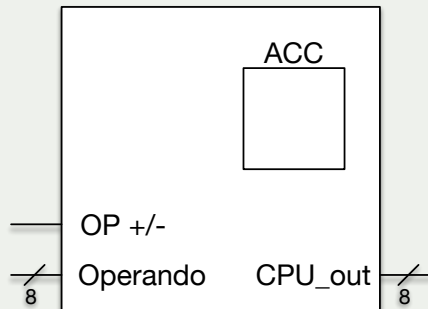
- Reutilizar ao máximo o hardware.
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- Comandos:
  - $ACC \leftarrow Operando$
  - $CPU\_out \leftarrow ACC$





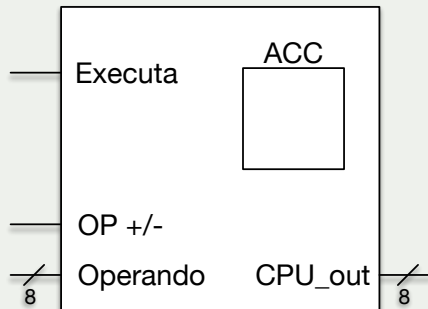
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  - $ACC \leftarrow ACC + Operando$
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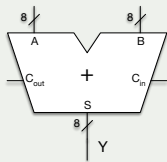
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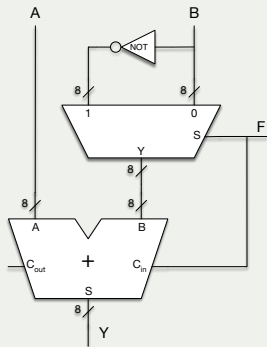


Rudi: Caminho de dados e sinais de controle

- Apenas um somador para soma e subtração:



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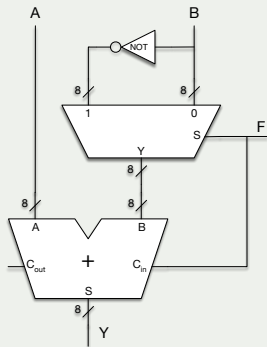


- $F = 0 \rightarrow Y = A + B$

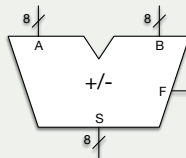
- $F = 1 \rightarrow Y = A + \overline{B} + 1 = A - B$



- Apenas um somador para soma e subtração:



- Símbolo:



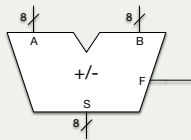
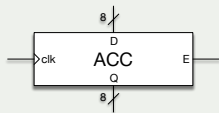
- $F = 0 \rightarrow Y = A + B$

- $F = 1 \rightarrow Y = A + \overline{B} + 1 = A - B$

# Caminho dos dados

Operando

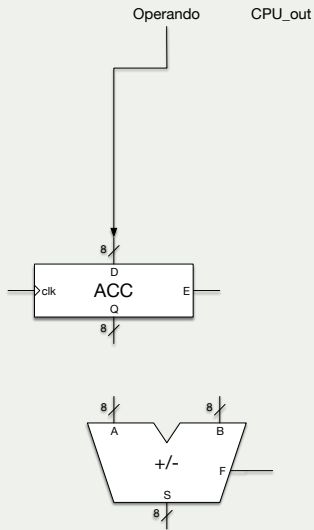
CPU\_out



- $ACC \leftarrow Operando$
- $ACC \leftarrow ACC + Operando$
- $ACC \leftarrow ACC - Operando$
- $CPU\_out \leftarrow ACC$



# Caminho dos dados

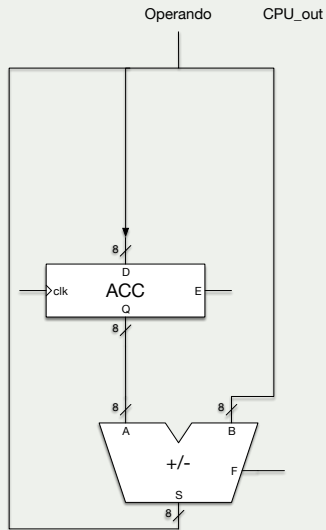


- $ACC \leftarrow Operando$
- $ACC \leftarrow ACC + Operando$
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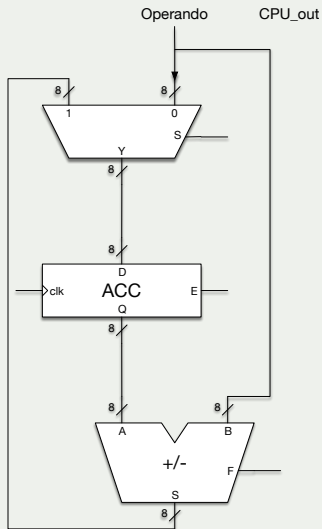
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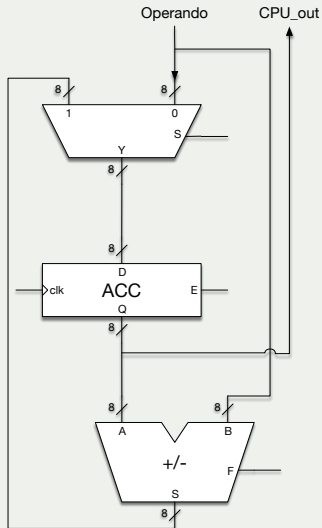
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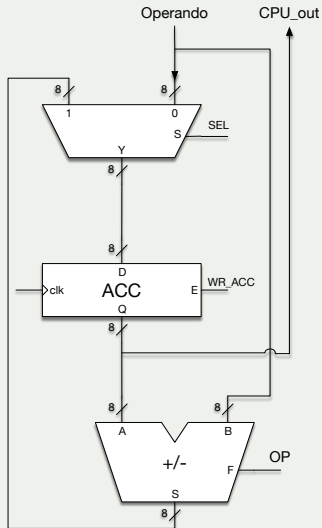
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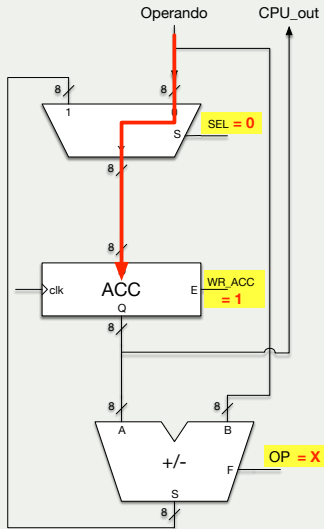
# Caminho dos dados



- Uma entrada: Operando
- Uma saída: CPU\_out
- Três sinais de controle internos:
  - SEL, WR\_ACC e OP



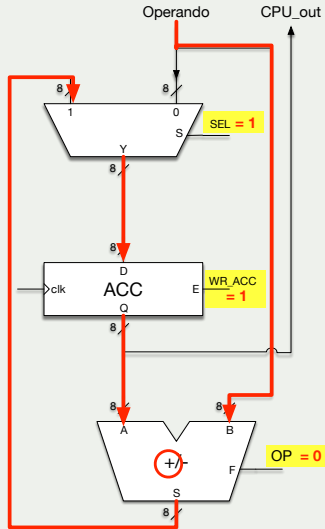
# Caminho dos dados



■  $ACC \leftarrow Operando$



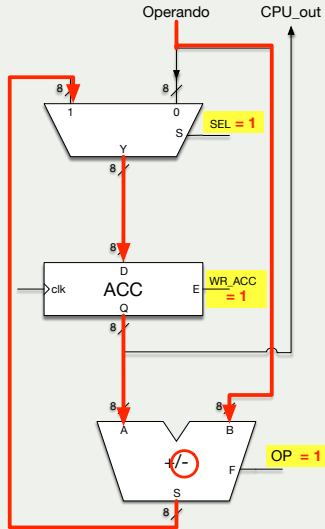
# Caminho dos dados



$$ACC \leftarrow ACC + Operando$$



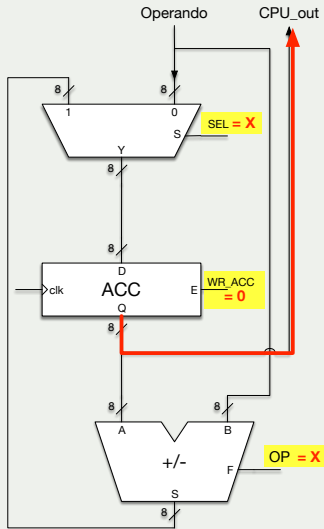
# Caminho dos dados



$$\blacksquare \text{ ACC} \leftarrow \text{ACC} - \text{Operando}$$



# Caminho dos dados

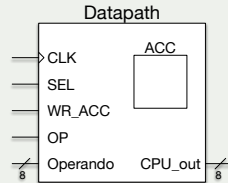
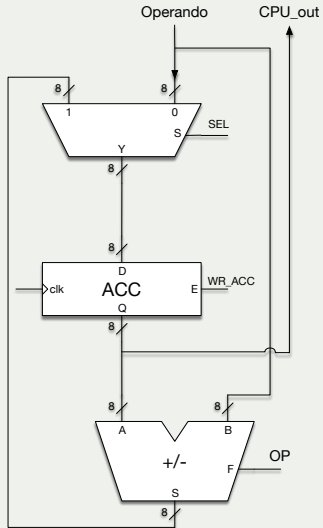


■  $CPU\_out \leftarrow ACC$





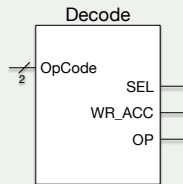
# Caminho dos dados



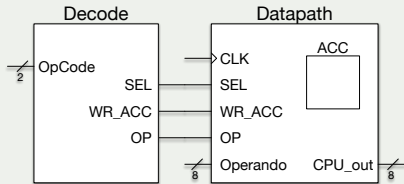
Operação	OpCode	SEL	WR_ACC	OP
$CPU_{out} \leftarrow ACC$	00	X	0	X
$ACC \leftarrow Operando$	01	0	1	X
$ACC \leftarrow ACC + Operando$	10	1	1	0
$ACC \leftarrow ACC - Operando$	11	1	1	1



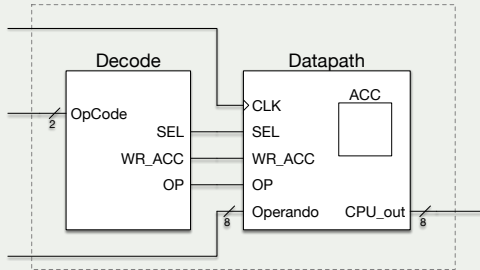
Operação	OpCode	SEL	WR_ACC	OP
$CPU\_out \leftarrow ACC$	00	X	0	X
$ACC \leftarrow Operando$	01	0	1	X
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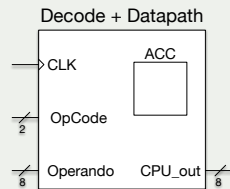
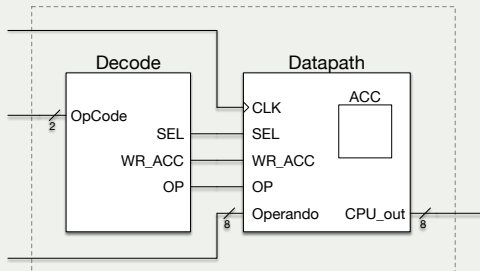
# Caminho de dados e decodificador



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- $H + I + J + K + L - N - O$

- $5 + 3 + 7 + 8 + 16 - 2 - 1$



■  $H + I + J + K + L - N - O$

■  $5 + 3 + 7 + 8 + 16 - 2 - 1$

Operação	OpCode	Operando
$ACC \leftarrow 5$	01	00000101
$ACC \leftarrow ACC + 3$	10	00000011
$ACC \leftarrow ACC + 7$	10	00000111
$ACC \leftarrow ACC + 8$	10	00001000
$ACC \leftarrow ACC + 16$	10	00010000
$ACC \leftarrow ACC - 2$	11	00000010
$ACC \leftarrow ACC - 1$	11	00000001
$CPU\_out \leftarrow ACC$	00	XXXXXXXX

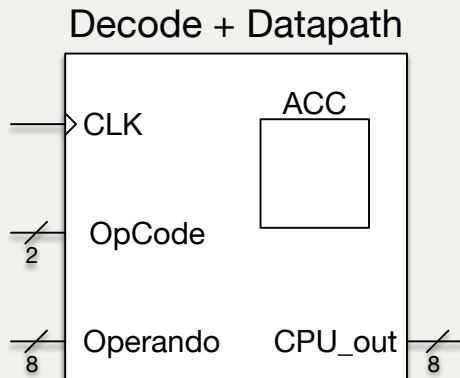




■  $H + I + J + K + L - N - 0$

■  $5 + 3 + 7 + 8 + 16 - 2 - 1$

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$ACC \leftarrow 5$	01	00000101
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$ACC \leftarrow ACC + 7$	10	00000111
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$ACC \leftarrow ACC + 16$	10	00010000
$ACC \leftarrow ACC - 2$	11	00000010
$ACC \leftarrow ACC - 1$	11	00000001
$CPU\_out \leftarrow ACC$	00	XXXXXXXX

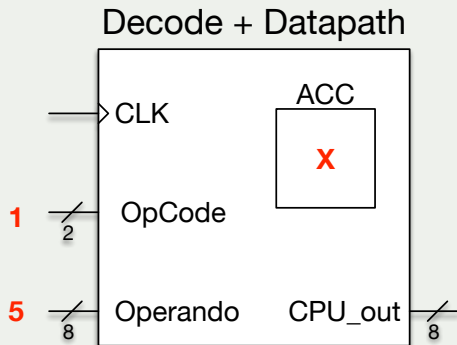


# Calculando com o Rudi

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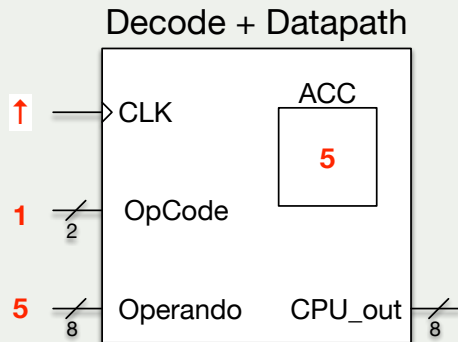


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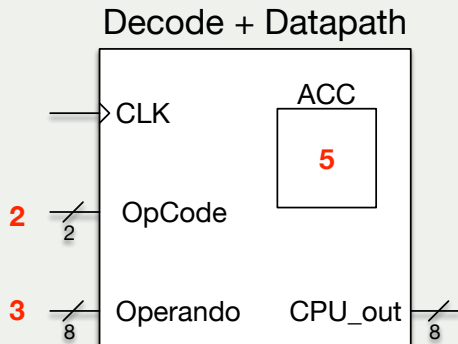


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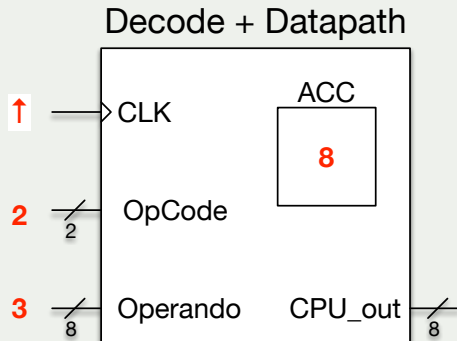


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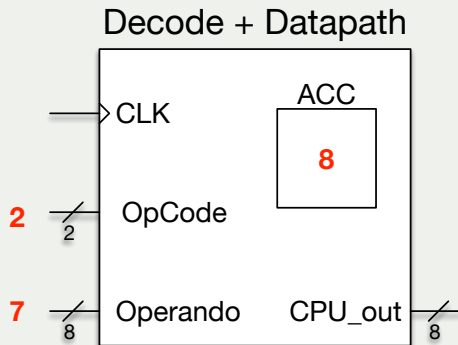


# Calculando com o Rudi

$$\blacksquare H + I + J + K + L - N - O$$

$$\blacksquare 5 + 3 + 7 + 8 + 16 - 2 - 1$$

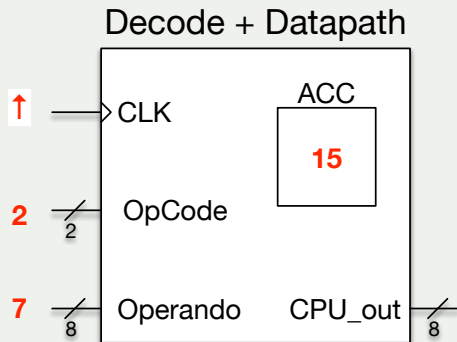
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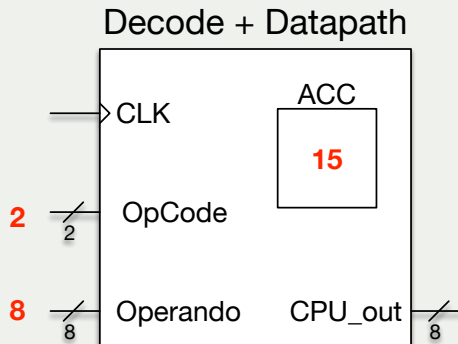


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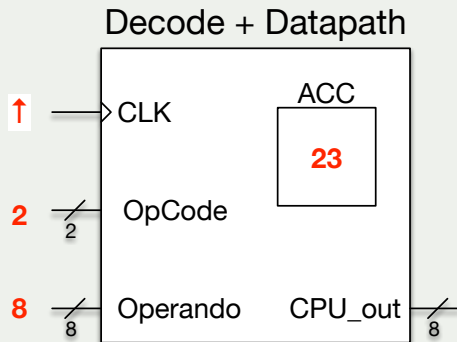


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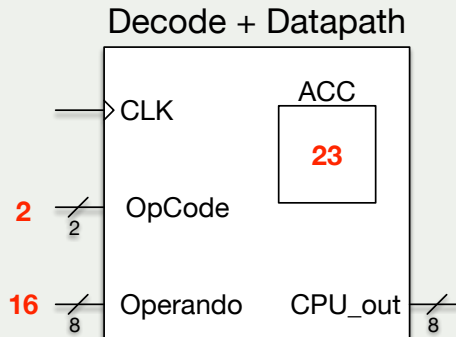


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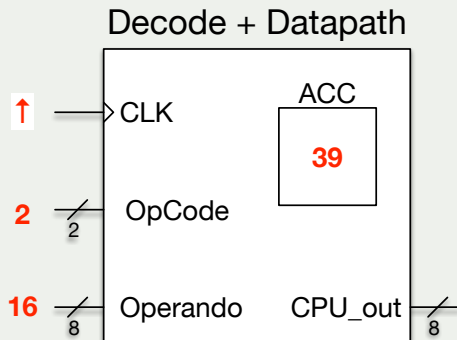


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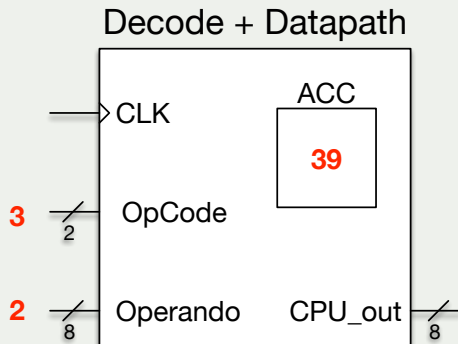


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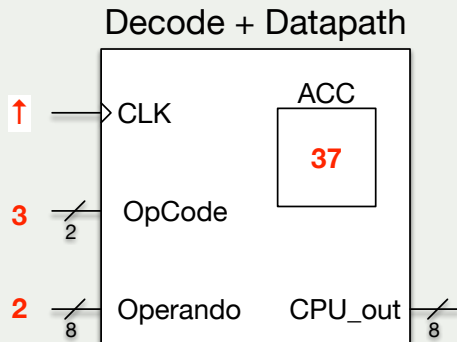


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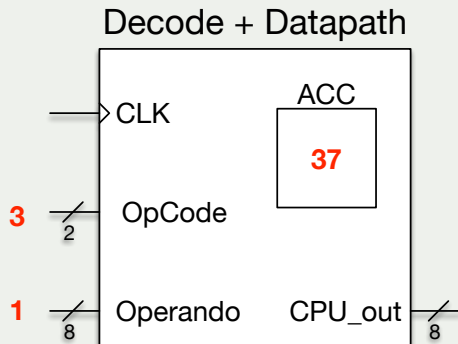


# Calculando com o Rudi

$$\blacksquare H + I + J + K + L - N - O$$

$$\blacksquare 5 + 3 + 7 + 8 + 16 - 2 - 1$$

Operação	OpCode	Operando
$ACC \leftarrow 5$	01	00000101
$ACC \leftarrow ACC + 3$	10	00000011
$ACC \leftarrow ACC + 7$	10	00000111
$ACC \leftarrow ACC + 8$	10	00001000
$ACC \leftarrow ACC + 16$	10	00010000
$ACC \leftarrow ACC - 2$	11	00000010
$ACC \leftarrow ACC - 1$	11	00000001
$CPU\_out \leftarrow ACC$	00	xxxxxxxx

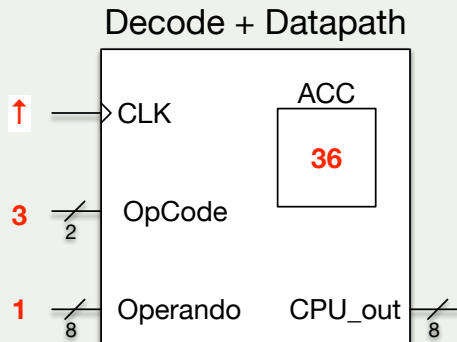


# Calculando com o Rudi

$$\blacksquare H + I + J + K + L - N - O$$

$$\blacksquare 5 + 3 + 7 + 8 + 16 - 2 - 1$$

Operação	OpCode	Operando
$ACC \leftarrow 5$	01	00000101
$ACC \leftarrow ACC + 3$	10	00000011
$ACC \leftarrow ACC + 7$	10	00000111
$ACC \leftarrow ACC + 8$	10	00001000
$ACC \leftarrow ACC + 16$	10	00010000
$ACC \leftarrow ACC - 2$	11	00000010
$ACC \leftarrow ACC - 1$	11	00000001
$CPU\_out \leftarrow ACC$	00	xxxxxxxx

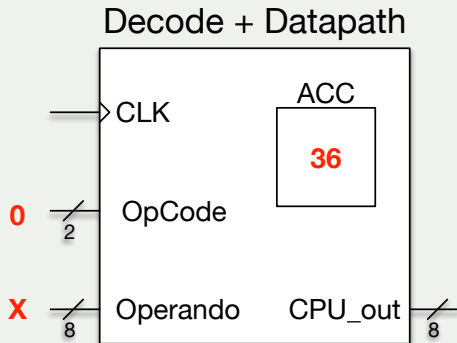


# Calculando com o Rudi

■  $H + I + J + K + L - N - 0$

■  $5 + 3 + 7 + 8 + 16 - 2 - 1$

Operação	OpCode	Operando
$ACC \leftarrow 5$	01	00000101
$ACC \leftarrow ACC + 3$	10	00000011
$ACC \leftarrow ACC + 7$	10	00000111
$ACC \leftarrow ACC + 8$	10	00001000
$ACC \leftarrow ACC + 16$	10	00010000
$ACC \leftarrow ACC - 2$	11	00000010
$ACC \leftarrow ACC - 1$	11	00000001
$CPU\_out \leftarrow ACC$	00	XXXXXXXX



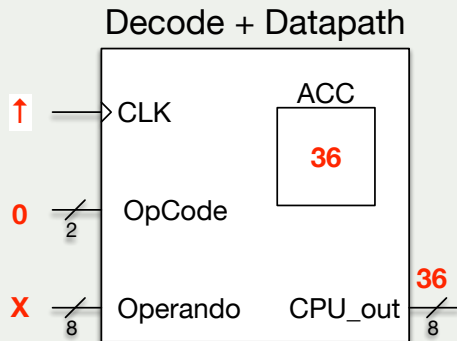


# Calculando com o Rudi

■  $H + I + J + K + L - N - 0$

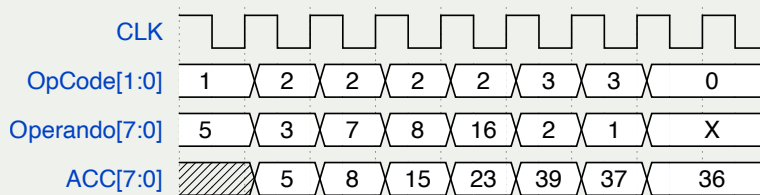
■  $5 + 3 + 7 + 8 + 16 - 2 - 1$

Operação	OpCode	Operando
$ACC \leftarrow 5$	01	00000101
$ACC \leftarrow ACC + 3$	10	00000011
$ACC \leftarrow ACC + 7$	10	00000111
$ACC \leftarrow ACC + 8$	10	00001000
$ACC \leftarrow ACC + 16$	10	00010000
$ACC \leftarrow ACC - 2$	11	00000010
$ACC \leftarrow ACC - 1$	11	00000001
$CPU\_out \leftarrow ACC$	00	XXXXXXXX



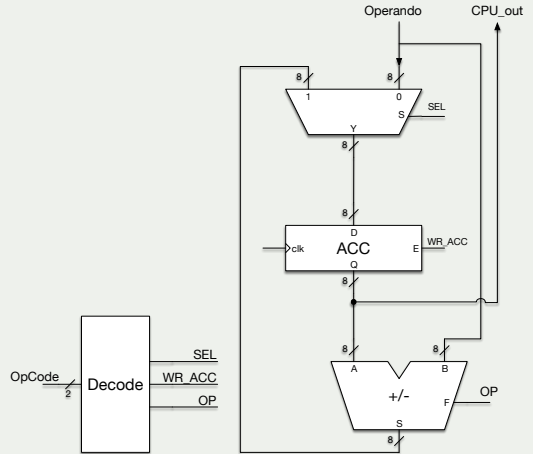
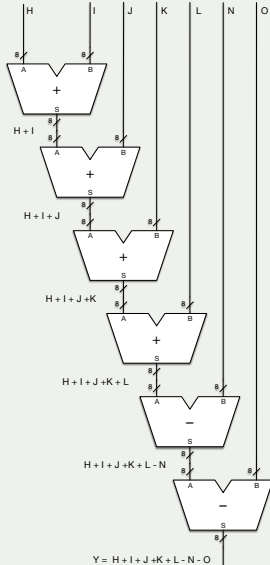
# Calculando com o Rudi – Forma de onda

Operação	OpCode	Operando
$ACC \leftarrow 5$	01	00000101
$ACC \leftarrow ACC + 3$	10	00000011
$ACC \leftarrow ACC + 7$	10	00000111
$ACC \leftarrow ACC + 8$	10	00001000
$ACC \leftarrow ACC + 16$	10	00010000
$ACC \leftarrow ACC - 2$	11	00000010
$ACC \leftarrow ACC - 1$	11	00000001
$CPU\_out \leftarrow ACC$	00	XXXXXXXX



# Comparação

# Implementação Paralela vs. Rudi



## ■ Implementação paralela:

- 4 somadores e 2 subtratores
- Entrada dos operandos em paralelo (pode ser mais rápida)
- Fixa, apenas para a expressão projetada

## ■ Rudi:

- 1 somador, 2 multiplexadores, 1 decodificado e 1 registrador
- Entrada dos operandos sequenciais
- Flexível, pode implementar qualquer expressão com soma e subtração



FIM!