

GraphVertexGates::getVerilog
String

GraphVertexGates::toVerilog

VertexUtils::gateToString

```
graph LR; A[GraphVertexGates::getVerilogString] --> C[VertexUtils::gateToString]; B[GraphVertexGates::toVerilog] --> C;
```

The diagram illustrates a mapping from two methods in the `GraphVertexGates` class to a single utility method in the `VertexUtils` class. Two white rectangular boxes on the left, representing `GraphVertexGates::getVerilogString` and `GraphVertexGates::toVerilog`, have blue arrows pointing to a single gray rectangular box on the right representing `VertexUtils::gateToString`.