

GraphVertexGates::getVerilog
String



```
graph LR; A[GraphVertexGates::getVerilogString] --> B[VertexUtils::gateToString]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is shaded gray and contains the text 'GraphVertexGates::getVerilogString' on two lines. The right box is white with a black border and contains the text 'VertexUtils::gateToString' on one line. A blue arrow points from the right side of the gray box to the left side of the white box.

VertexUtils::gateToString