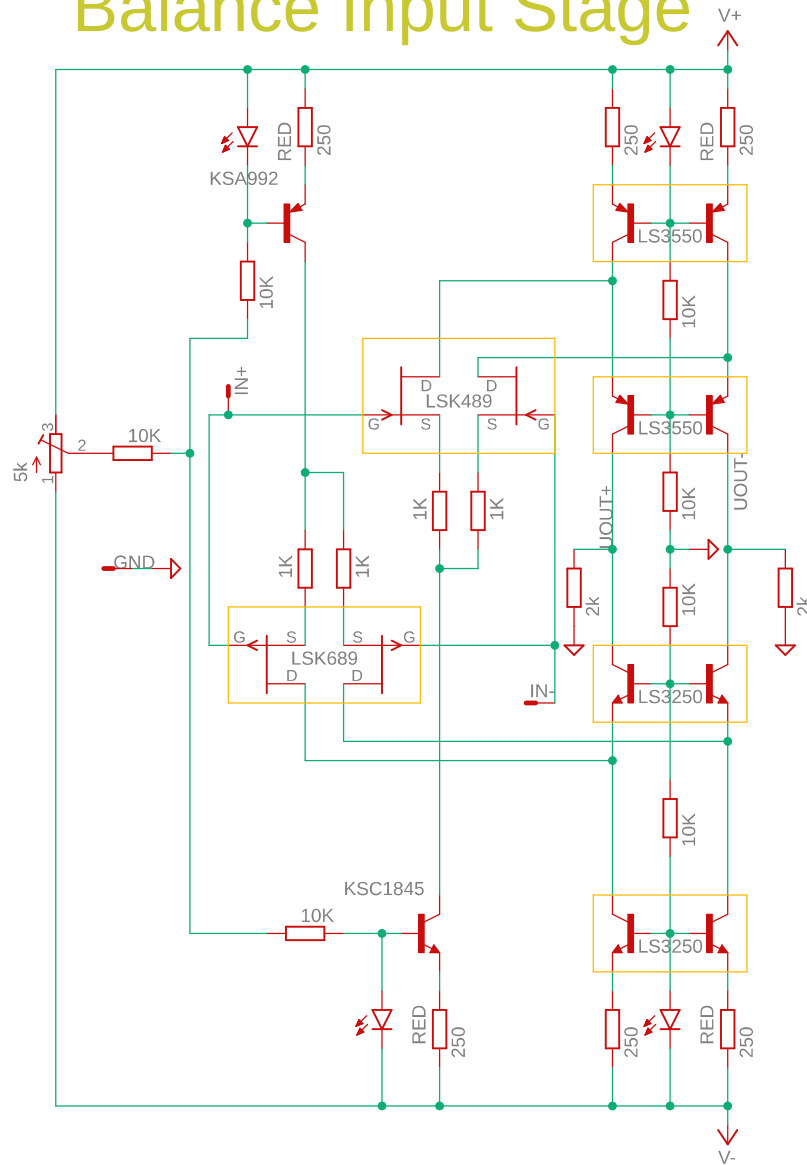
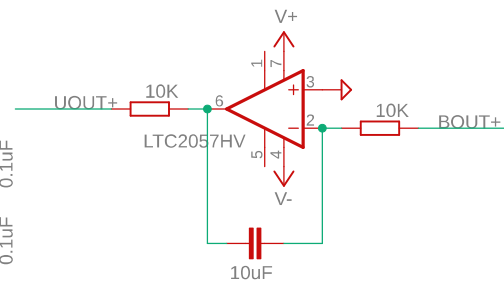
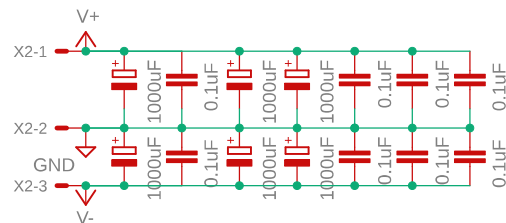


Balance Input Stage



vvv
m3 m3 m3 m3
m3 m3 m3



Buffer

