|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| IC | FPGA | I/O | ARDUINO | I/O |
| IC1 | 48 | OUT | D2 | IN |
| IC1 | 47 | OUT | D3 | IN |
| IC1 | 46 | IN | D4 | OUT |
| IC1 | 45 | IN | D5 | OUT |
| IC2 | 44 | OUT | D12 | IN |
| IC2 | 43 | OUT | D11 | IN |
| IC2 | 42 | IN | D10 | OUT |
| IC2 | 41 | IN | D9 | OUT |
| IC3 | 40 | OUT | D8 | IN |
| IC3 | 39 | OUT | D7 | IN |
| IC3 | 38 | OUT | D6 | IN |
| IC3 | 37 |  |  |  |
|  |  |  |  |  |

SPI\_VFD vfd(A5, A3, A4); //(data / clock / stb)