

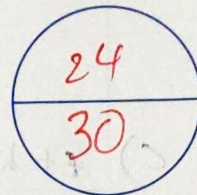


# GITAM

(Deemed to be University)  
(Estd. u/s 3 of UGC Act, 1956)

HYDERABAD CAMPUS - 502 329

## MID TERM / PRACTICAL EXAMINATION



Name of the Student S. Vyshnavi Reddy Roll No. 222010407011 Branch ECE(VLSI)  
Fabrication

Course Code 19EEEL458 Course Name Fundamentals of VLSI device Date 23/02/24

Mid I, II, III / Section II Invigilator Signature [Signature]

Question No.	1					2	3	4	5	6	Total Marks
	A	B	C	D	E						
Marks											

1) Diffusion :-

a) Diffusion is the process of two-step diffusion sequence is commonly used in which a predeposition diffusion layer is formed under a constant surface-concentration condition and is followed by a drive-in diffusion or redistribution under a constant total dopant condition. Adding impurities atoms from a region with high concentration to a region of low concentration

2) The diffusion coefficient is a parameter's constant dependent on molecule size and other properties of the diffusion substance as well as on temperature and pressure.

- \* solid solubility
- \* diffusion-time
- \* diffusion temperature



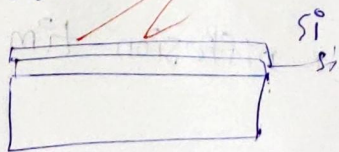
### c) Etching

- \* Strain gauges
- \* Galvanometer mirror frames
- \* Electrical contacts and terminals
- \* Gaskets for meters

d) BULK Micromachining is a process used to produce Micromachinery unlike surface micromachining which uses a succession of thin film deposition and selective etching, bulk micromachining defines structures by selectively etching inside a substrate. whereas surface micromachining creates structures on top of a substrate, bulk machining produces structures inside a substrate

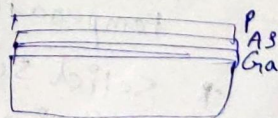
### e) Homoepitaxy

- \* In homo the growth layers are made up of the same materials of the substrate



### Heteroepitaxy

- \* In Hetero the growth layers are of a material different from the substrate





f) Ion Implantation is a low-temperature technique for the introduction of impurities (dopants) into semiconductors and offers more flexibility than diffusion.

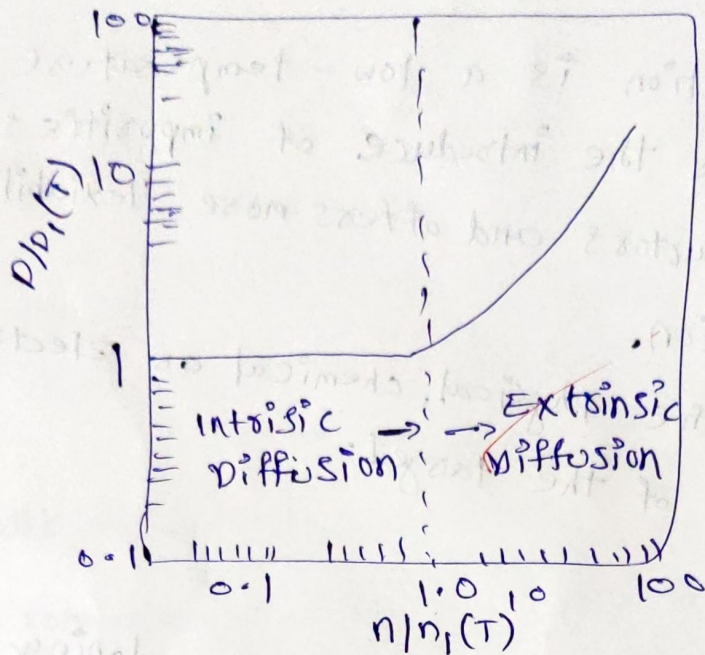
\* changing the physical, chemical or electrical properties of the target.

e) Diffusion that occurs when the doping concentration is lower than the intrinsic carrier concentration at the diffusion temperature is called intrinsic diffusion.

In this region, the resulting dopant profiles of sequential or simultaneous diffusion type of n-type or p-type impurities can be determined by superposition, that is the diffusion processes can be treated independently.

However, when the dopant concentration exceeds (e.g. at  $1000^{\circ}\text{C}$ ,  $n_i = 5 \times 10^{18} \text{ atoms/cm}^3$ ) the process becomes extrinsic and diffusion coefficients become concentration dependent as





In order to make diffusion calculations of reasonable accuracy Fair's diffusivity model will be presented in the next few sections. Fair has developed a model for diffusivity based on impurities interactions with charged vacancy states that provides a reasonable good fit to most observed diffusion results.

$$D_i = D^0 + D^+ + D^- + D^{=+} + \text{for } n \text{ or } p \ll n_i$$

$D^+$  is the positively charged vacancy impurity diffusion  $D^-$  is a negatively charged vacancy impurity diffusion and  $D^{=+}$  is the doubly negatively charged vacancy - impurity diffusivity and  $n_i$  is the intrinsic carrier concentration for extrinsic silicon



Name : S. Vyshnavi Reddy

Semester / Section : A2

Branch : ECE (VLSI)

Roll No. : 2201060701

Subject : Fundamentals of VLSI device fabrication

Invigilator Signature with Date :

$$D_x = D^0 + D^+ \left[ \frac{1}{n_i} \right] + D^- \left[ \frac{n}{n_i} \right]^2 \text{ for } n \text{ or } p \gg n_i$$

\* where  $D_x$  is the extrinsic diffusivity for unambiguous impurities not all vacancy change state - impurity combinations will participate in the diffusivity

\* In order to grow the silicon wafer we need to use the intrinsic and extrinsic semiconductors

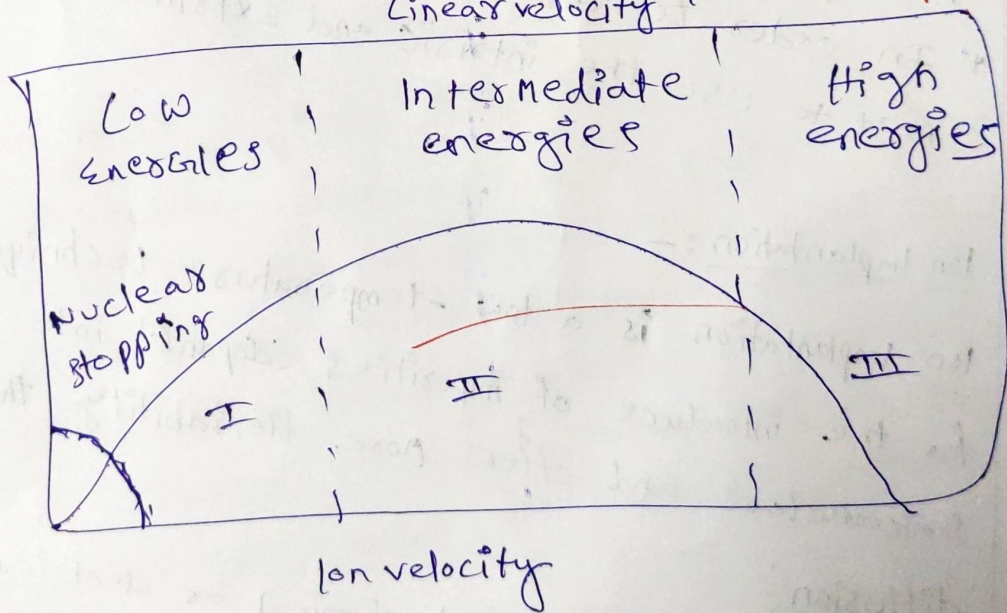
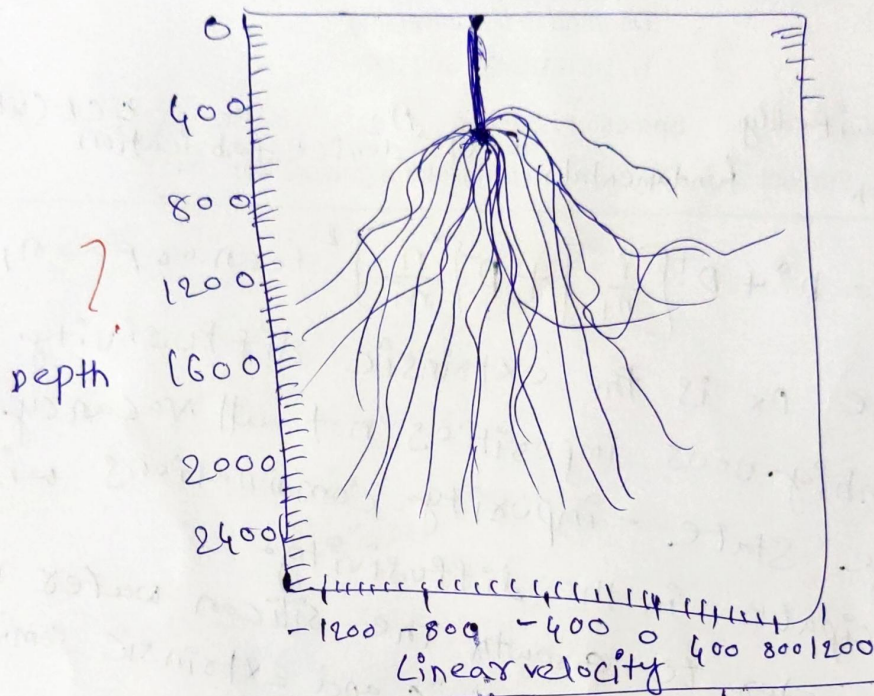
5) Ion Implantation :-

Ion Implantation is a low-temperature technique for the introduction of impurities (dopant) into semiconductors and offers more flexibility than diffusion.

\* In changing the physical, chemical or electrical properties of the target.

$$\beta = \left[ \frac{dE}{dx} \right]_{\text{nuclear}} + \left[ \frac{dE}{dx} \right]_{\text{electrical}}$$





Annealing is the process of treating the grown material at high temperature around ( $500-800^{\circ}\text{C}$ ) to remove the defects and together implantation throughout the substrate materials.