

CSE/EE 5/7385 Microprocessor Architecture and Interfacing
Final Lab: ARM MCBSTM32C Finite State Machine with timers
Checklist

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Functionality	Yes / No
State 1	✓
State 2	✓
State 3	✓
State 4	✓
State 5	✓
Timer	✓
Comments:	
Overall functionality	
Project does not work at all	NA
Project works partially	NA
Project is fully functional	✓
Late demonstration?	Yes / <u>No</u>

Signature: Hui Jiang

Date signed: 05/11/18