

You will see this stupid low-pass filter on the VCC line in some IR receiver chip datasheet. I don't know why they are so much vulnerable to the power noise.

A filter circuit which prevents the IR LED remains ON state after the ESP32 hangs up during the IR_OUT going high. Non-pulse driven IR LED would easily be destroyed by the heat.

Use AE-BME280 as I2C mode.

Pin_Header_Straight_1x05_Pitch2.54mm DNI

Conn_01x03_Female DNI

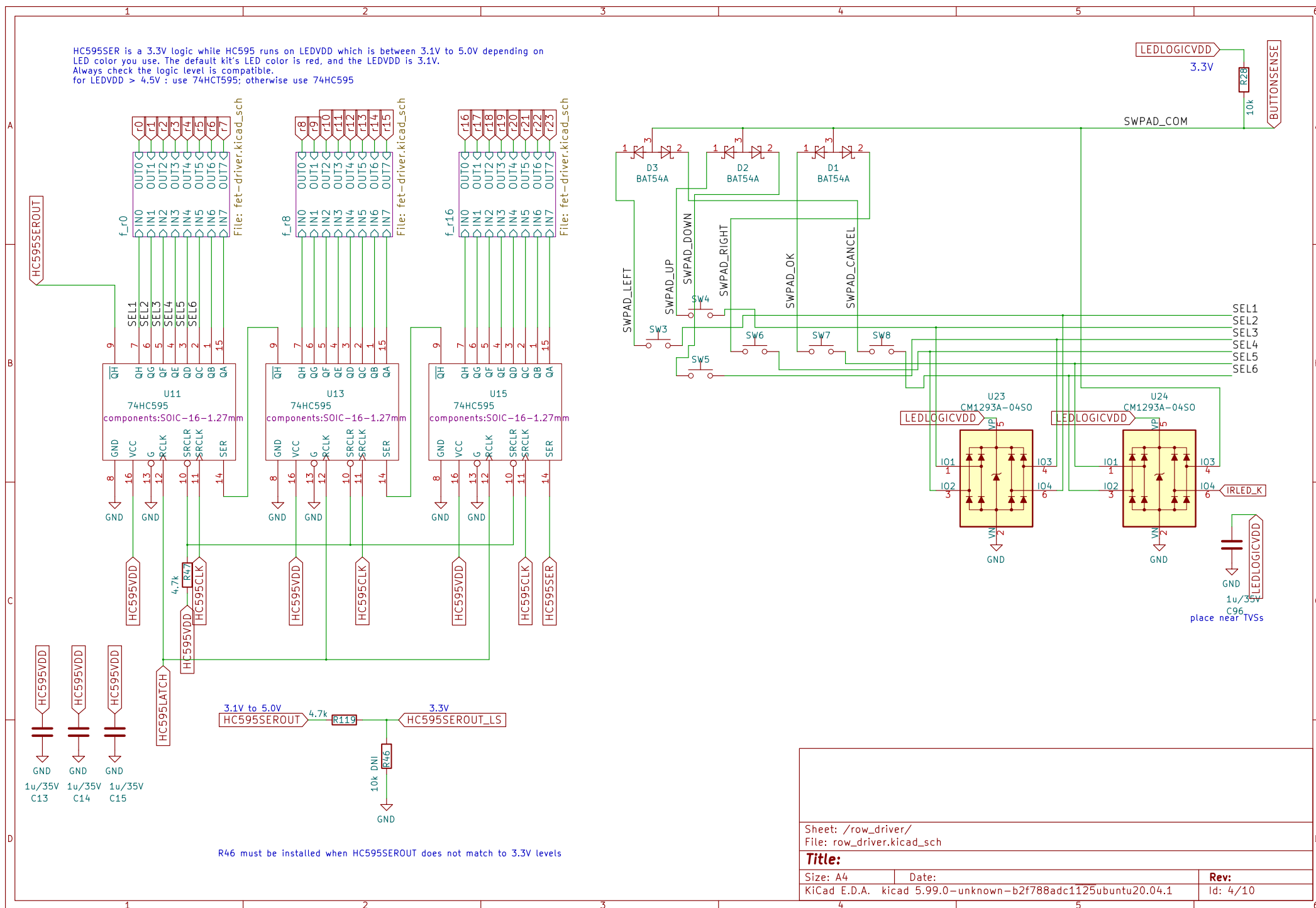
Sheet: /IR_Sensor/
File: IR_Sensor.kicad_sch

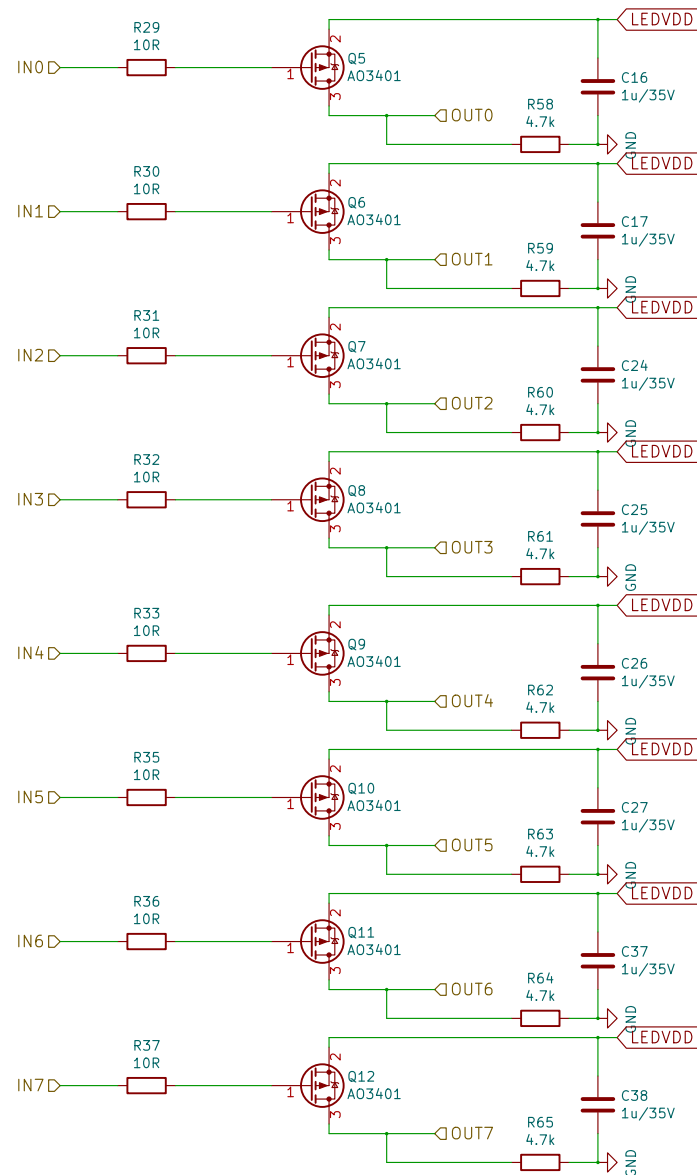
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Date:
Rev:
Id: 2/10







Some P-MOS FETs which will fit:
A03401
DMG2305UX
IRLML6402

Sheet: /row_driver/f_r0/
File: fet-driver.kicad_sch

Title:

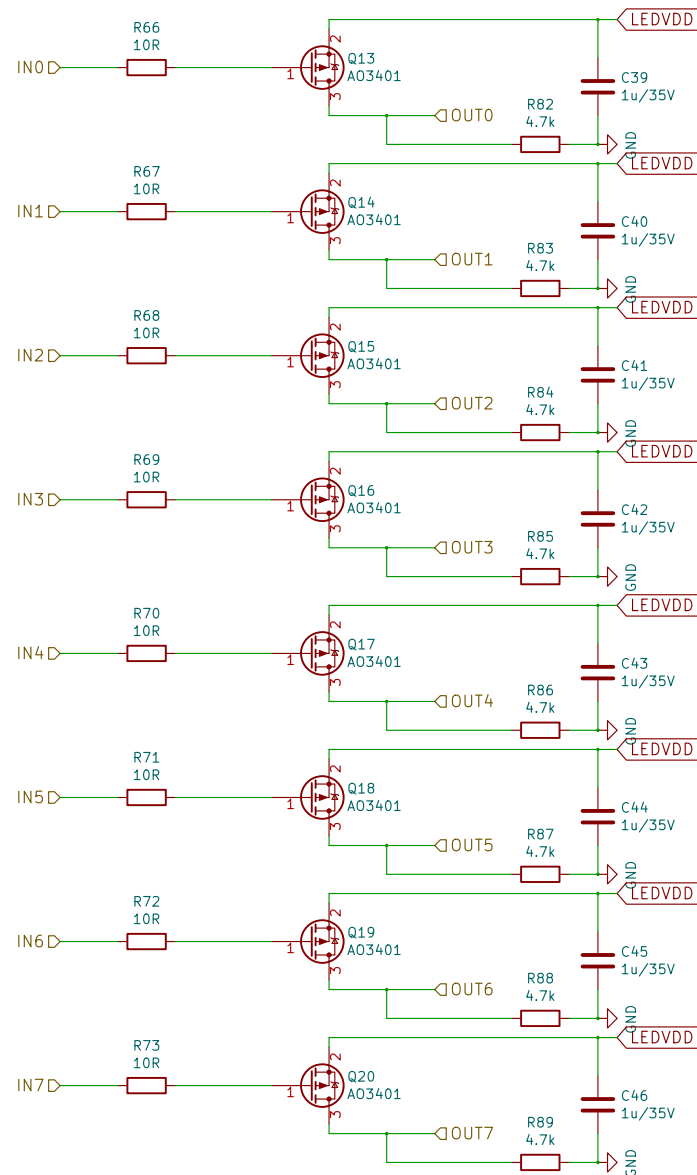
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Rev:

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Some P-MOS FETs which will fit:
A03401
DMG2305UX
IRLML6402

Sheet: /row_driver/f_r8/
File: fet-driver.kicad_sch

Title:

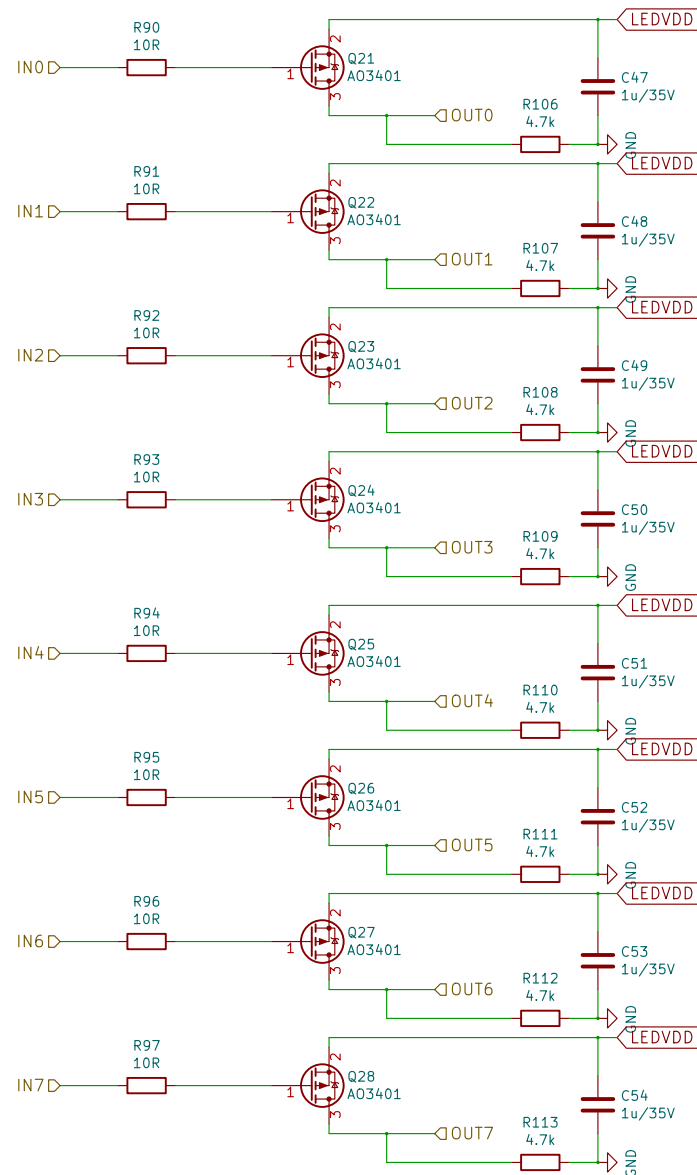
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Rev:

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Id: 6/10



Some P-MOS FETs which will fit:
A03401
DMG2305UX
IRLML6402

Sheet: /row_driver/f_r16/
File: fet-driver.kicad_sch

Title:

Size: A4	Date:	Rev:
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