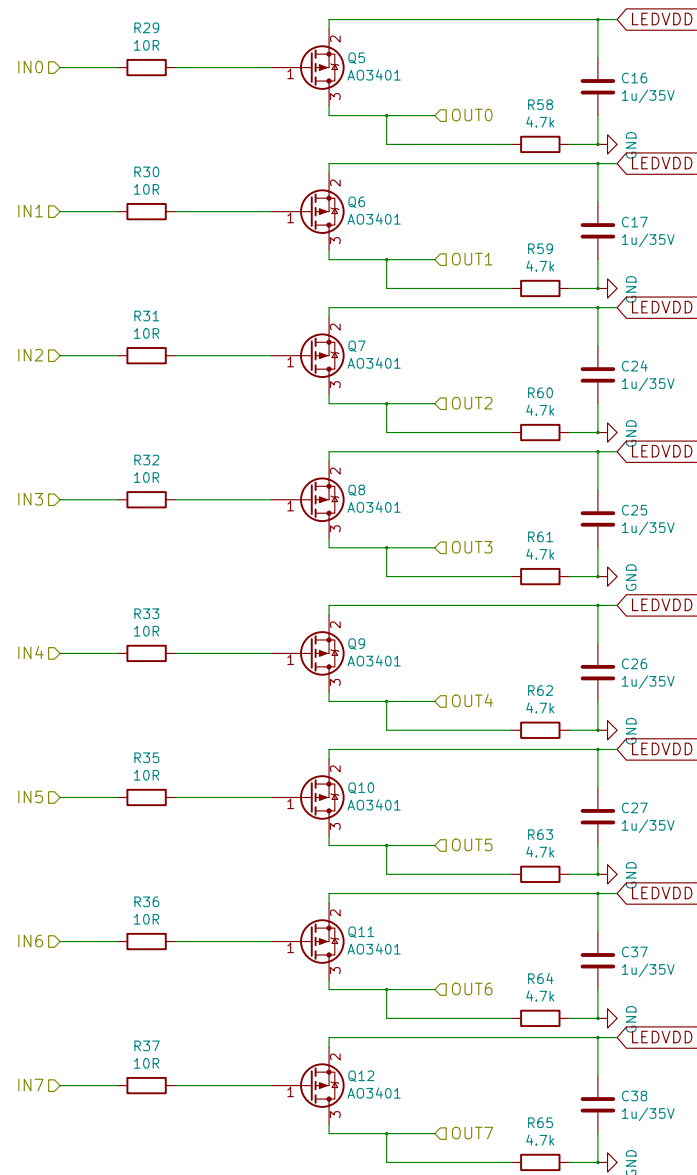


[illegible]





Some P-MOS FETs which will fit:  
A03401  
DMG2305UX  
IRLML6402

Sheet: /row\_driver/f\_r0/  
File: fet-driver.kicad\_sch

**Title:**

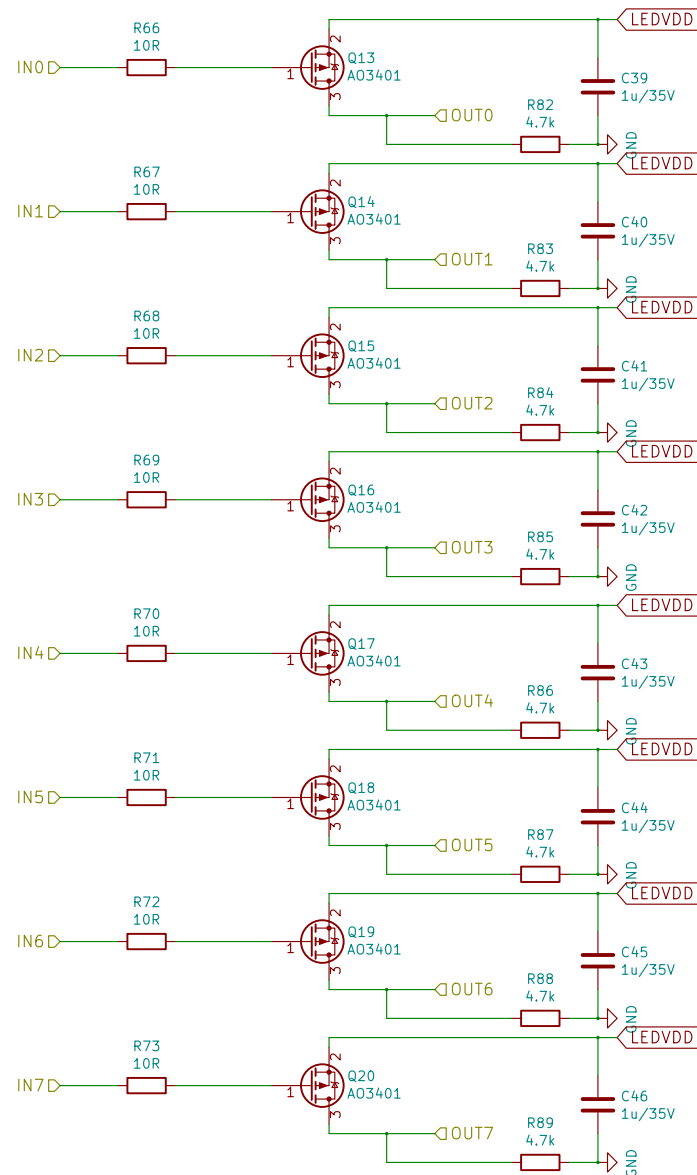
Size: A4

Date:

KiCad E.D.A. kicad 5.99.0-unknown-51fcbe47a106ubuntu20.04.1

**Rev:**

Id: 5/10



Some P-MOS FETs which will fit:  
A03401  
DMG2305UX  
IRLML6402

Sheet: /row\_driver/f\_r8/  
File: fet-driver.kicad\_sch

# Title:

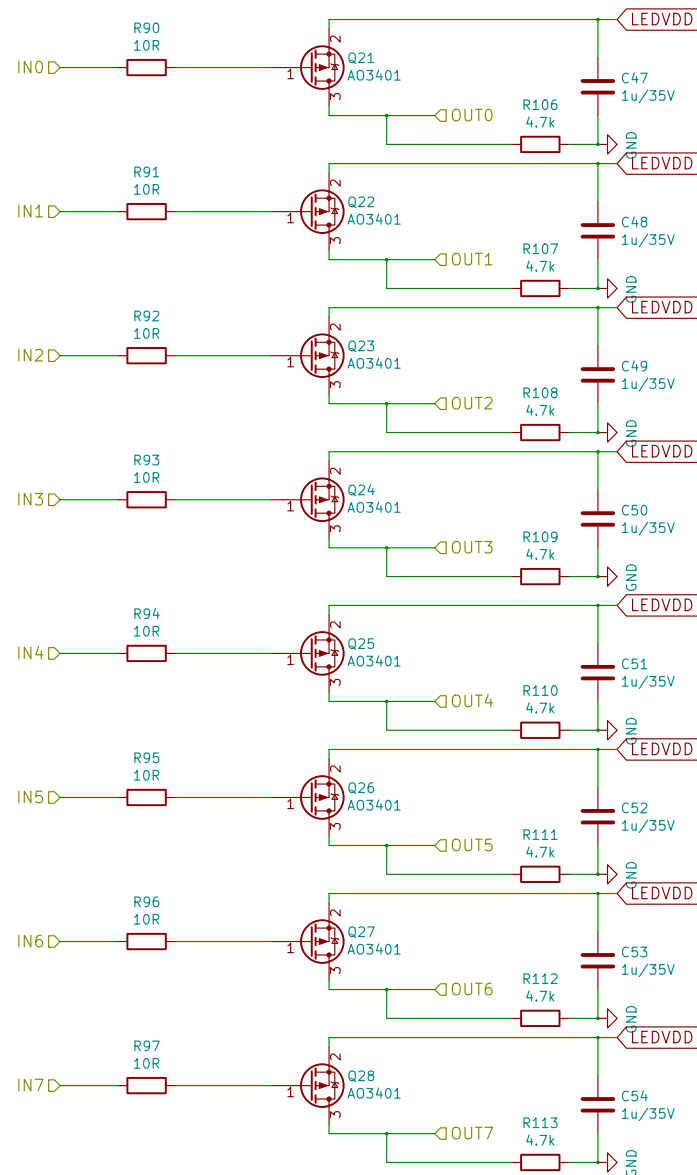
Size: A4

Date:

Rev:

KiCad E.D.A. kicad 5.99.0-unknown-51fcb47a106ubuntu20.04.1

Id: 6/10



Some P-MOS FETs which will fit:  
A03401  
DMG2305UX  
IRLML6402

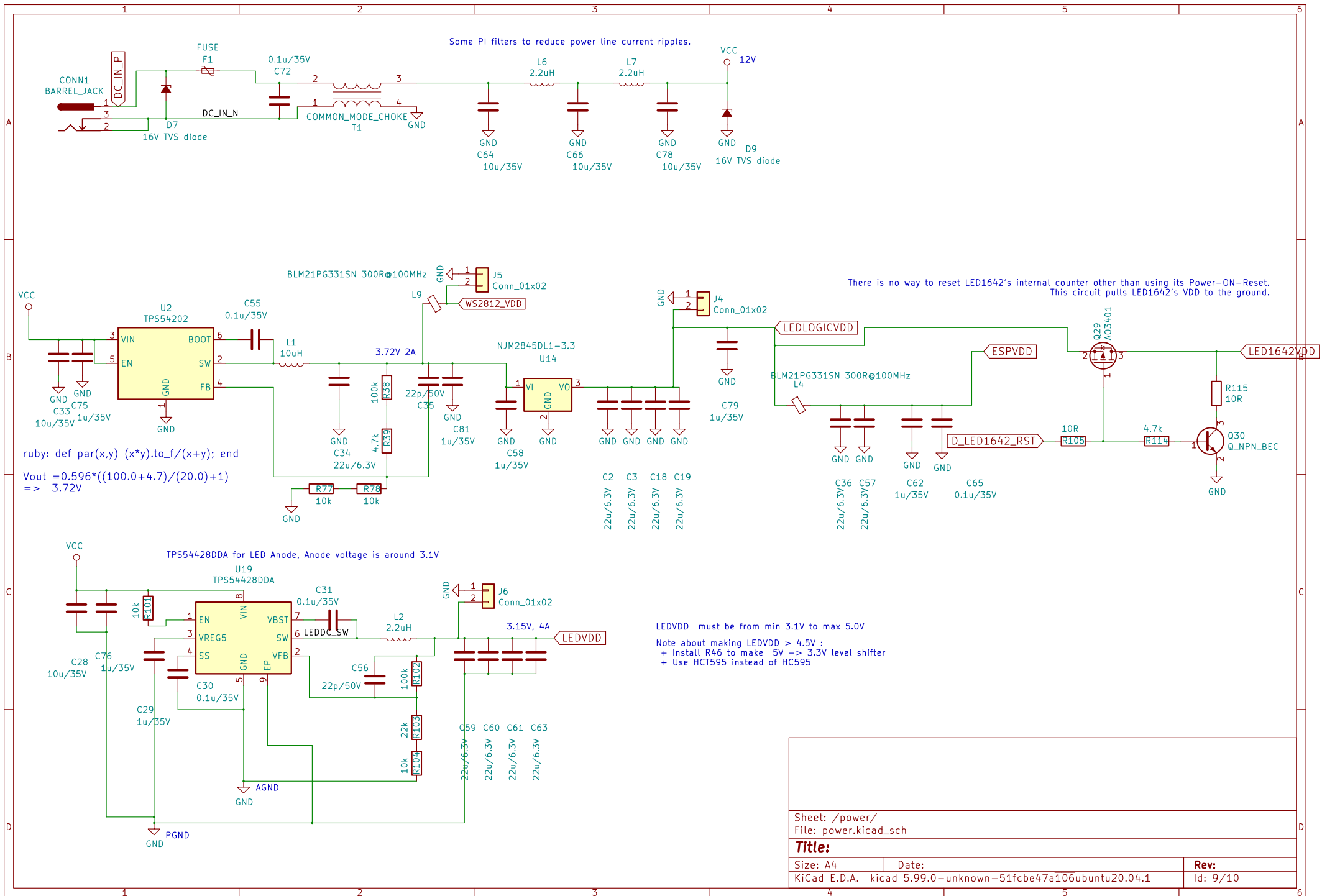
Sheet: /row\_driver/f\_r16/  
File: fet-driver.kicad\_sch

# Title:

Size: A4	Date:	Rev:
KiCad E.D.A. kicad 5.99.0-unknown-51fcbe47a106ubuntu20.04.1		Id: 7/10

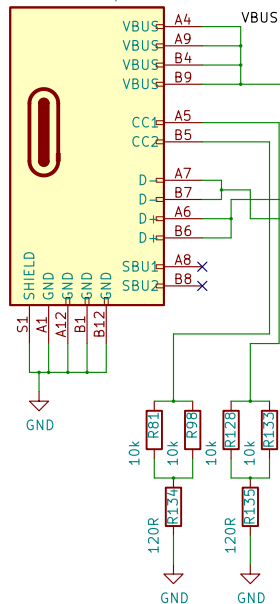




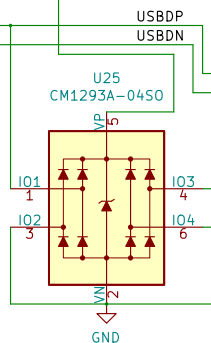


The connector is JAE DX07S016JA1 .  
Always check the pin order is compatible  
when you use another connector.

J9  
USB\_C\_Receptacle\_2.0



2x 5.12k  
(5.1k +/- 10% is the spec)



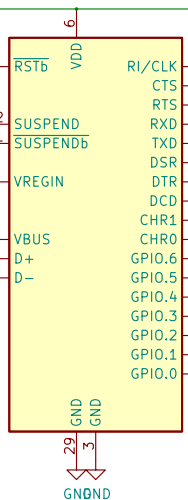
3mm at light speed requires a time to travel of : 0.01 ns  
A time needed by one phase of 12MHz wave : 83.33 ns

Therefore, 3mm length mismatch is negligible at this transmission speed.  
Don't stick to the length matching so much.

22u/6.3V  
1u/35V  
0.1u/35V

10u/35V  
1u/35V  
0.1u/35V

U20  
CP2102N-A01-GQFN28



USB3V3

E RX  
E TX  
DTR  
RTS

Sheet: /USB\_UART/  
File: usb\_uart.kicad\_sch

**Title:**

Size: A4

Date:

**Rev:**

KiCad E.D.A. kicad 5.99.0-unknown-51fcbe47a106ubuntu20.04.1

Id: 10/10