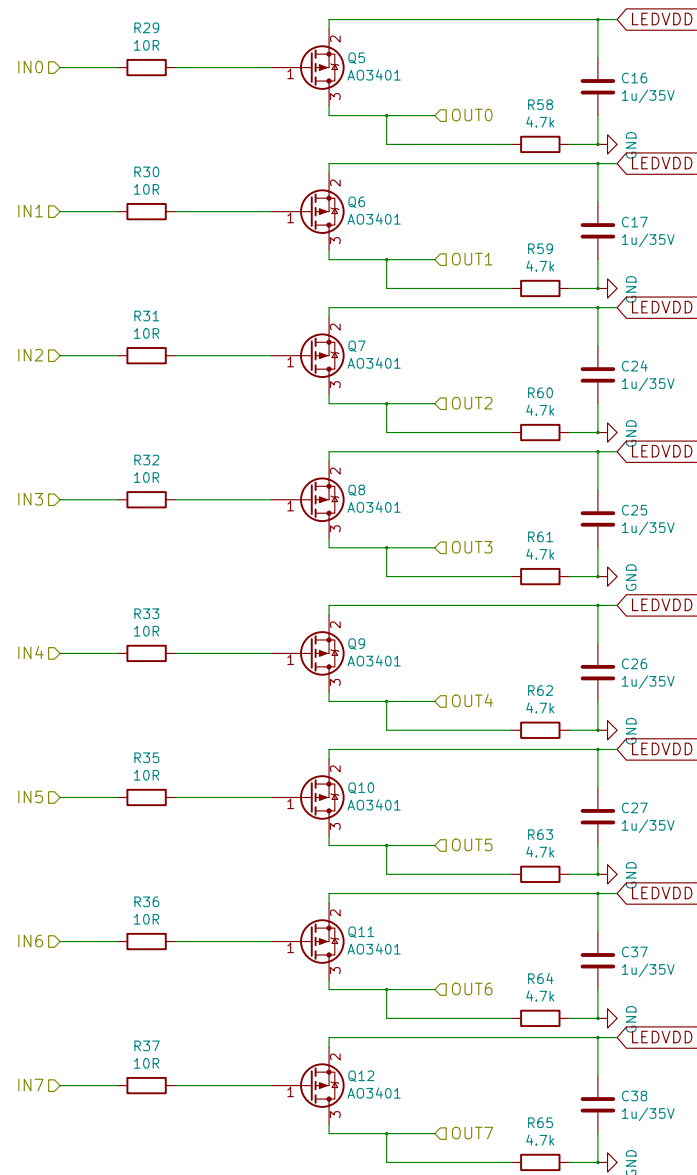


[illegible]



Some P-MOS FETs which will fit:
A03401
DMG2305UX
IRLML6402

Sheet: /row_driver/f_r0/
File: fet-driver.kicad_sch

Title:

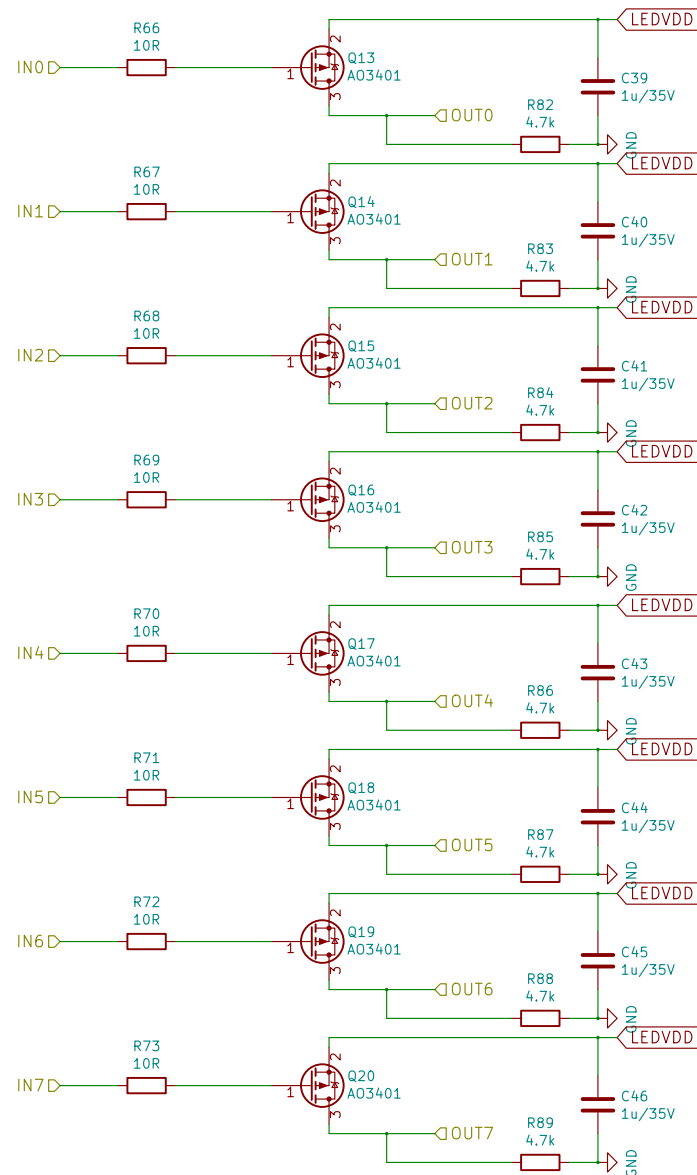
Size: A4

Date:

Rev:

KiCad E.D.A. kicad 5.99.0-unknown-ca2252686104ubuntu20.04.1

Id: 5/10



Some P-MOS FETs which will fit:
A03401
DMG2305UX
IRLML6402

Sheet: /row_driver/f_r8/
File: fet-driver.kicad_sch

Title:

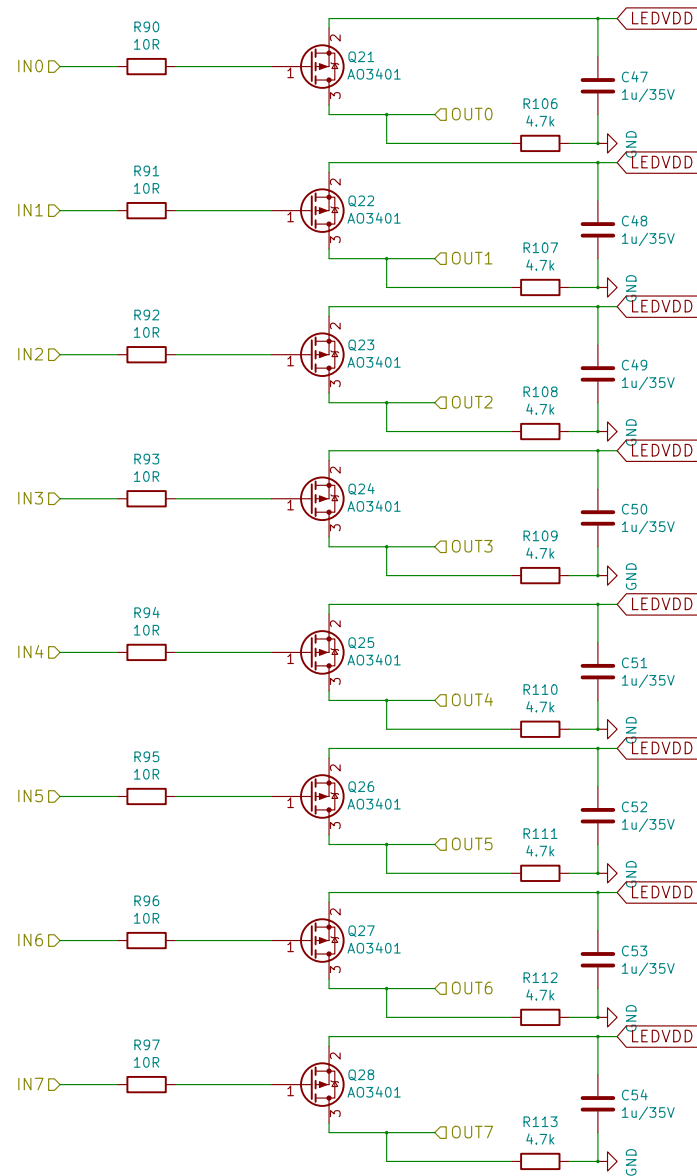
Size: A4

Date:

Rev:

KiCad E.D.A. kicad 5.99.0-unknown-ca2252686104ubuntu20.04.1

Id: 6/10

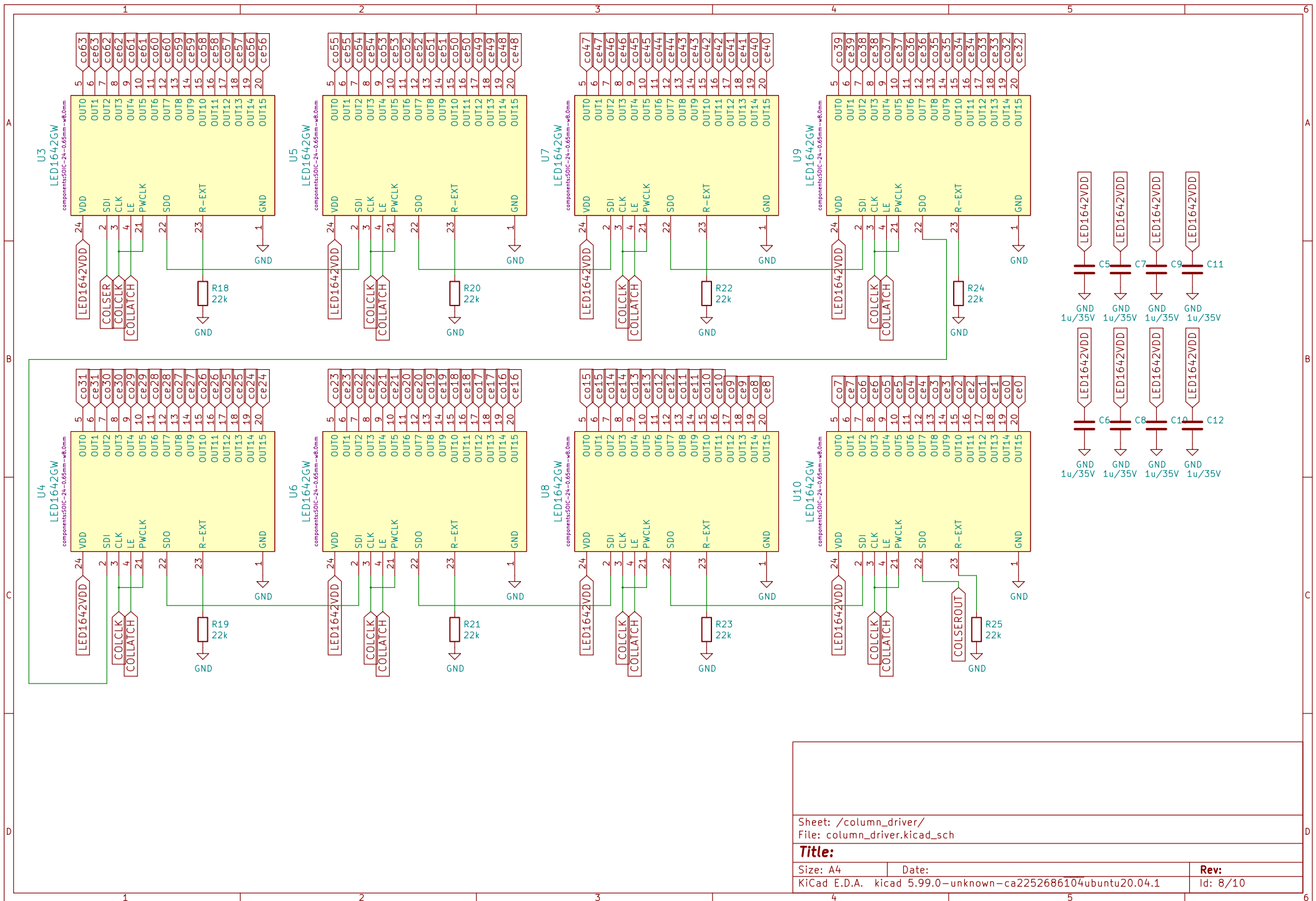


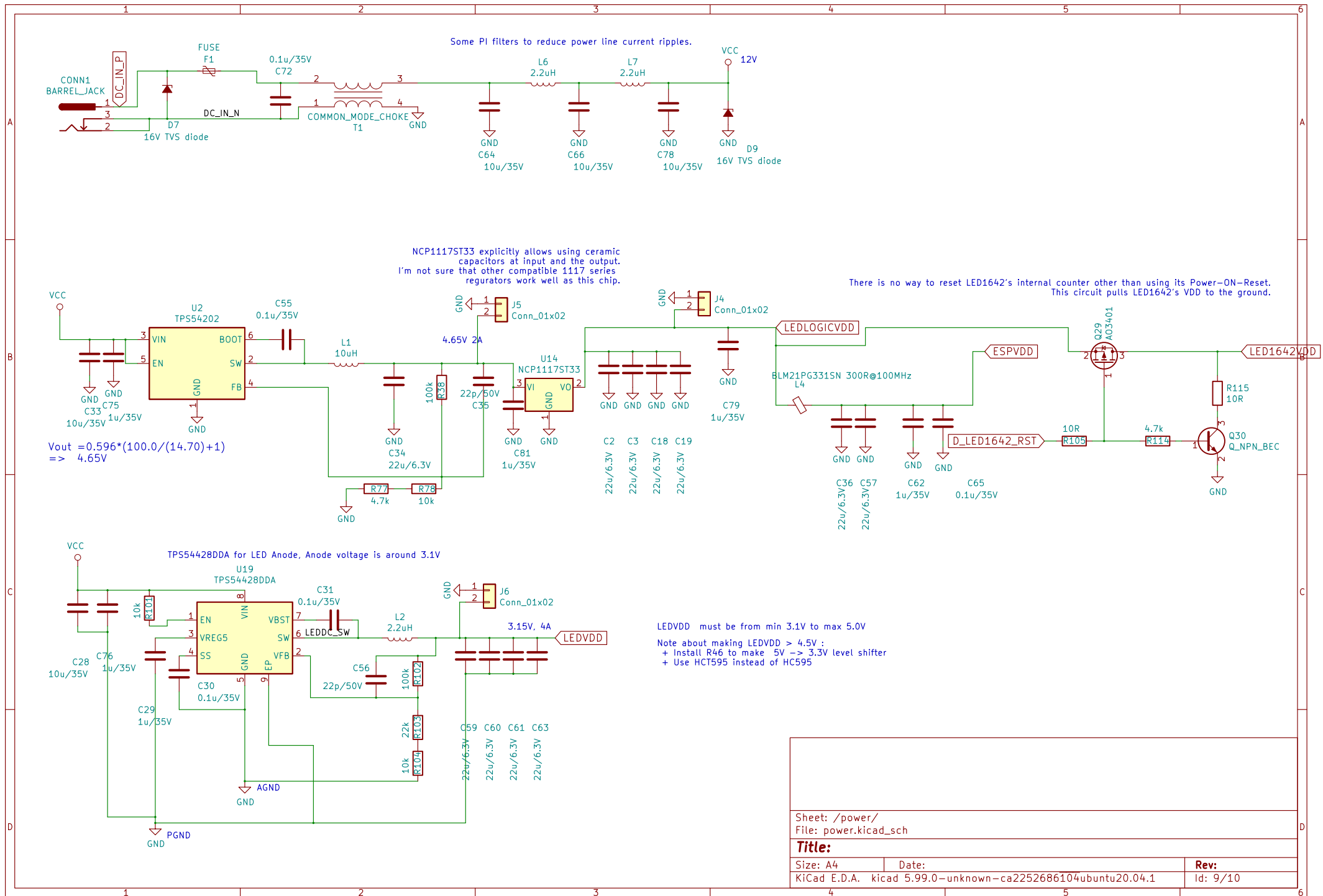
Some P-MOS FETs which will fit:
A03401
DMG2305UX
IRLML6402

Sheet: /row_driver/f_r16/
File: fet-driver.kicad_sch

Title:

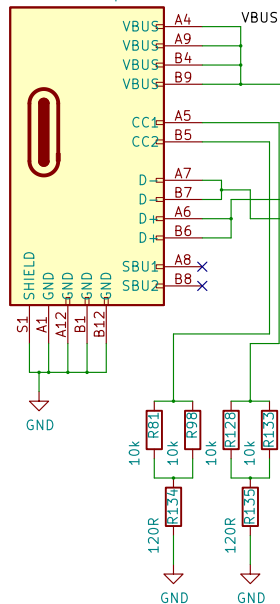
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 5.99.0-unknown-ca2252686104ubuntu20.04.1		Id: 7/10



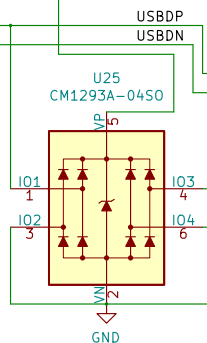


The connector is JAE DX07S016JA1 .
Always check the pin order is compatible
when you use another connector.

J9
USB_C_Receptacle_2.0



2x 5.12k
(5.1k +/- 10% is the spec)



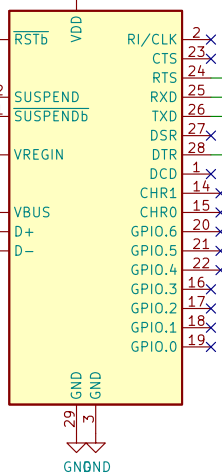
3mm at light speed requires a time to travel of : 0.01 ns
A time needed by one phase of 12MHz wave : 83.33 ns

Therefore, 3mm length mismatch is negligible at this transmission speed.
Don't stick to the length matching so much.

22u/6.3V
1u/35V
0.1u/35V

U20
CP2102N-A01-GQFN28

USB3V3



E RX
E TX
DTR
RTS

Sheet: /USB_UART/
File: usb_uart.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 5.99.0-unknown-ca2252686104ubuntu20.04.1

Rev:

Id: 10/10