

You will see this stupid low-pass filter on the VCC line in some IR receiver chip datasheet. I don't know why they are so much vulnerable to the power noise.

A filter circuit which prevents the IR LED remains ON state after the ESP32 hangs up during the IR\_OUT going high. Non-pulse driven IR LED would easily be destroyed by the heat.

Use AE-BME280 as I2C mode.

Sheet: /IR\_Sensor/  
File: IR\_Sensor.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. kicad 5.99.0-unknown-a6c3d74a73125ubuntu20.04.1

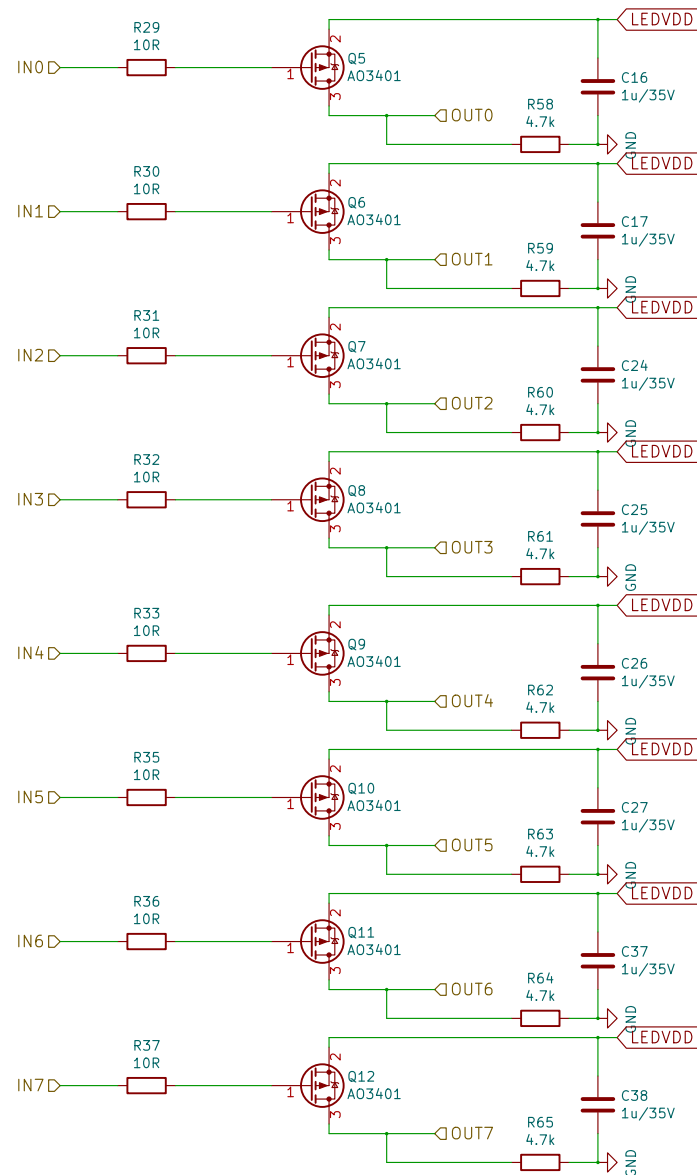
**Rev:**

Id: 2/10

The diagram illustrates a 6x6 LED matrix layout. The grid is defined by columns 1-6 and rows A-F. Each LED is represented by a hexagonal symbol with two pins: one labeled 'ce' (Cathode) and one labeled 'R' (Anode). The pins are numbered sequentially from 1 to 24. The labels 'LED1' and 'LED64x48' are placed near the grid. A title block at the bottom right contains the following information:

|  |       |          |
|--|-------|----------|
| Sheet: /led_matrix/<br>File: ledmatrix.kicad_sch             |       |          |
| <b>Title:</b>  |       |          |
| Size: A4   | Date: | Rev:     |
| KiCad E.D.A. kicad 5.99.0-unknown-a6c3d74a73125ubuntu20.04.1 |       | Id: 3/10 |





Some P-MOS FETs which will fit:  
A03401  
DMG2305UX  
IRLML6402

Sheet: /row\_driver/f\_r0/  
File: fet-driver.kicad\_sch

# Title:

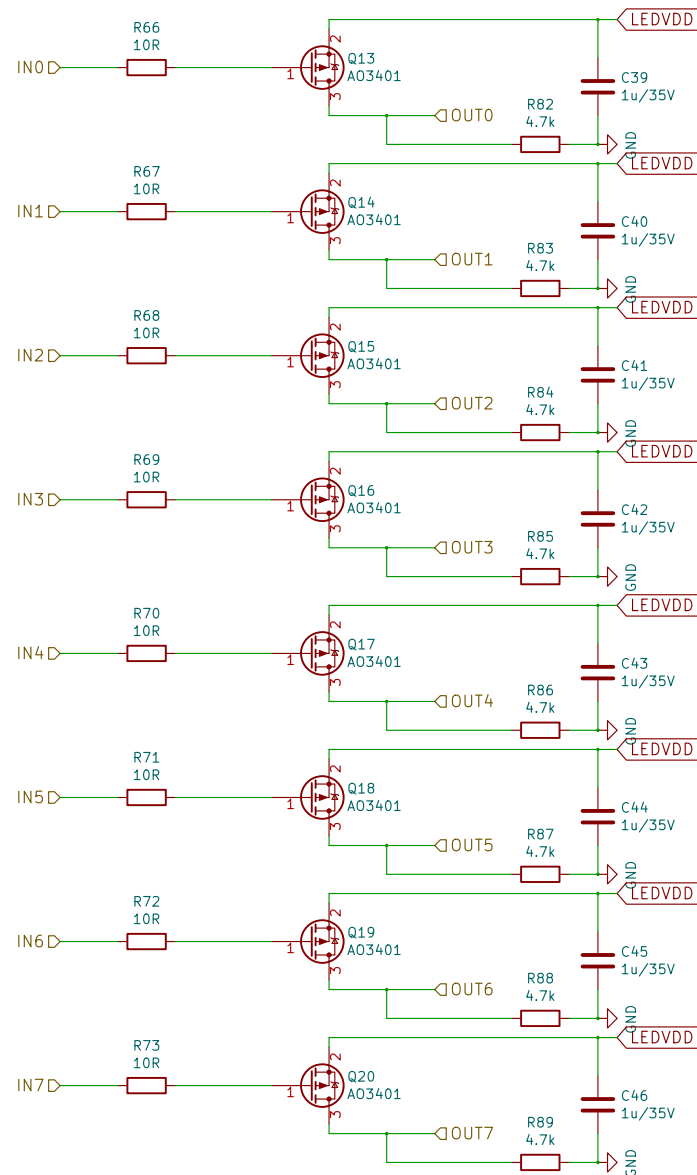
Size: A4

Date:

Rev:

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Id: 5/10



Some P-MOS FETs which will fit:  
A03401  
DMG2305UX  
IRLML6402

Sheet: /row\_driver/f\_r8/  
File: fet-driver.kicad\_sch

# Title:

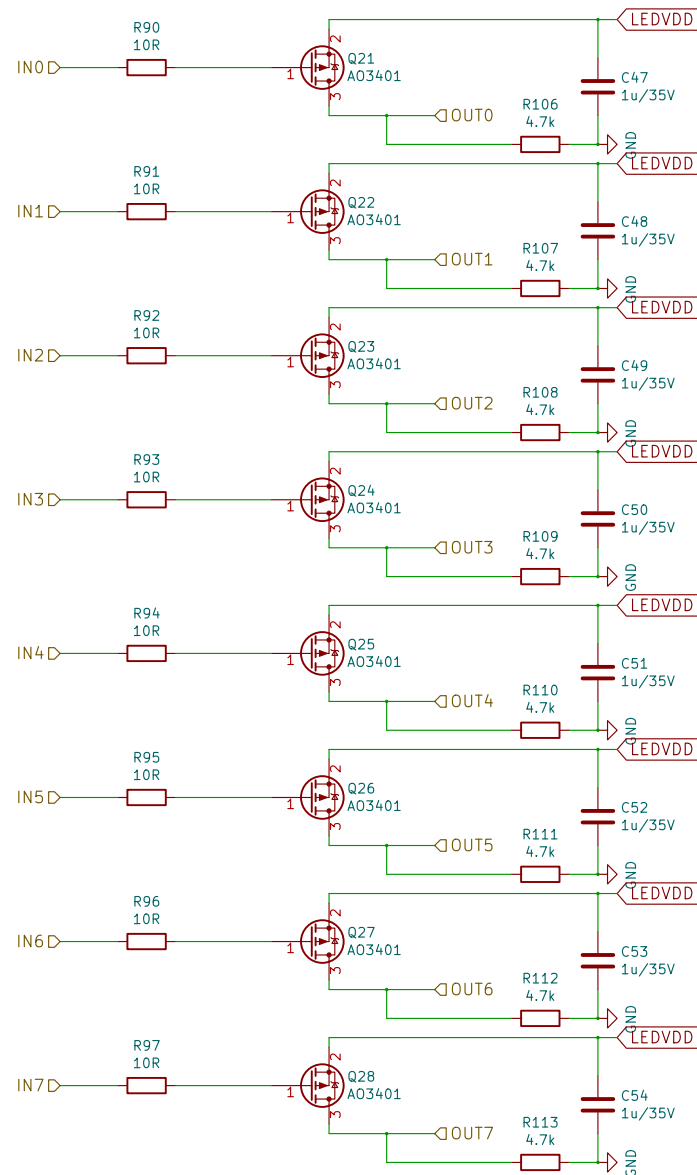
Size: A4

Date:

Rev:

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Id: 6/10



Some P-MOS FETs which will fit:  
A03401  
DMG2305UX  
IRLML6402

Sheet: /row\_driver/f\_r16/  
File: fet-driver.kicad\_sch

# Title:

Size: A4

Date:

Rev:

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Id: 7/10

