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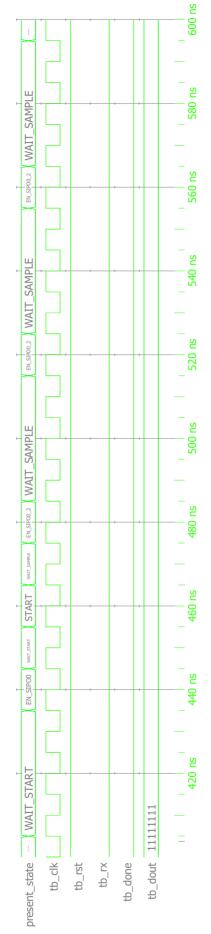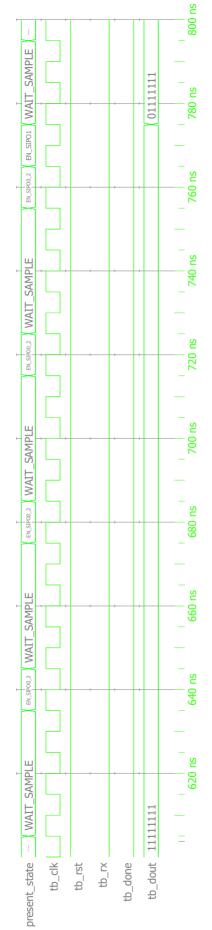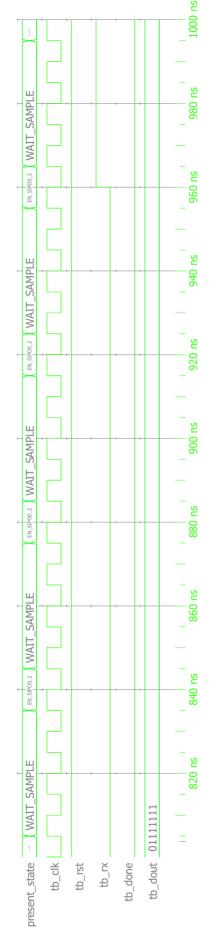
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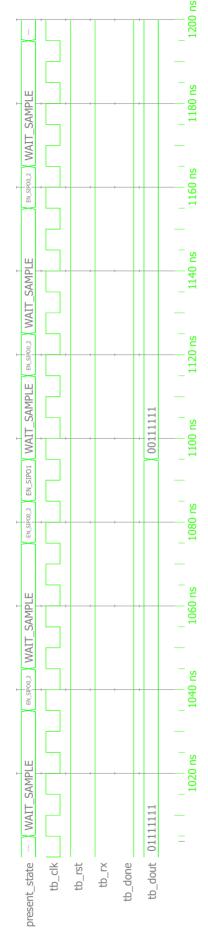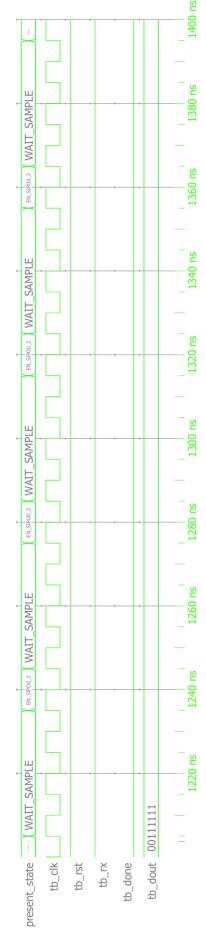
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Entity:tb_uart_rx Architecture:test Date: Fri Dec 02 22:03:26 CET 2022 Row: 6 Page: 6

present_state | tb_clk | tb_rst | tb_rx | tb_done | tb_dout

WAIT_SAMPLE | EN_SIPO0..2 | WAIT_SAMPLE | EN_SIPO0..2 | WAIT_SAMPLE | EN_SIPO0..2 | WAIT_SAMPLE | EN_SIPO0..2 | WAIT_SAMPLE

00111111

1220 ns | 1240 ns | 1260 ns | 1280 ns | 1300 ns | 1320 ns | 1340 ns | 1360 ns | 1380 ns | 1400 ns
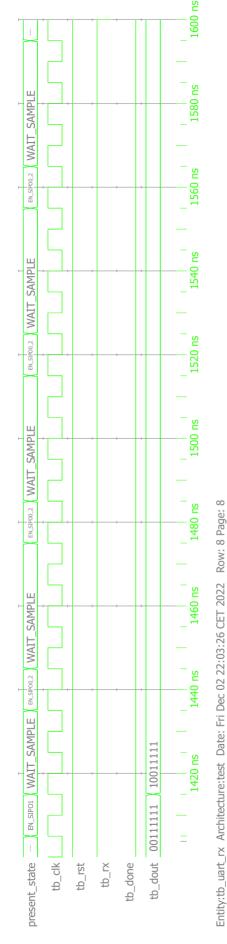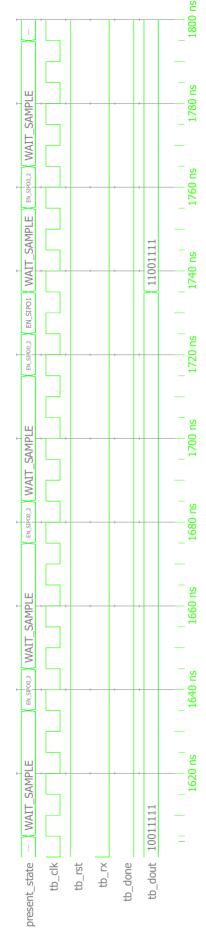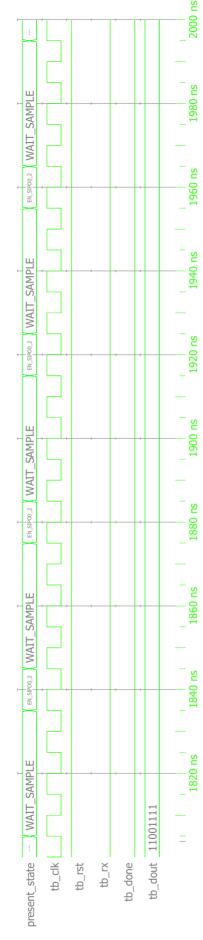
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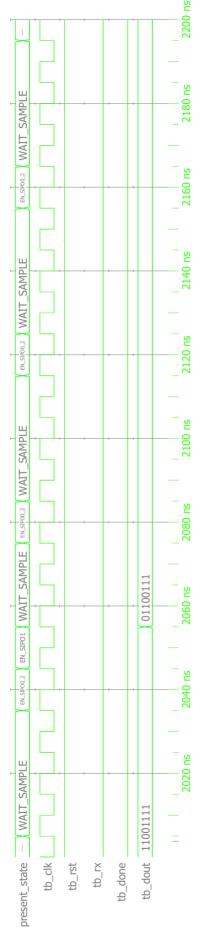
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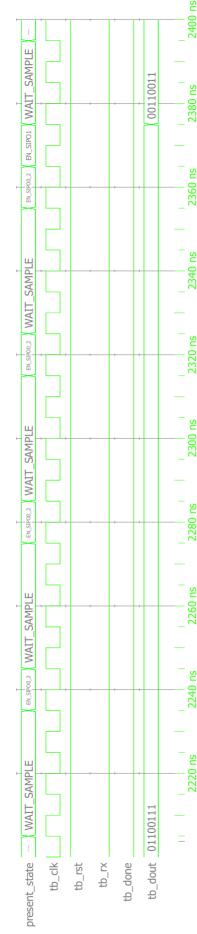
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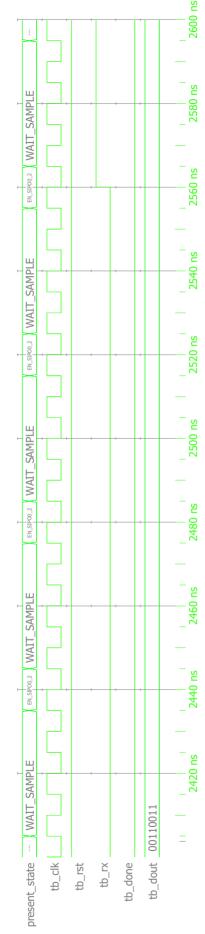
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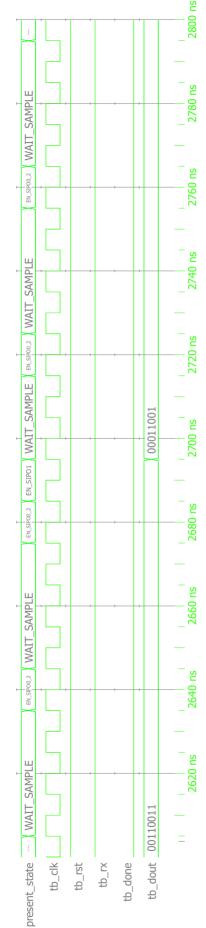
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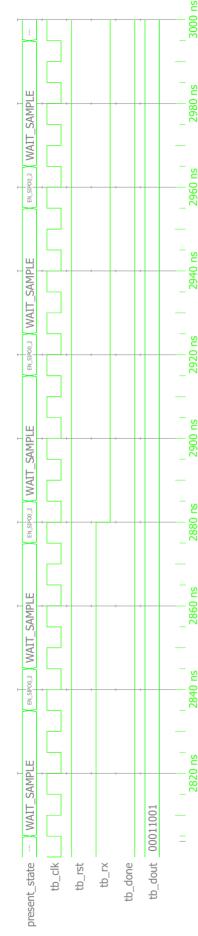
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Entity:tb_uart_rx Architecture:test Date: Fri Dec 02 22:03:26 CET 2022 Row: 15 Page: 15

present_state ... | EN_SIPO1 | WAIT_SAMPLE | EN_SIPO0_2 | WAIT_SAMPLE | EN_SIPO0_2 | WAIT_SAMPLE | EN_SIPO0_2 | WAIT_SAMPLE | EN_SIPO0_2 | WAIT_SAMPLE | ...

tb_clk

tb_rst

tb_rx

tb_done

tb_dout   00011001 | 10001100

3020 ns   3040 ns   3060 ns   3080 ns   3100 ns   3120 ns   3140 ns   3160 ns   3180 ns   3200 ns
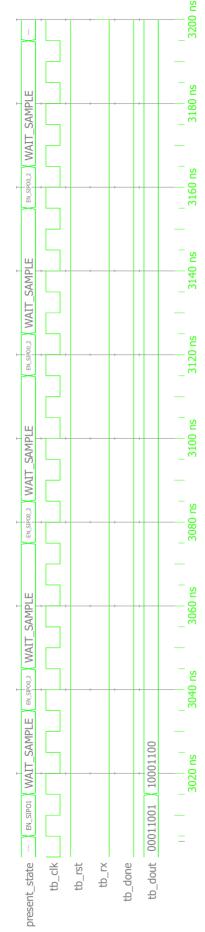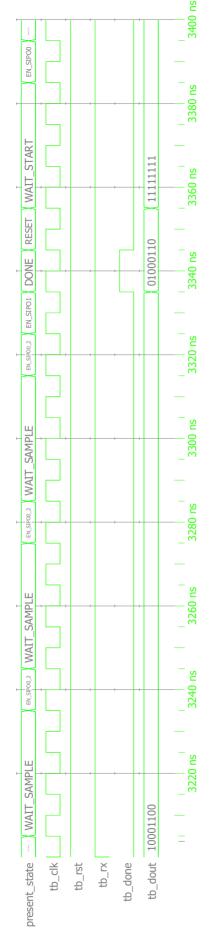
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Entity:tb_uart_rx Architecture:test Date: Fri Dec 02 22:03:26 CET 2022 Row: 17 Page: 17