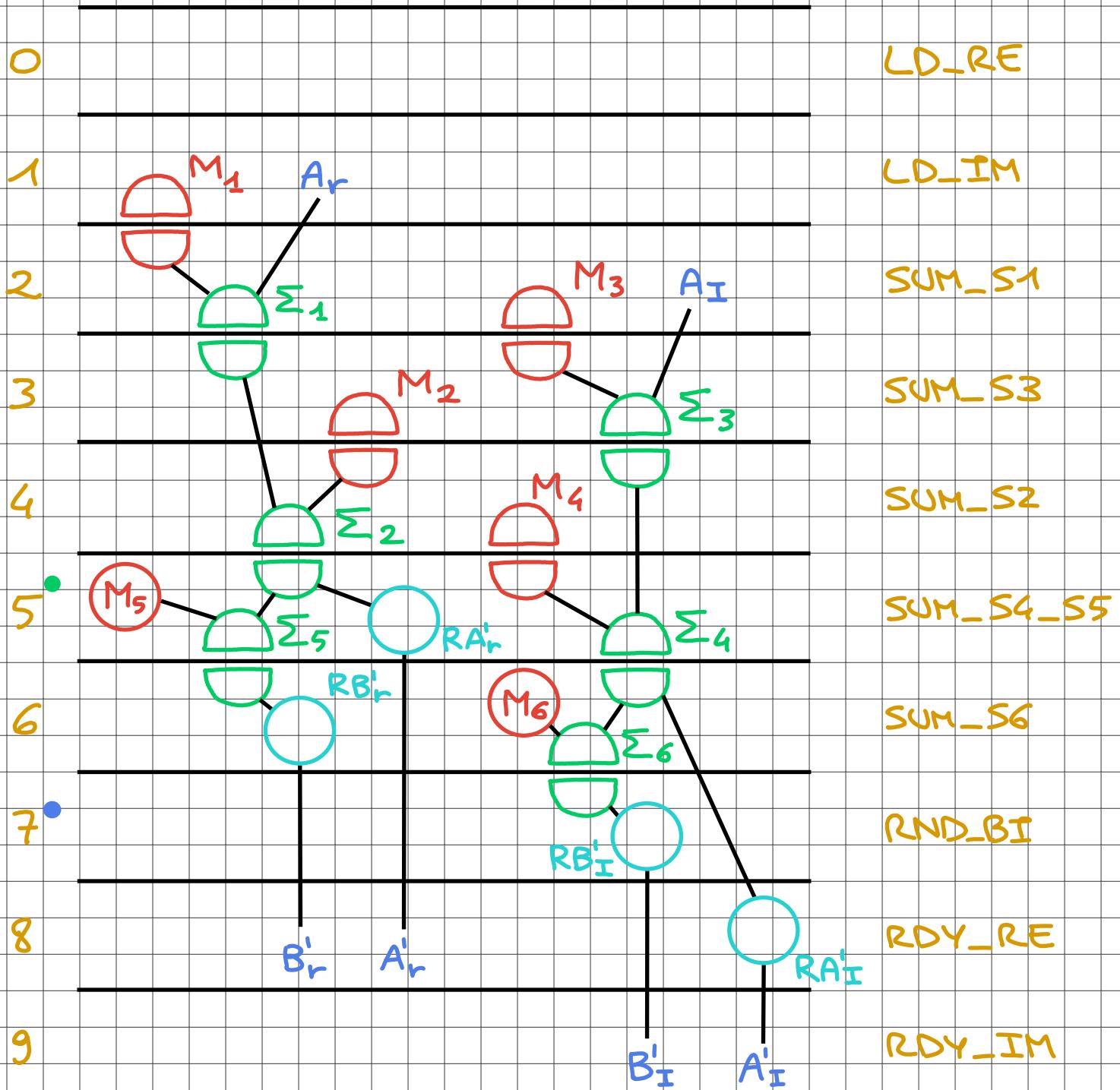


BUTTERFLY DESIGN

CONTROL DATA FLOW GRAPH



● ← CICLO SENSIBILE A START

● ← CICLO CHE FORNISCE DONE

TEMPO DI VITA VARIABILI

6 REGISTRI IN
TOTALE NEL DF

	I	II	III	IV	V	VI	VII	VIII	IX
Ar	0 0	0 0	0 0	0 0	0 0				
Ai		0 1	0 1	0 1	0 1	0 1			
B _r	0 2	0 2							
B _i		0 3	0 3	0 3					
W _r	0 4	0 4	0 4	0 4					
W _i	0 5	0 5	0 5						
M ₁		0							
M ₂				0					
M ₃		0							
M ₄					0				
M ₅									
M ₆									
Σ_1		0	0 2						
Σ_2				0	0 2				
Σ_3			0	0 2					
Σ_4					0	0 2	0 2		
Σ_5					0				
Σ_6						0			
R _{A'} _r					0 0	0 0	0 0		
R _{B'} _r						0 1	0 1		
R _{A'} _i								0 2	
R _{B'} _i							0 3	0 3	

DATAPATH

- PARALLELISMO INTERNO DA AGGIUNGERE ALLO SCHEMATICICO
- 33 BIT NECESSARI PER I DATI

