

1	2	3	4	5	6	7	8	9	10
HLT	M11	RI	RO	IO	II	AI	AO	ΣO	ΣI

11	12	13	14	15	16	17	18	19	20
BI	BO	OI	OE	CO	J	FI	M2I	RM0	LRS

21
LCI

$\overline{\text{CLK}}$	RAM Address	RAM Write	Flags Register	
CLK	RAM Data	Run/Prog Mode	$\overline{\text{CF}} \mid \overline{\text{ZF}}$	
CLK Man Pulse	CLK Freq Ctl	CLK Auto/Man	Reset	
Instruction Register		Bus Data	Carry	Zero
Step Counter		ROM Address	ROM Data	
Program Counter		Program Select	A Register	
B Register		ALU Output	Output Register	
LCD Screen		Display Mode	Multiplex Freq Ctl	
Contrast		000 - Dec 001 - Signed Dec 010 - Hex 011 - Binary 100 - Octal 101 - Off 110 - Off 111 - Off		
Brightness				

Control EEPROMs