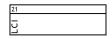
1	2	3	4	5	6	7	8	9	10
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Instruction Register





Carry

Zero

CLK	RAM Address	RAM Write	Flags Register
CLK	RAM Data	Run/Prog Mode	CF ZF
CLK Man Pulse	CLK Freq Ctl	CLK Auto/Man	Reset

Step Counter	<b>ROM Address</b>	<b>ROM Data</b>

**Bus Data** 

Program Counter	Program Select	A Register
	JEIECI	

B Register	<b>ALU Output</b>	Output Register
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LCD Screen	Display Mode	Multiplex Freq Ctl
Contrast	001 - Signed Dec	
	010 - Hex	
	011 - Binary	
Brightness	100 - Octal	
	101 - Off	
	110 - Off	
	111 - Off	

Control EEPROMs