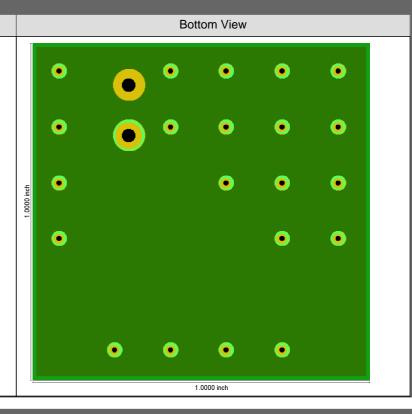
QED Report

Integr8tor

Name	n1wpk8fe.zip	ld.	15456 - QED OK
Report Generated on	Nov 3, 2017 10:00:53 PM	Customer	InstantDFM
Board Id			

Single F	PCB Vie	ew - Origir	nal			
			Тор	o View		
	•	•	•	• (O U5	•
,	•	•	•	• (9 4	•
1.0000 inch	•	•	•		C6	•
1.	•	•	U1		C5 C4 L1	•
		0 0	C1	C2 •	• U3	
			1.0	000 inch		



Summary - General - Original	
PCB Size	1.0000 inch x 1.0000 inch
PCB Thickness	62.00 mil
Customer Panel Size	
SMD Pads Top	18
SMD Pads Bottom	0
SMD Density Top	18 SMD/inch ²
SMD Density Bottom	0 SMD/inch ²
Number of Nets	31
Electrical Test	Single Sided
Max. Aspect Ratio on PTH	4.1

Copper Layers	2
Solder Mask	Both
Solder Mask Color	Green
Legend	Top Only
Legend Color	White
Peeloff Mask	None
Carbon Mask	None
Drill Hole Density	23 Holes/inch ²
Holes in SMD Pads	No
Edge Connectors	No
Surface Finish	

Summary - Copper Layers - Original											
Layer Type	Min. Line Width	Min. Copper Width	Min. Ring	Min. Clr. to Copper	Min. Clr. to Plated Hole	Min. Clr. to NPTH	Min. Clr. to Outline				
	mil	mil	mil	mil	mil	mil	mil				
Outer	>16.00	9.71	7.50	6.16	14.39		5.51				

Ucamco **

Integr8tor v2017.05-170524 Page1



Integr8tor

Name	n1wpk8fe.zip	ld.	15456 - QED OK		
Report Generated on	Nov 3, 2017 10:00:53 PM	Customer	InstantDFM		
Board Id					

Summary - Sequences - Original												
Туре	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Clr. Hole to Copper				
			mil	mil		mil	mil	mil				
Blind	0											
Buried	0											
PTH	1	2	15.00	39.37	23	7.50		14.39				
Plated (Total)	1	2	15.00	39.37	23	7.50		14.39				
NPTH	0											
Total	1	2	15.00	39.37	23	7.50		14.39				



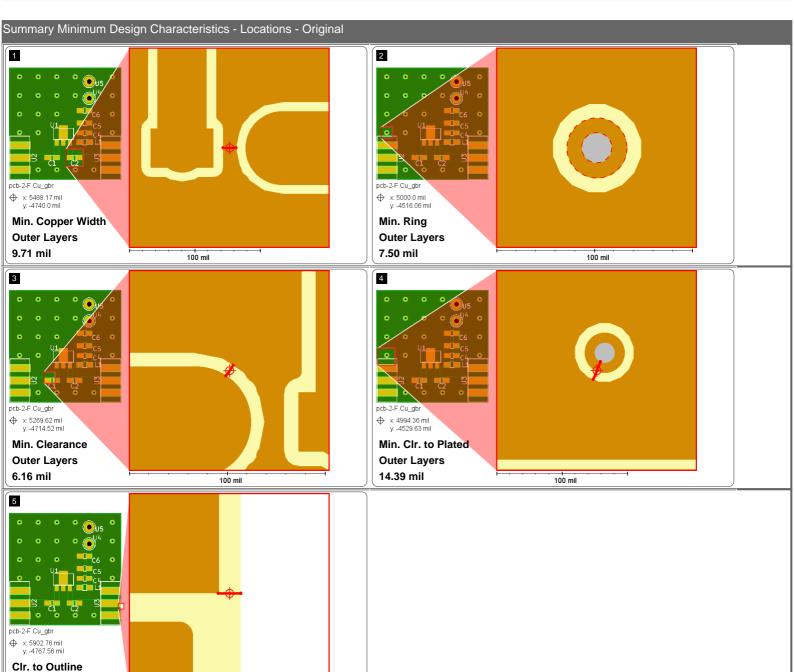
Integr8tor v2017.05-170524

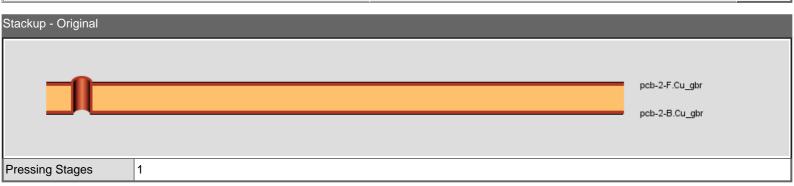
The information on this document is not only based on files in a clearly defined format, but also on freely structured files and inference rules. Ucamco strives to make it as accurate as possible, but it cannot guarantee the result in all situations. This information is used at the sole risk of its user.

QED Report

Integr8tor

Name	n1wpk8fe.zip	ld.	15456 - QED OK
Report Generated on	Nov 3, 2017 10:00:54 PM	Customer	InstantDFM
Board Id			





The information on this document is not only based on files in a clearly defined format, but also on freely structured files and inference rules. Ucamco strives to make it as accurate as possible, but it cannot guarantee the result in all situations. This information is used at the sole risk of its user.

Outer Layers 5.51 mil

50 mil

QED Report

Integr8tor

Name	n1wpk8fe.zip	ld.	15456 - QED OK
Report Generated on	Nov 3, 2017 10:00:54 PM	Customer	InstantDFM
Board Id			

Copper Layers - Original											
File	Pos.	Min. Line Width	Min. Copper Width	Min. Ring	Min. Clr. to Copper	Min. Same Net spacing	Min. Clr. to Plated Hole	Min. Clr. to NPTH	Min. Clr. to Outline	Copper Area	
		mil	mil	mil	mil	mil	mil	mil	mil	inch ²	%
pcb-2-F.Cu_gbr	1	>16.00	9.71	7.50	6.16	>20.00	14.39		5.51	0.9003	90
pcb-2-B.Cu_gbr	2	>16.00	>16.00	7.50	6.89	>20.00	14.39		11.89	0.9344	93

onii 100is - Original																
File	Tool Nr.	Span	Type	Method	FilledVi a	Countere d	Dia.	Tol. Min	Tol. Plus	Holes (in PCB)	Moves (in PCB)	Doubl e Hits (in File)	Predril I Hits (in File)	Min. Ring on Outer	Min. Ring on Inner	Min. Pad Size
							mil	mil	mil					mil	mil	mil
pcb-2_drl	1	1-2	PTH	unknown	unknow n	unknown	15.00	0.00	0.00	21	0	0	0	7.50		30.00
pcb-2_drl	2	1-2	PTH	unknown	unknow n	unknown	39.37	0.00	0.00	2	0	0	0	19.68		78.73

Sequence	Sequences - Original											
Span	Туре	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Ring on Outer NPTH	Min. Ring on Inner NPTH	Min. Clr. Hole to Copper	Min. Clr. Hole to Outline	Min. Clr. Slot to Outline
			mil	mil		mil	mil	mil	mil	mil	mil	mil
1-2	PTH	2	15.00	39.37	23	7.50				14.39	71.24	disabled
All	Plated	2	15.00	39.37	23	7.50				14.39	71.24	disabled
All	All	2	15.00	39.37	23	7.50				14.39	71.24	disabled

Rout Tools - Original						
File	Tool Nr.	Туре	Tool Dia.	End Dia.	Draw Length	Nibble Count
			mil	mil	mil	

Routed Holes - Original						
File	Hole Nr.	Instances	X Size	Y Size	Draw Length	Nibble Count
			mil	mil	mil	

Solder Mask - Original									
Side	Min. Ring on Cu Defined Pads	Min. Ring on SM Defined Pads	Min. Clr. Mask to Mask	Min. Web	Min. Clr. Mask to Copper	Fully Covered Via Holes	Partly Covered Via Holes		
	mil	mil	mil	mil	mil				
Тор	5.51		>10.00	>10.00	>10.00	No	No		
Bottom	7.87		>10.00	>10.00	>10.00	No	No		

Integr8tor v2017.05-170524



Integr8tor

Name	n1wpk8fe.zip	ld.	15456 - QED OK
Report Generated on	Nov 3, 2017 10:00:54 PM	Customer	InstantDFM
Board Id			

Files - Original								
Initial	Renamed	Format	Function	Position	Color			
pcb-2-F.SilkS.gbr	pcb-2-F.SilkS_gbr	ger274x	silk	top	white			
pcb-2-F.Mask.gbr	pcb-2-F.Mask_gbr	ger274x	mask	top	green			
pcb-2-F.Cu.gbr	pcb-2-F.Cu_gbr	ger274x	outer	1				
pcb-2-B.Cu.gbr	pcb-2-B.Cu_gbr	ger274x	outer	2				
pcb-2-B.Mask.gbr	pcb-2-B.Mask_gbr	ger274x	mask	bottom	green			
pcb-2.drl	pcb-2_drl	excellon2	plated	1-2				
pcb-2-B.SilkS.gbr	pcb-2-B.SilkS_gbr	ger274x	empty	none				
pcb-2-Edge.Cuts.gbr	pcb-2-Edge.Cuts_gbr	ger274x	cad_outline	none				

Comments - Original



Integr8tor v2017.05-170524 The information on this document is not only based on files in a clearly defined format, but also on freely structured files and inference rules. Ucamco strives to make it as accurate as possible, but it cannot guarantee the result in all situations. This information is used at the sole risk of its user.