Chenye Wang

396 Station Road, Liushi Town, Yueqing City, Zhejiang Province, 325604 * +86 13868757785 * wangchenye@mail.ustc.edu.cn

EDUCATION

University of Science and Technology of China, Hefei, China

Bachelor of Science in Computer Science and Technology, Sep. 2022- Jun. 2026

- GPA: 3.26/4.3
- Core Courses: Operating System, Compiler, and Algorithm
- **Excellent Student Scholarship - Bronze** (Top 30%) Dec. 2023

PROJECT EXPERIENCE

Optimization of Large Language Model Serving Framework for Accelerating Model Inference

Research Assistant, Sep. 2024 - Jan. 2025

- https://github.com/w666cy/my-presentation
- Reviewed portions of the open-source code for vLLM, including block memory allocation, and request scheduling.
- Gained familiarity with the entire call stack of vLLM and GPU-related architecture.
- Learn about leading edge technologies for efficient LLM serving, such as prefill-decode separation, dynamic memory management, and piggybacking.
- Help senior members solve the problem that the batching case did not match the resource configuration when using vLLM.
- This is my first piece of research aimed at facilitating my entry into the field of MLSys.

Design and Implementation of a Modern Compiler using LLVM and LoongArch backend

- https://github.com/w666cy/2024ustc-jianmu-compiler
- Designed and implemented a modern compiler using the LLVM framework, optimized for the LoongArch architecture.
- Proficient in C++ programming language, with a deep understanding of the LLVM compilation framework and experience in developing for the LoongArch architecture.

OPERATING SYSTEM RELATED PRACTICES

Malloc Memory Alloctor and Simple File System

- https://github.com/w666cy/USTC-OS-2024
- Implemented a FUSE-based FAT16 file system supporting file and directory read, create, delete, and write operations.
- Utilized C language for system programming and mastered the principles of memory allocation and file management.

RISC-V Operating System

- https://github.com/w666cy/riscv-operating-system
- Developed and implemented an operating system kernel based on the RISC-V architecture, step by step.

ADDITIONAL SKILLS

- **Languages**: Chinese and English
- **Programming Languages**: C/C++, Python, and Verilog
- **Relevant Software**: VSCode, Typora, and WSL
- **Interests**: Running, Basketball, Music, and Reading