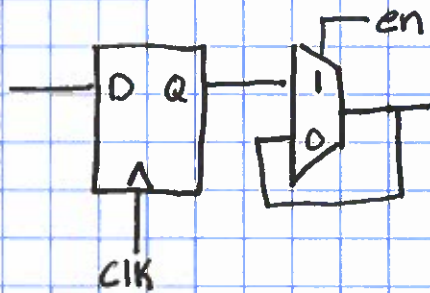
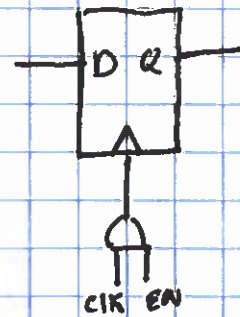


Deliverable 1

D-Flip-Flop



Gated Clock



Deliverable 6

The behavioral decoder works by bit-shifting the enable pin to the left by a value equal to the address values.

This reflects the characteristics of a decoder because the enable pin gets zero-padded to match the 32-bit length of the output. ~~The placement of the address bit~~