# OpenMIPS

## PC模块

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **序号** | **接口名** | **宽度(bit)** | **输入/输出** | **作用** |
| 1 | rst | 1 | I | 复位信号 |
| 2 | clk | 1 | I | 时钟信号 |
| 3 | pc | 6 | O | 要读取的指令地址 |
| 4 | ce | 1 | O | 指令存储器使能信号 |

## 指令存储器ROM设计与实现

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **序号** | **接口名** | **宽度(bit)** | **输入/输出** | **作用** |
| 1 | ce | 1 | I | 使能信号 |
| 2 | addr | 6 | I | 要读取的指令地址 |
| 3 | inst | 32 | O | 读出的指令 |

## 1.3连接PC模块和指令寄存器ROM的inst\_fetch模块

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **序号** | **接口名** | **宽度(bit)** | **输入/输出** | **作用** |
| 1 | rst | 1 | I | 复位信号 |
| 2 | clk | 1 | I | 时钟信号 |
| 3 | inst | 32 | O | 读出的指令 |