# Simulation Record

## des\_key\_check && des\_key

the simulation result of des\_key\_check module and des\_key module.

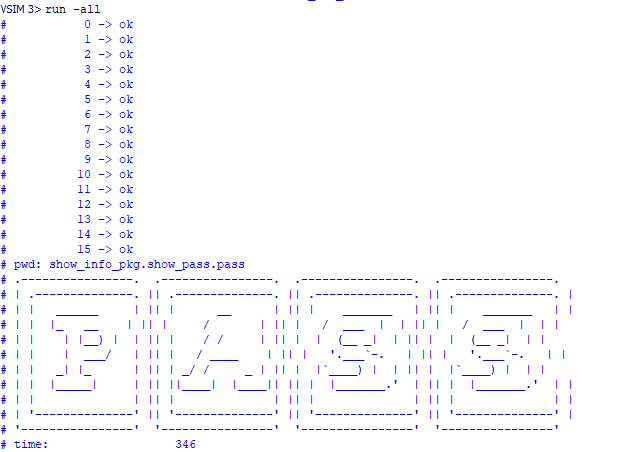


Figure 1 des\_key module’s simulation result

## des\_ip

the simulation result of des\_ip module. (initial permutation)

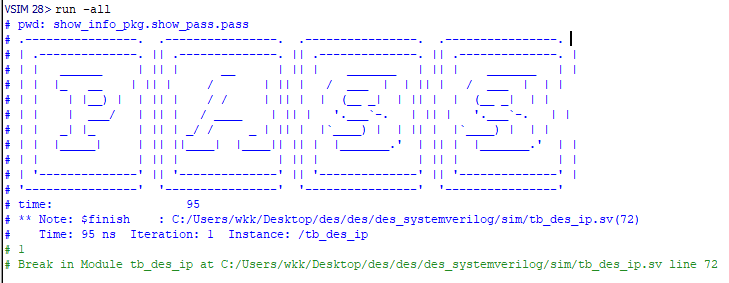


Figure 2 des\_ip module’s simulation result

## des\_ext

the simulation result of des\_ext module.

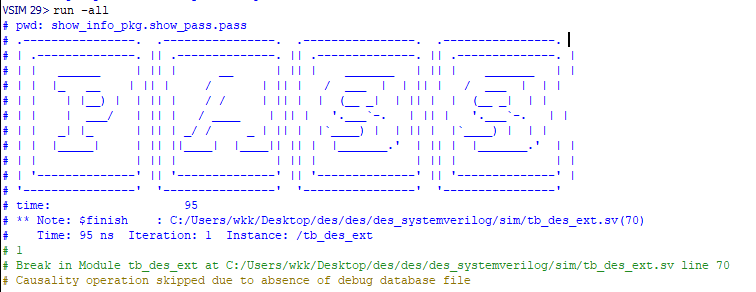


Figure 3 des\_ext module’s simulation result

## des\_scomp

the simulation result of des\_scomp module.

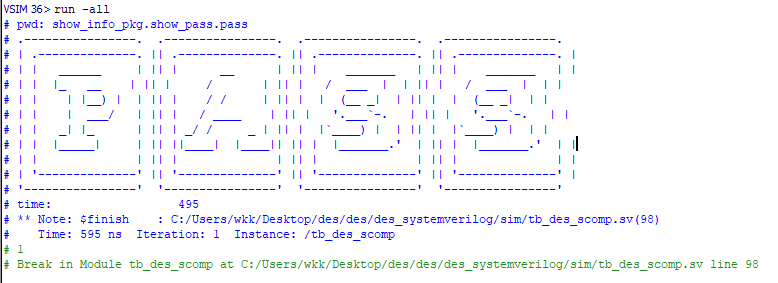


Figure 4 des\_scomp module’s simulation result

## des

the simulation result of des module.

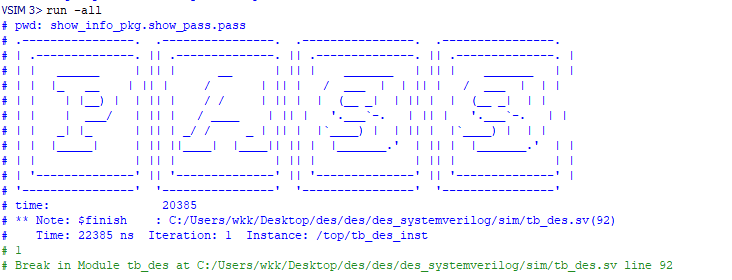


Figure 5 des module’s simulation result