表 1: Effectiveness of Post-Refinement

Circuit	SDP w/o. Refinement				SDP w. Refinement				with fast color trail			
	st#	cn#	cost	CPU(s)	st#	cn#	cost	CPU(s)	st#	cn#	cost	CPU(s)
C432	4	0	0.4	0.1	4	0	0.4	0.1	4	0	NA	NA
C499	0	0	0	0.1	0	0	0	0.1	0	0	NA	NA
C880	7	0	0.7	0.1	7	0	0.7	0.1	9	0	NA	NA
C1355	3	0	0.3	0.1	3	0	0.3	0.1	3	0	NA	NA
C1908	1	0	0.1	0.1	1	0	0.1	0.1	1	0	NA	NA
C2670	6	1	1.6	0.2	6	0	0.6	0.2	6	0	NA	NA
C3540	9	2	2.9	0.3	9	1	1.9	0.4	9	1	NA	NA
C5315	9	0	0.9	0.3	9	0	0.9	0.4	9	0	NA	NA
C6288	213	5	26.3	2.1	213	1	22.3	1.7	230	1	NA	NA
C7552	26	2	4.6	0.6	26	0	2.6	0.6	36	1	NA	NA
S1488	3	0	0.3	0.1	3	0	0.3	0.1	2	0	NA	NA
S38417	59	21	26.9	1.5	57	20	25.7	1.8	86	28	NA	NA