Microcontroller

U1
CH552G

Pin	Signal		Pin	Signal

TDI — 1 P3.2 · V33 16 — +3V3
TDO — 2 P1.4 VCC 15
SWD — 3 P1.5 GND 14
SWK — 4 P1.6 UDM 13 — DM
RST — 5 P1.7 UDP 12 — DP
 — 6 RST P3.4 11 — TRST
TXD — 7 P3.1 P3.3 10
RXD — 8 P3.0 P1.1 9 — LED

+3V3 — C1 100n — GND
+3V3 — C2 10u — GND
+3V3 — SW1 BOOT — R1 10k — DP

USB Connector

+5V
F1 1A

DM
DP — USB1 USB-Plug
+5V D- D+ GND

GND

Status LEDs

+3V3

LED — R2 1k — LED1 PRG
R3 1k — LED2 PWR

GND

Voltage Regulator

U2
AP2112K-3.3TRG1

+5V — 1 VIN VOUT 5 — +3V3
2 GND
3 EN NC 4

C3 1u
C4 1u

GND GND GND

Connector

H1
HDR

+5V +3V3

RXD — 100R R11 — 2 1 — R9 100R — TRST
TXD — R10 — 4 3 — R8 — RST
6 5 — R7 — SWK
8 7 — R6 — SWD
10 9 — R5 — TDO
12 11 — R4 — TDI

GND

TITLE: CH552G DAPLink SWD+JTAG+VCP   REV: 1.0

嘉立创EDA   Company: wagiminator   Sheet: 1/1

Date: 2023-05-05   Drawn By: Stefan Wagner