# Tsao-Lun Chen

### CONTACT INFORMATION

**TEL**: +886-912-711-870 **Mail**: a870128rlen@gmail.com

Github: https://github.com/wahahahaya

#### EDUCATION

### Yuan Ze University

Taoyuan, Taiwan

Sep 2016 - Jun 2020

Bachelor of Electrical Engineering

• GPA: 3.82

- Conference: Automatic Reference Current Architecture in Computing in Memory by MRAM
- Conference: Based on deep learning analyze brainwave signals of hand movements
- Award: IEEE ECICE best conference paper award

### National Taiwan University of Science and Technology

Taipei, Taiwan

Sep 2020 - present

Master of Electrical Engineering
• Advisor: Shun-Feng Su

## Work Experience

#### Student Inter

May 2019 – Oct 2019

Hsinchu, Taiwan

 $Industrial\ Technology\ Research\ Institute$ 

- Digital IC design
- STT-MRAM/Computing in Memory/Sense Amplifier

#### Publish

### Conference

- Chen, Tsao-Lun, and Wei-Tang Tseng. "Automatic Reference Current Architecture in Computing in Memory by MRAM." 2019 IEEE Eurasia Conference on IOT, Communication and Engineering (ECICE). IEEE, 2019.
- Chen, Tsao-Lun, and Chien-Cheng Lee. "Based on deep learning analyze brainwave signals of hand movements." 2019 Mobile Computing Workshop 2019.

### PROJECTS

### **Zero-shot Learning** | *Python*

Sep 2020 – Present

• end-to-end zero-shot learning model

#### **OPG Decomposition** | Python

Oct 2021 - Present

#### Image Noise Distribute | Python

Sep 2020 – May 2021

• Use GAN to generate the clear image by inputting the mixed noise(AWGN, SPIN, RVIN).

### TECHNICAL SKILLS

**Programming Languages**: Python, C/C++, R, Hspice

Domain Expertise: Computer Vision, Deep Learning, Zero-shot Learning, VLSI

#### AWARD

• Best conference paper in IEEE ECICE: Automatic Reference Current Architecture in Computing in Memory by MRAM

### **Podcast Producer**

- Writing interviews
- Inviting guests
- Recording
- Editing

### Teaching Assistant

- Introduction to Intelligent Control, NTUST, Fall 2021
- Decision Support and Recommender Systems, NTUST, Fall 2021
- $\bullet\,$  Programming Language, YZU, Spring and Fall 2020, Fall 2019

### Research Assistant

• Logic Circuit Lab., YZU, Fall 2019