|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Instruction class | Opcode | RegDst | AluSrc | MemtoReg | RegWrite | MemRead | MemWrite | Branch | Jump | ALUop |
| R format | 000000 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | **0** | 1111 |
| Load Word | 100011 | **0** | **1** | **1** | **1** | 1 | 0 | 0 | 0 | 0010 |
| Store Word | 101011 | X | 1 | X | 0 | 0 | 1 | 0 | 0 | 0010 |
| BEQ | 000100 | X | 0 | X | 0 | 0 | 0 | 1 | 0 | 0110 |
| Jump | 000010 | X | X | X | 0 | 0 | 0 | X | 1 | xx |
| Addi | 001000 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0010 |
| ADDIU | 001001 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1000 |
| ANDI | 001100 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0000 |
| LUI | 001111 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1110 |
| ORI | 001101 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0001 |
| SLTI | 001010 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0111 |
| SLTIU | 001011 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1011 |
| XORI | 001110 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1010 |