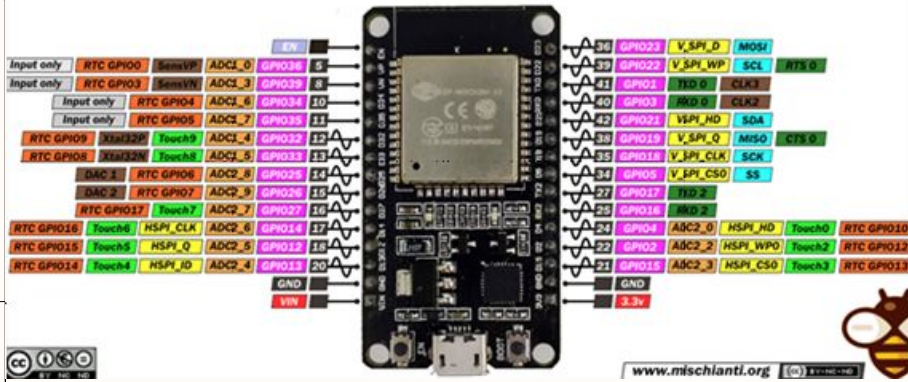
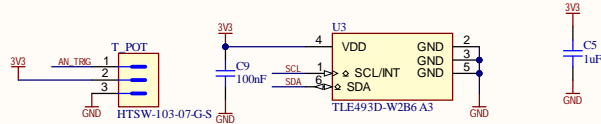


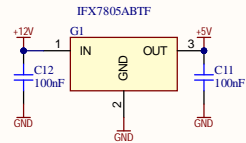
# ESP32 DEV KIT V1 PINOUT



TRIGGERS - 2 options: magnetic (default SW) or a potentiometer

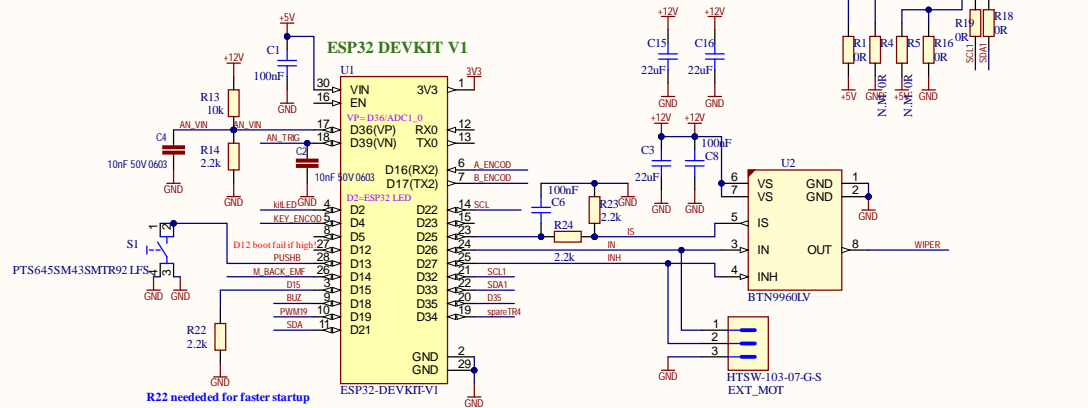
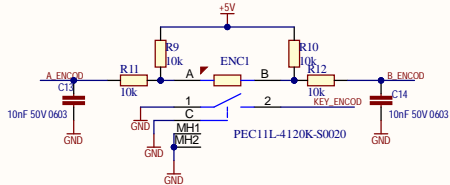


5V regulator for ESP32

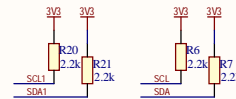


RevHistory 2.1 to 3.0  
Added filter to BTN9960 IS pin  
Divided I2C for OLED and Mag sensor

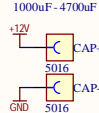
ROTARY ENCODER + PUSHBUTTON



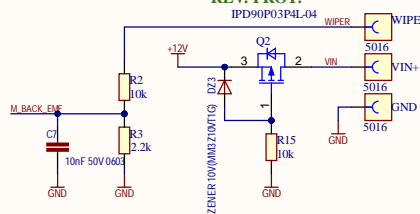
I2C FREQ. BOOST RESISTOR



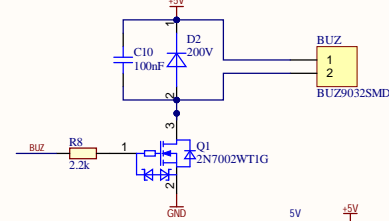
TANK CAP



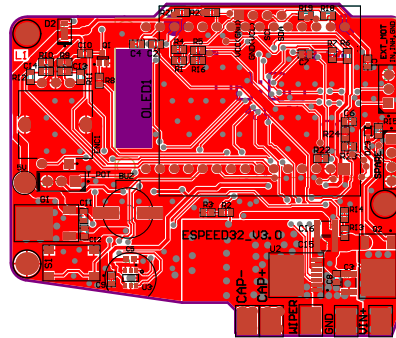
REV. PROT. MAIN CONNECTOR



BUZZER



APPROVALS	DATE	PROJECT	PROJECT REVISION	DOCUMENT REVISION	DESIGN ITEM
ENG: -					
DSN: -					
CHK: -					
REFERENCE DOCUMENTS		TITLE			
BOM:		ESPEED 32			
ASSY DWG:	SIZE	CAGE CODE	DWG NO.	REV	
FAB DWG:	A3			3.0	
PCB DWG:	SCALE:	FILE NAME	ESPEED32_V3_0.SchDoc	SHEET	OF



0,0 ORIGIN +

COMPANY		BOARD		NOTES	
Maker Fabio		MF_SLOT_ESC_U1.0		TOL. CONTOUR CUT +0,00/-0,20 s.d.i.	
DATE		SCALE		TOL. HOLES POSITIONING +/-0,05 s.d.i.	
01-04-2024		1 : 1		TOL. FINISHEDE HOLES DIAMETER +0,1/-0,05 o.i.d.	
FEATURES		DRAWING			
2 LAYERS + SOLDER + SILK		ZOTTI M. HSE			
MATERIAL		EXT. BASIC COPPER		PCB THICKNESS	
FR4		35um		1,6mm	
DIMENSION		INN. BASIC COPPER		TOLLERANCE	
59,85x50,80mmq				SEE NOTES	
				THE ORIGIN OF DRILLING FILE IS REFERS ZERO CARD	
				THE MEASURE OF HOLES IS A HOLE FINISHED	